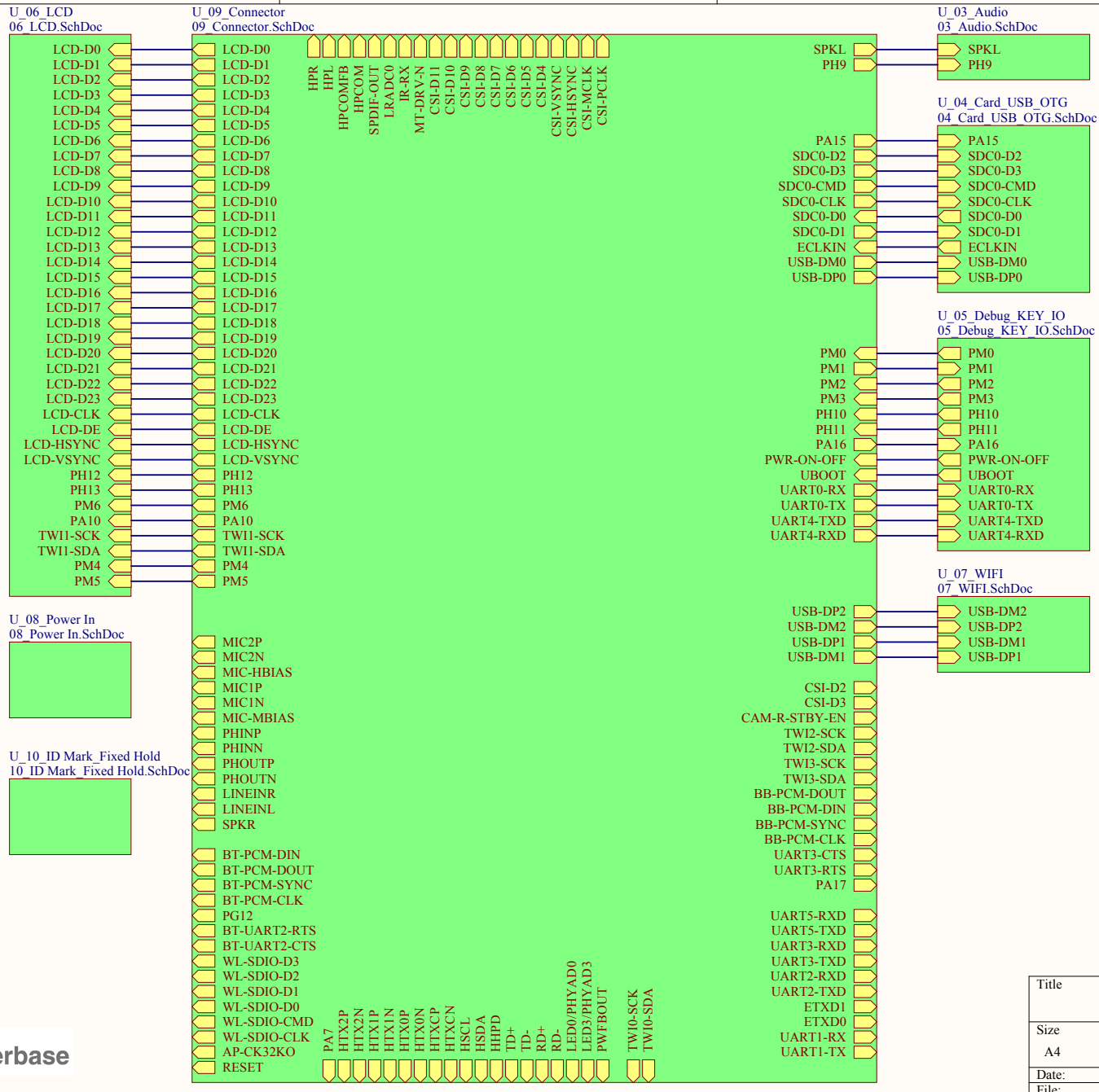
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06_LCD.SchDoc
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LCD-D1
LCD-D2
LCD-D3
LCD-D4
LCD-D5
LCD-D6
LCD-D7
LCD-D8
LCD-D9
LCD-D10
LCD-D11
LCD-D12
LCD-D13
LCD-D14
LCD-D15
LCD-D16
LCD-D17
LCD-D18
LCD-D19
LCD-D20
LCD-D21
LCD-D22
LCD-D23
LCD-CLK
LCD-DE
LCD-HSYNC
LCD-VSYNC
PH12
PH13
PM6
PA10
TWI1-SCK
TWI1-SDA
PM4
PM5
U_08_Power In
08_Power In.SchDoc
U_10_ID Mark_Fixed Hold
10_ID Mark_Fixed Hold.SchDoc
U_09_Connector
09_Connector.SchDoc
LCD-D0
LCD-D1
LCD-D2
LCD-D3
LCD-D4
LCD-D5
LCD-D6
LCD-D7
LCD-D8
LCD-D9
LCD-D10
LCD-D11
LCD-D12
LCD-D13
LCD-D14
LCD-D15
LCD-D16
LCD-D17
LCD-D18
LCD-D19
LCD-D20
LCD-D21
LCD-D22
LCD-D23
LCD-CLK
LCD-DE
LCD-HSYNC
LCD-VSYNC
PH12
PH13
PM6
PA10
TWI1-SCK
TWI1-SDA
PM4
PM5
HPR
HPL
HPCOMFB
HPCOM
SPDIF-OUT
LRADC0
IR-RX
MT-DRV-N
CSI-D11
CSI-D10
CSI-D9
CSI-D8
CSI-D7
CSI-D6
CSI-D5
CSI-D4
CSI-VSYNC
CSI-HSYNC
CSI-MCLK
CSI-PCLK
MIC2P
MIC2N
MIC-HBIAS
MIC1P
MIC1N
MIC-MBIAS
PHINP
PHINN
PHOUTP
PHOUTN
LINEINR
LINEINL
SPKR
BT-PCM-DIN
BT-PCM-DOUT
BT-PCM-SYNC
BT-PCM-CLK
PG12
BT-UART2-RTS
BT-UART2-CTS
WL-SDIO-D3
WL-SDIO-D2
WL-SDIO-D1
WL-SDIO-D0
WL-SDIO-CMD
WL-SDIO-CLK
AP-CK32KO
RESET
PA7
HTX2P
HTX2N
HTX1P
HTX1N
HTX0P
HTX0N
HTXCP
HTXCN
HSCL
HSDA
HHPD
TD+
TD-
RD+
RD-
LED0/PHYAD0
LED3/PHYAD3
PWFBOUT
TWI0-SCK
TWI0-SDA
SPKL
PH9
PA15
SDC0-D2
SDC0-D3
SDC0-CMD
SDC0-CLK
SDC0-D0
SDC0-D1
ECLKIN
USB-DM0
USB-DP0
PM0
PM1
PM2
PM3
PH10
PH11
PA16
PWR-ON-OFF
UBOOT
UART0-RX
UART0-TX
UART4-TXD
UART4-RXD
USB-DP2
USB-DM2
USB-DP1
USB-DM1
CSI-D2
CSI-D3
CAM-R-STBY-EN
TWI2-SCK
TWI2-SDA
TWI3-SCK
TWI3-SDA
BB-PCM-DOUT
BB-PCM-DIN
BB-PCM-SYNC
BB-PCM-CLK
UART3-CTS
UART3-RTS
PA17
UART5-RXD
UART5-TXD
UART3-RXD
UART3-TXD
UART2-RXD
UART2-TXD
ETXD1
ETXD0
UART1-RX
UART1-TX
U_03_Audio
03_Audio.SchDoc
SPKL
PH9
U_04_Card_USB_OTG
04_Card_USB_OTG.SchDoc
PA15
SDC0-D2
SDC0-D3
SDC0-CMD
SDC0-CLK
SDC0-D0
SDC0-D1
ECLKIN
USB-DM0
USB-DP0
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PM0
PM1
PM2
PM3
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PH11
PA16
PWR-ON-OFF
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UART0-RX
UART0-TX
UART4-TXD
UART4-RXD
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USB-DM2
USB-DP2
USB-DM1
USB-DP1
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A4
Number
Revision
Date:
2020/3/4
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File:
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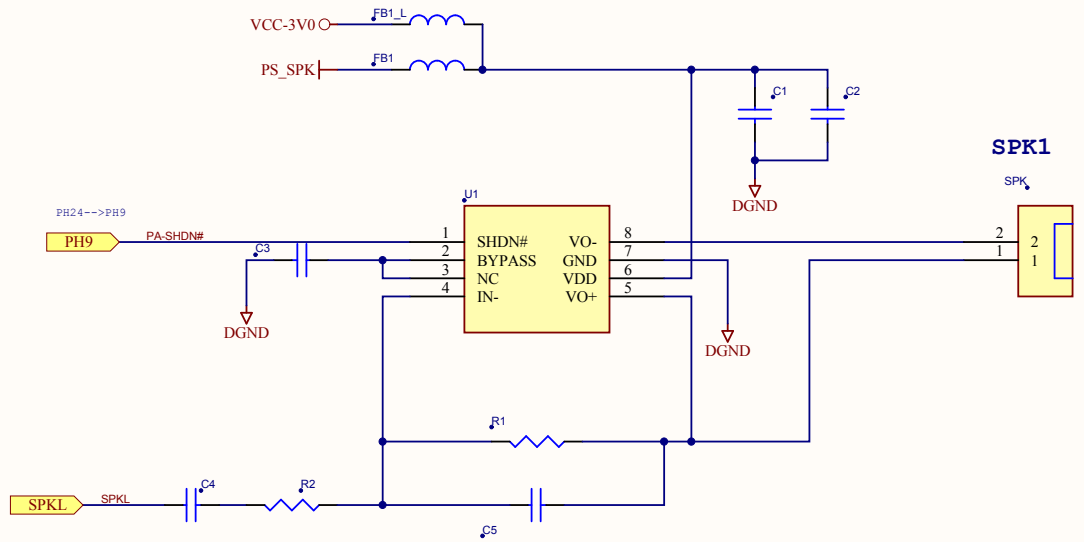

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Title	com pad	
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Date: 5	Sheet 3 6 of 10	

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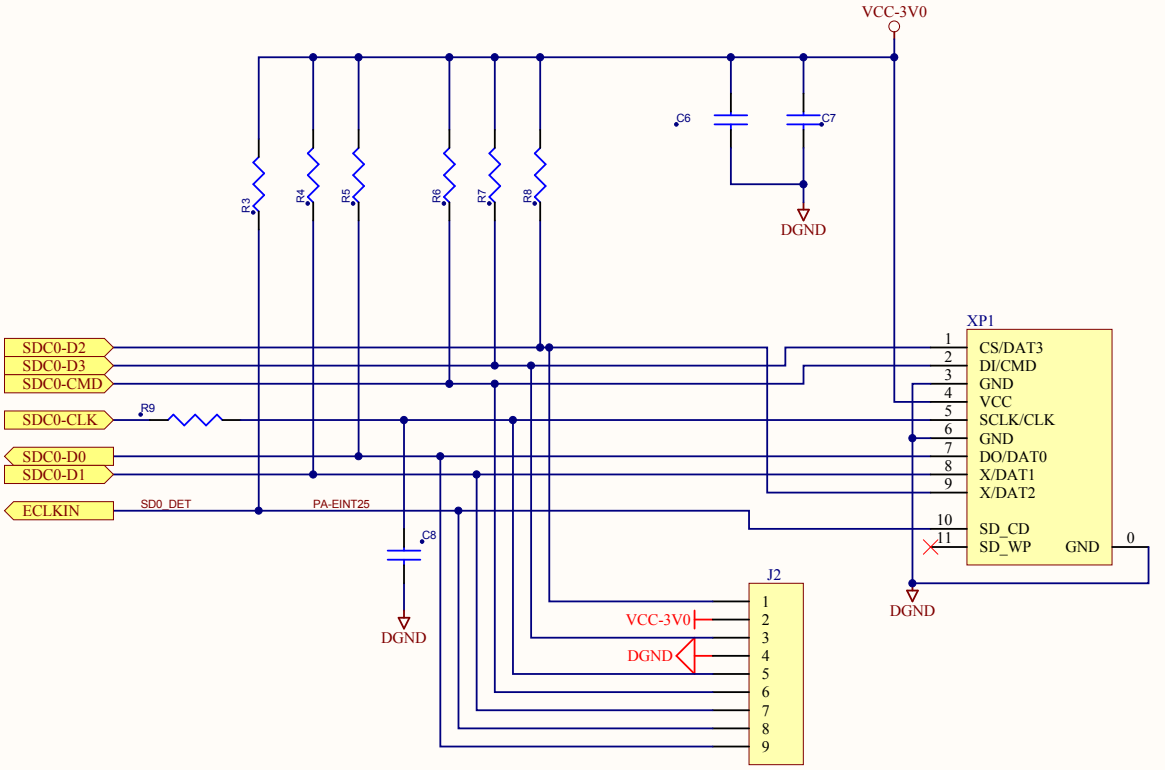

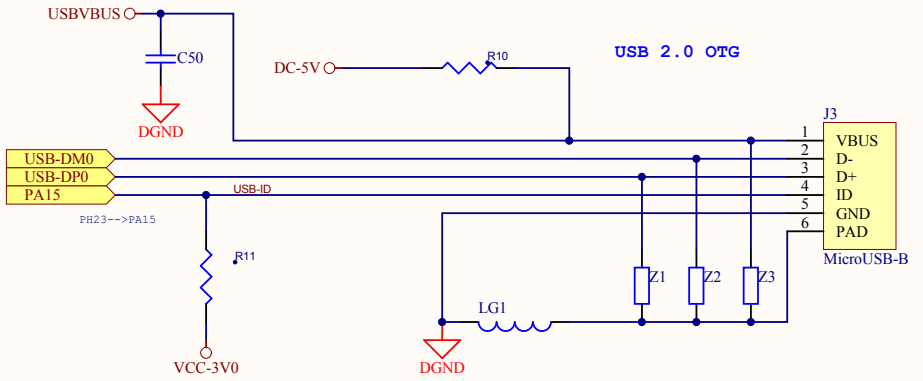

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Size	Document Number	Rev
A	05 Card/Sensor/USB_OTG	V1.0

Date: 5 Sheet 4 6 of 10

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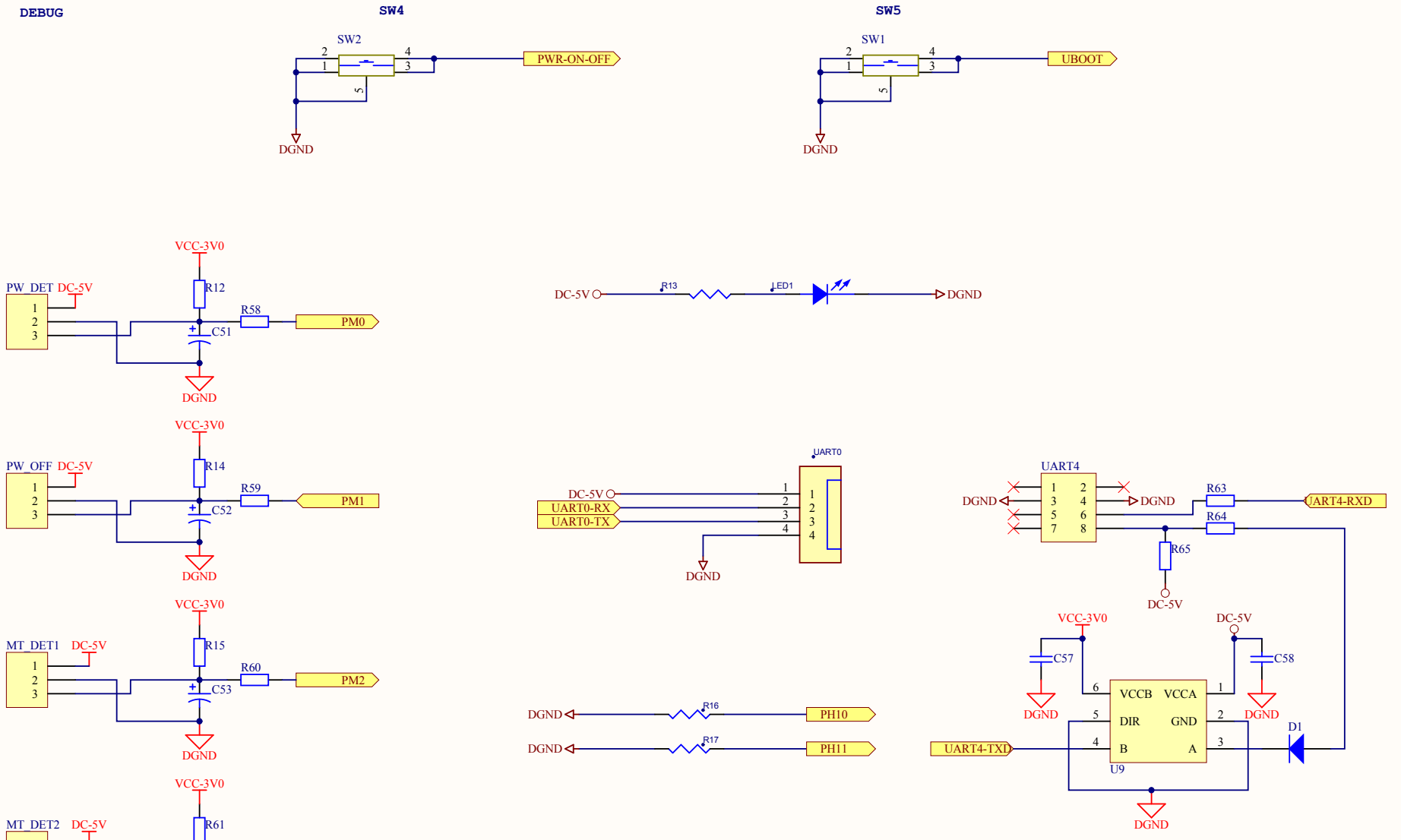
DEBUG
SW4
SW2
PWR-ON-OFF
DGND
SW5
SW1
UBOOT
DGND
VCC-3V0
PW_DET DC-5V
R12
R58
C51
PM0
DGND
VCC-3V0
PW_OFF DC-5V
R14
R59
C52
PM1
DGND
VCC-3V0
MT_DET1 DC-5V
R15
R60
C53
PM2
DGND
VCC-3V0
MT_DET2 DC-5V
R61
R62
C54
PM3
DGND
DC-5V
R13
LED1
DGND
UART0
DC-5V
UART0-RX
UART0-TX
DGND
DGND
R16
PH10
DGND
R17
PH11
UART4
DGND
DGND
R63
UART4-RXD
R64
R65
DC-5V
VCC-3V0
DC-5V
C57
C58
DGND
DGND
VCCB
VCCA
DIR
GND
B
A
U9
D1
UART4-TXD
DGND
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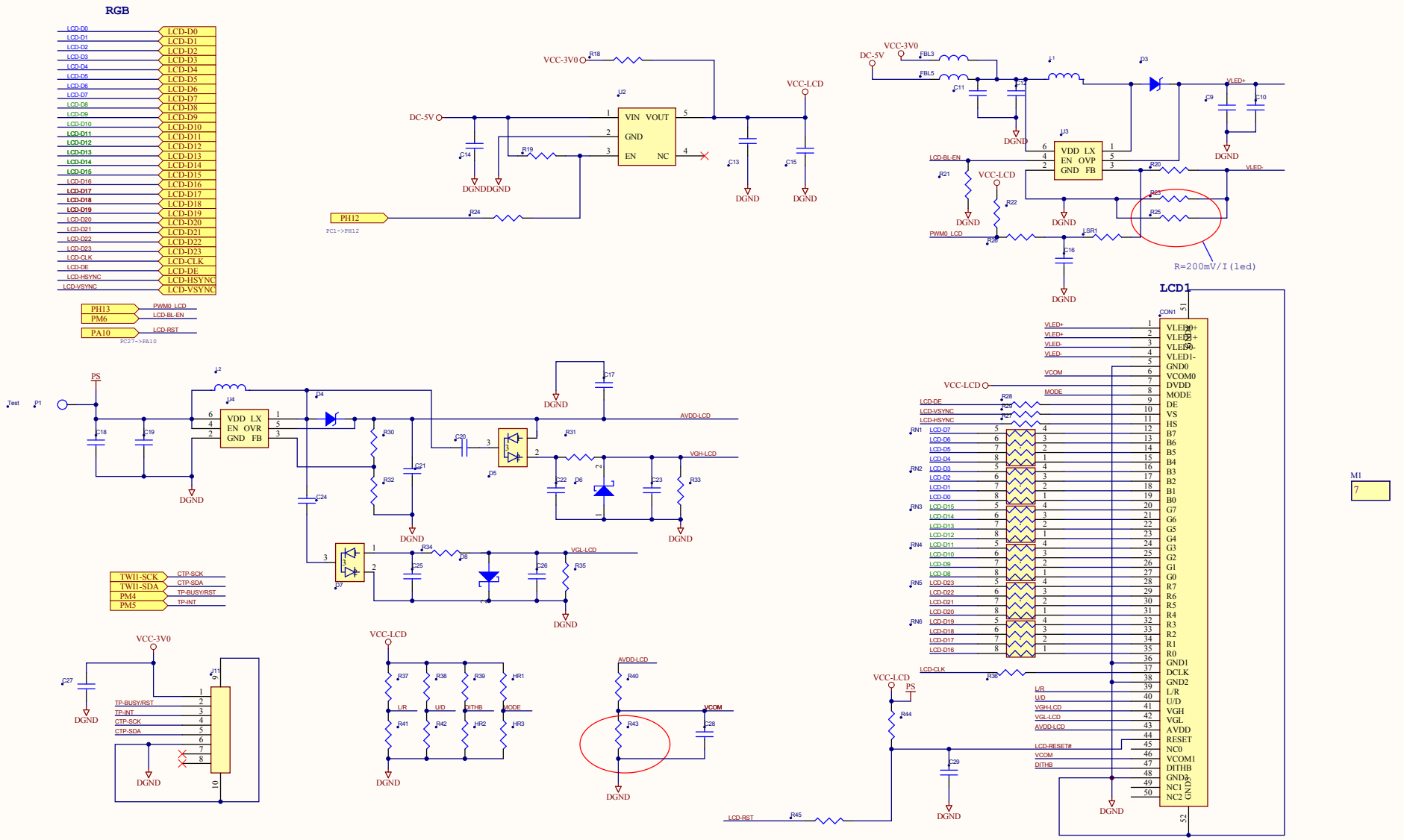
Size
A
Document Number
06 Debug
Rev
V1.0
Date:
Sheet
5
of
10



COMPRESSED IMAGE

COMPRESSED IMAGE

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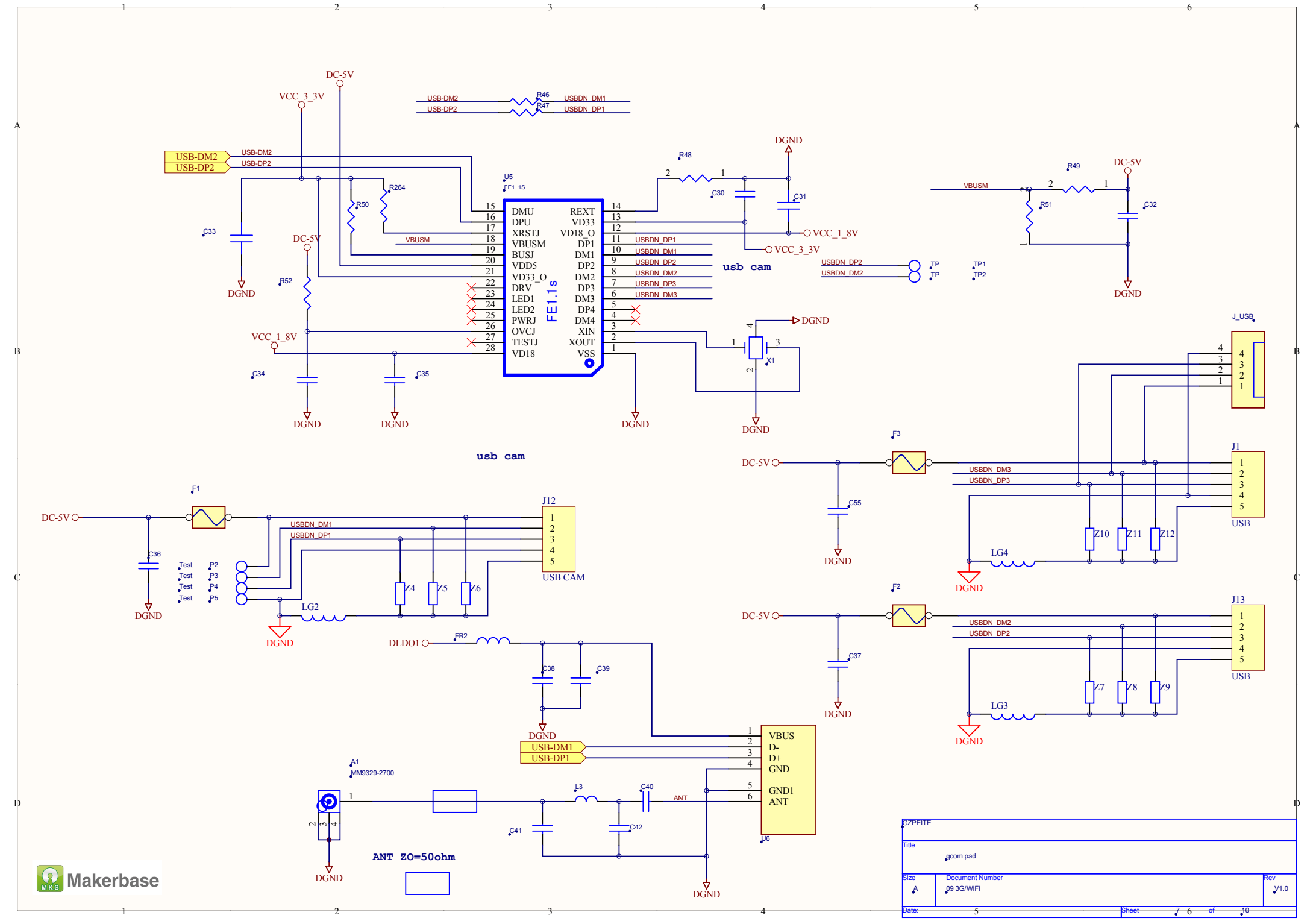
Date:	Sheet 6 of 10	


DC-5V
VCC_3_3V
USB-DM2
USB-DP2
USBDN_DM1
USBDN_DP1
R46
R47
R48
R49
R50
R51
R52
R264
U5
FE1_1S
FE1.1s
DMU
DPU
XRSTJ
VBUSM
BUSJ
VDD5
VD33_O
DRV
LED1
LED2
PWRJ
OVCJ
TESTJ
VD18
REXT
VD33
VD18_O
DP1
DM1
DP2
DM2
DP3
DM3
DP4
DM4
XIN
XOUT
VSS
USBDN_DP1
USBDN_DM1
USBDN_DP2
USBDN_DM2
USBDN_DP3
USBDN_DM3
VCC_1_8V
VCC_3_3V
C30
C31
C32
C33
C34
C35
X1
DGND
TP
TP1
TP2
usb cam
J_USB
J1
USB
F1
F2
F3
J12
USB CAM
J13
LG2
LG3
LG4
Z4
Z5
Z6
Z7
Z8
Z9
Z10
Z11
Z12
C36
C37
C38
C39
C40
C41
C42
C55
FB2
DLDO1
L3
ANT
A1
MM9329-2700
ANT ZO=50ohm
VBUS
D-
D+
GND
GND1
ANT
U6
Test
P2
P3
P4
P5
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Size
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09 3G/WiFi
Rev
V1.0
Date:
Sheet
7 6 of 10

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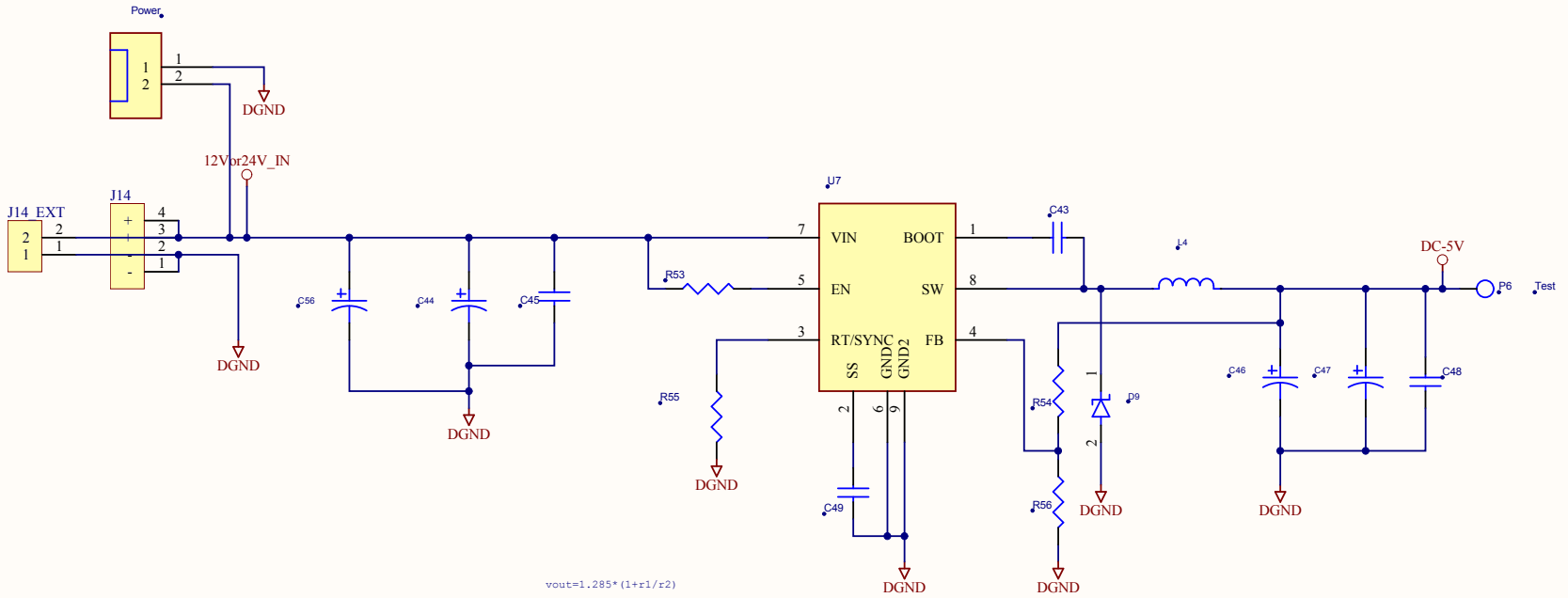

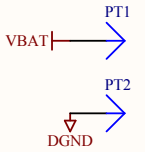

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Size: A	Document Number: 12 Power In	Rev: V1.0
Date: 5	Sheet 8 6 of 10	

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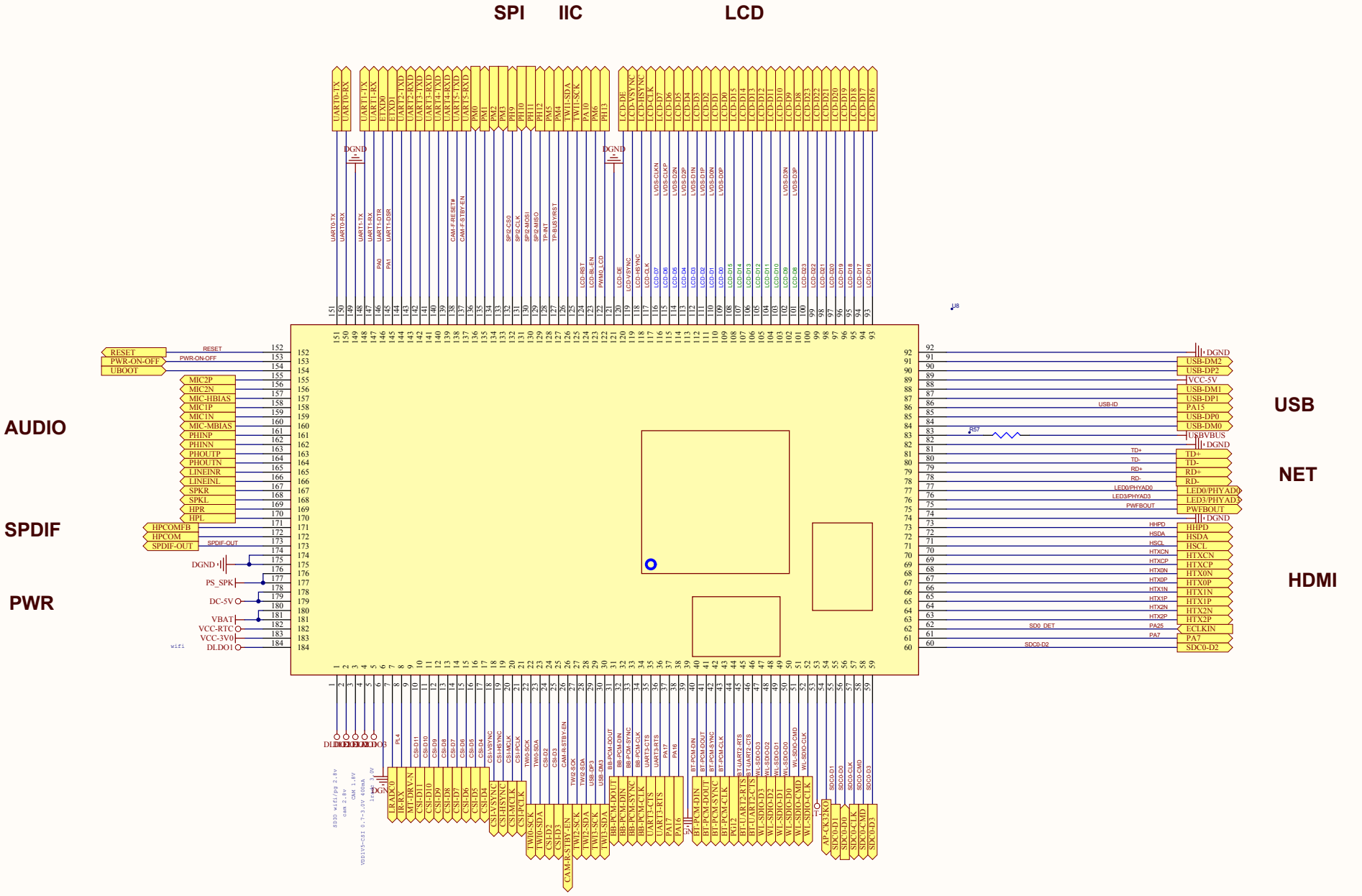

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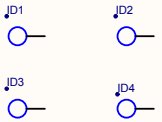
Rev	V1.0
Date:	Sheet 6 of 10



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Size: A	Document Number: 14 ID Mark/Fixed Hold	Rev: V1.0
Date: 5	Sheet 10 6 of 10	