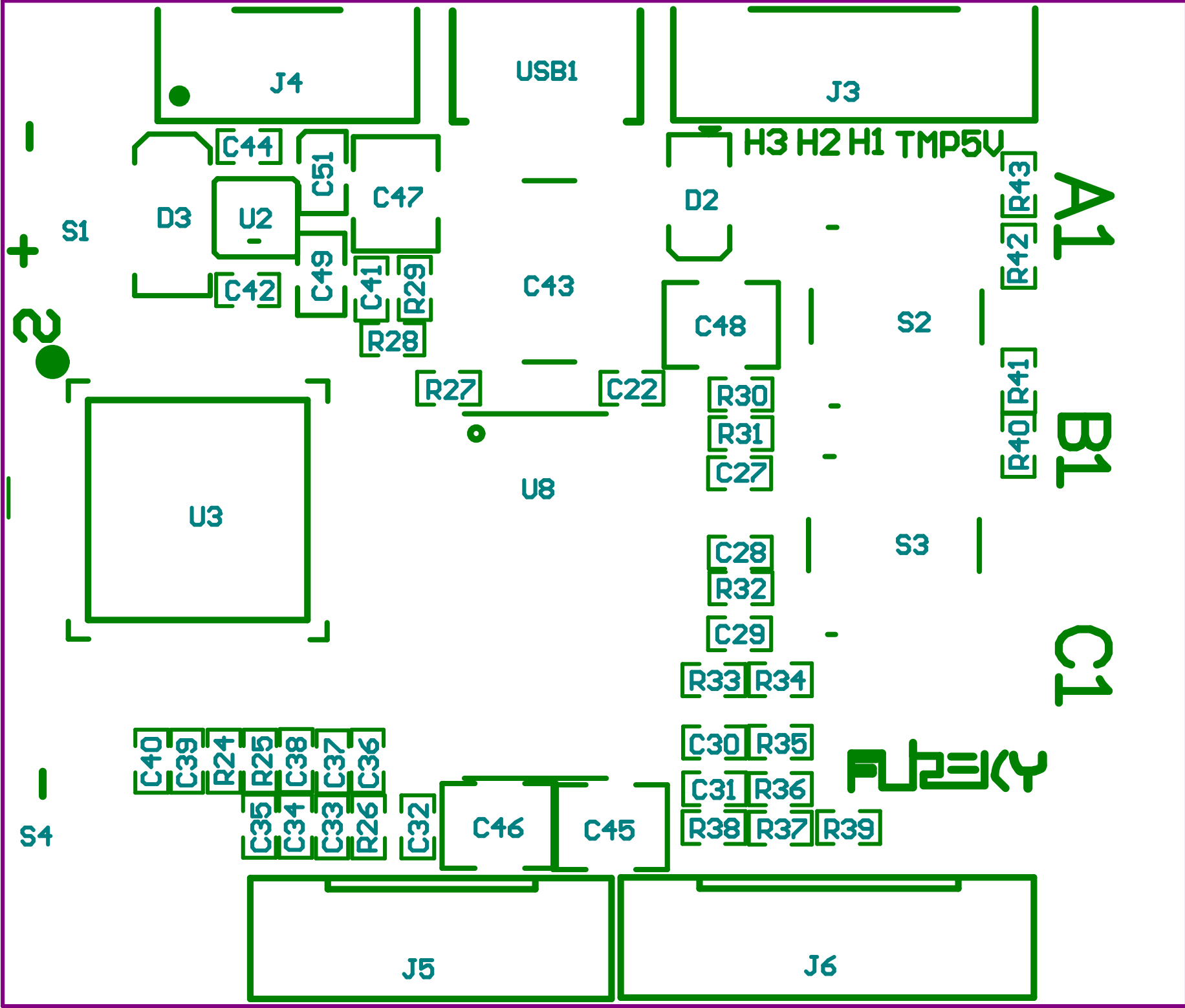


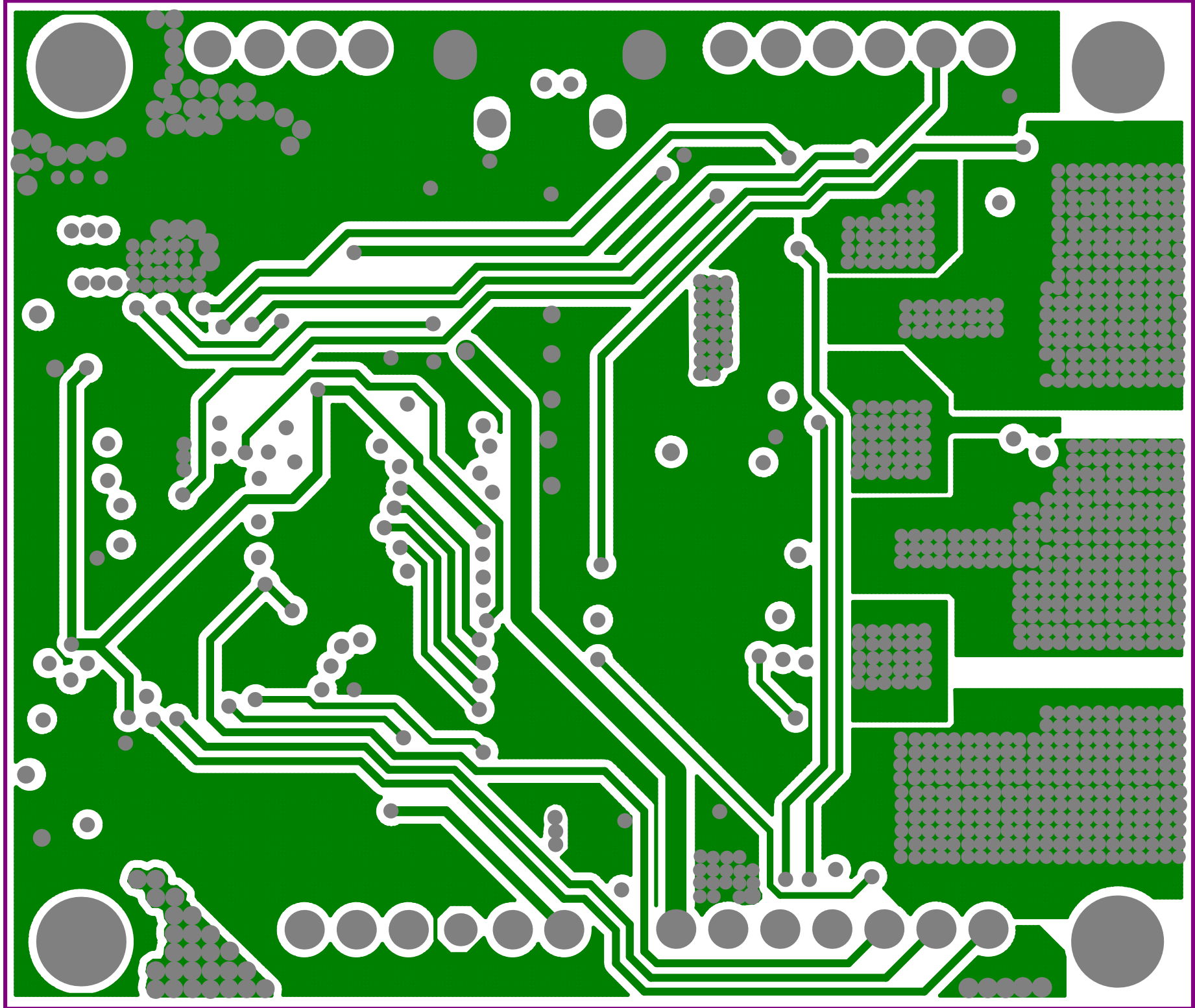
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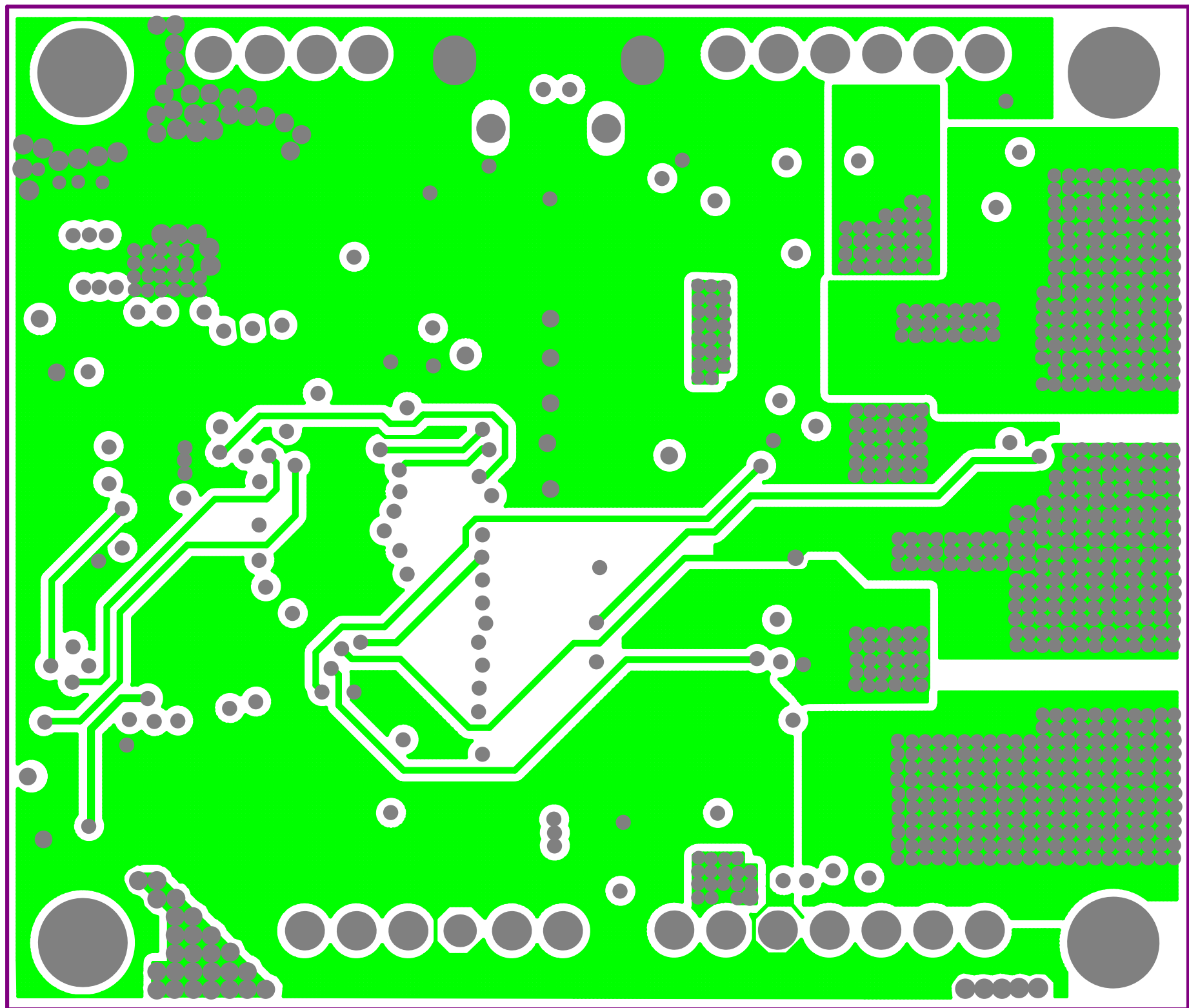
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C1

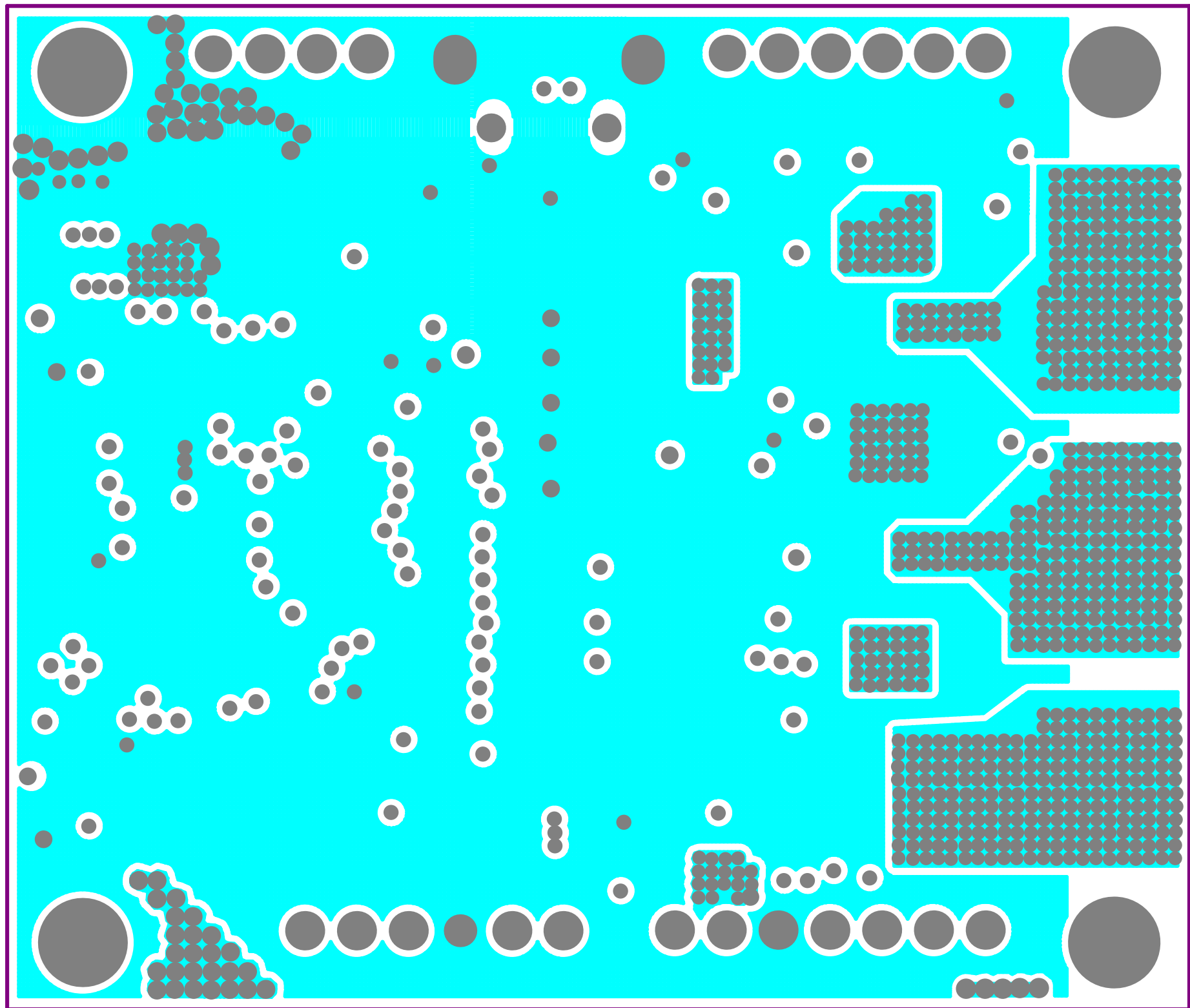
FLP3<Y



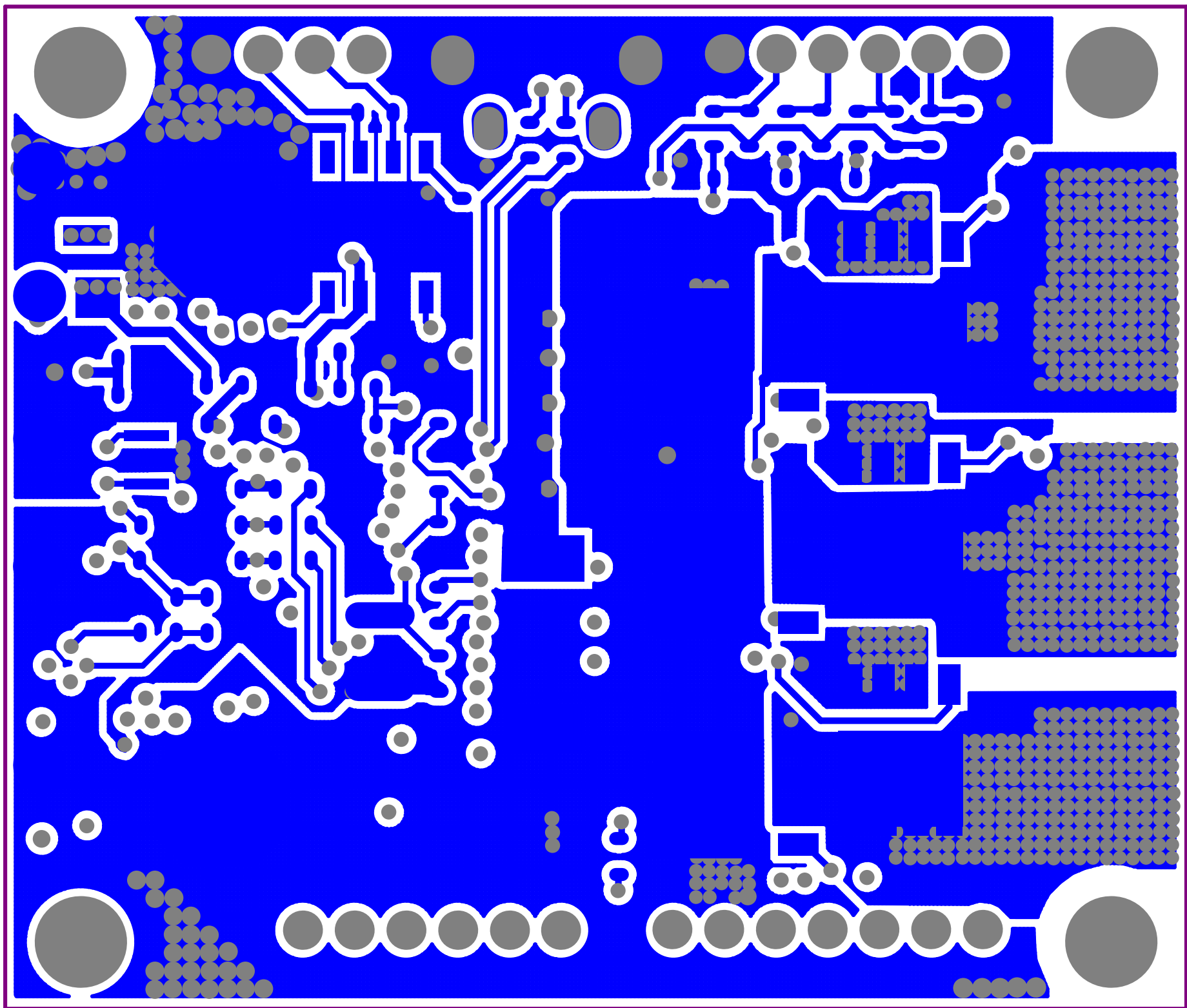




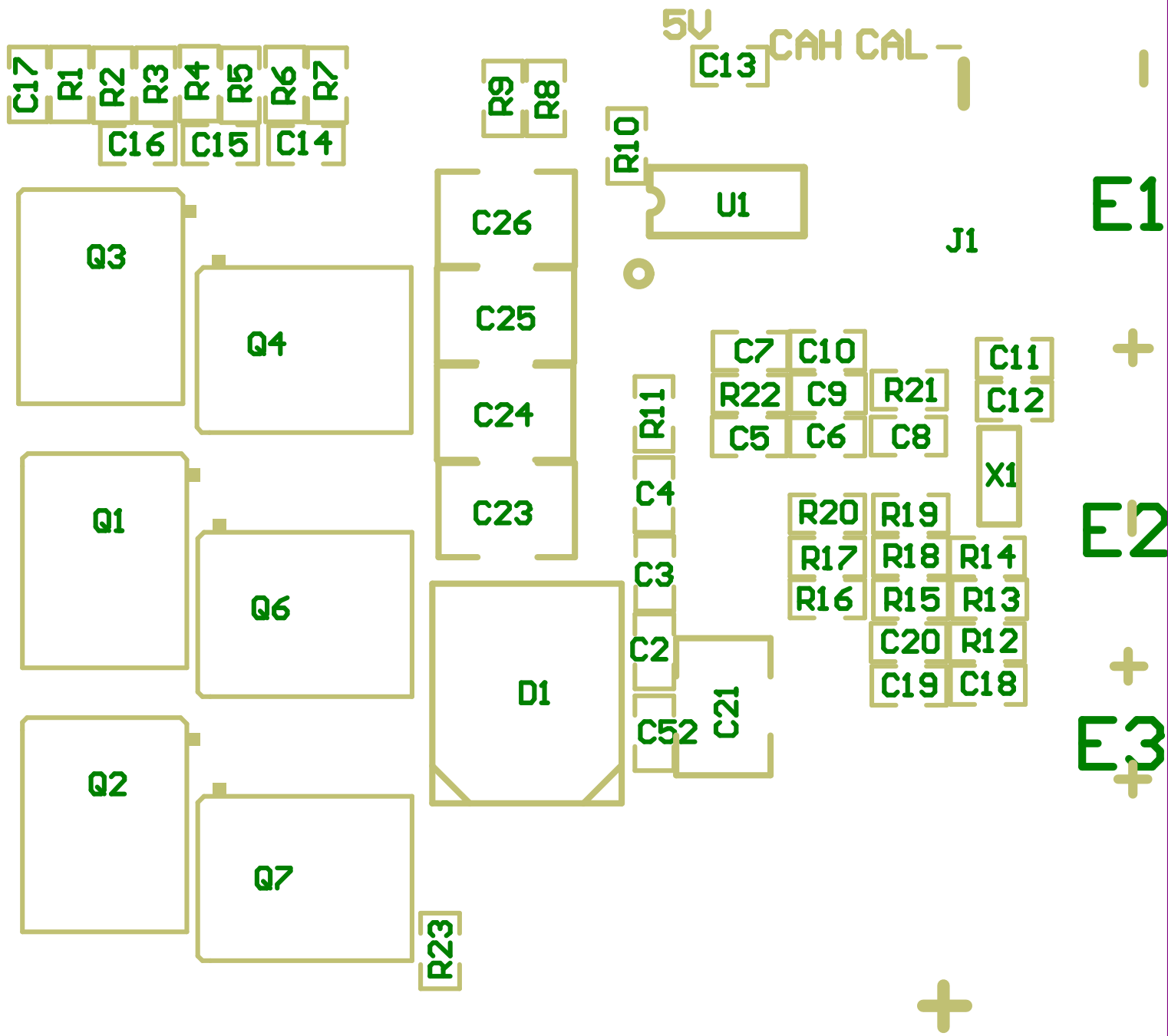








C1 B1 A1



ADC2 RX TX ADC - 3V3 5V 3V3 CLK - DIO RST