

	Raspberry Pi Zero (J8 Header)					
SP/OH	NAVE		2000		MAKE	GAO
	3.3 VDC Power	+	0	14	5.0 VDC Power	
8	SDAL (320)		00	-	5.6 VDC Power	
9	GPIO 9 SGL1 (EC)	10	00	0	Ground	
7	GPID 7 GPCLKD		00		GPIO 15 TIID (UART)	15
	Grewne		00	8	BPD 16 ReD (URRT)	16
0	GP10-0	п	00	н	FON_CLKIPWND	1
2	CNO 1	п	00	2	Ground	
3	6910.3	9	00	×	GPIO 4	4
	3.3 VDC Power	22	00	6	SPID 5	5
12	GPIO 12 MOSI (SPI)	23	00	8	Ground	
13	GPI0 13 MISO (SPI)	11	00	13	GPI0 6	6
14	GP10 14 SCLK (SPI)	n	00	¥	GPIO 10 (000 (SPI)	10
	Ground	19	00	12	GPI0 11 CE1 (SPI)	11
30	SDA0 (IZC ID EEPHON)	14	0 0	и	(OC ID EEPRON)	31
21	GPID 21 GPID.KI	я	00	8	Graund	
22	GPIO 22 GPCLK2	#	00	×	GPID 25 PWMD	26
23	GPIO 23 PWM1	я	00	×	Ground	
24	GPI0 24 POV_FS/PWWQ	10	00	×	GPI0 27	27
25	GPI0 25	h	00	2	GPIO 28 POM_DIN	28
	Ground	n	00	8	GPIO 29 PCNI DOUT	29

Attention! The GPO pin numbering used in this chapter is intended for use with WiringPi / PHI. This pin numbering is not the rew Broadcom CPIO pin numbers.

http://www.pi-lj.com

