

DBG_USB_5v

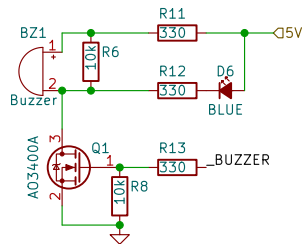
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Sheet: /_DEBUG_USB_UART/
File: _debug_usb_uart.kicad_sch

Title: MagLock

Size: A4 Date: 2022-07-20
KiCad E.D.A. eeschema 6.0.5+dfsg-1-bpo11+1

Rev: 2.0
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Thermometer

U4

3V3

DS_DATA

4k7

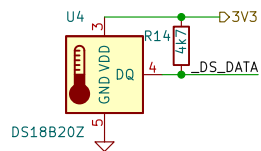
R14

DQ

GND VDD

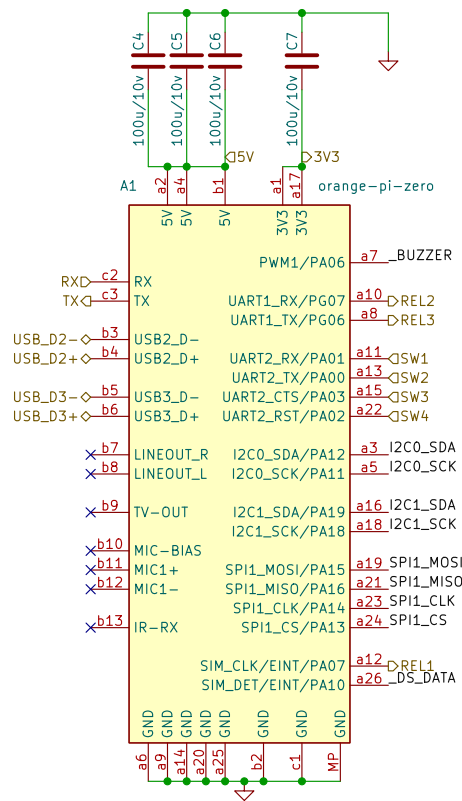
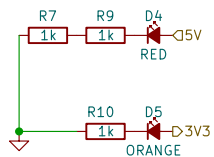
5V

DS18B20Z



Power Indicators

The diagram illustrates a circuit for power indicators. It features two parallel branches connected to a common ground. The top branch consists of a 1k resistor (R7), a 1k resistor (R9), and a red LED (D4) connected to a 5V supply. The bottom branch consists of a 1k resistor (R10) and an orange LED (D5) connected to a 3V3 supply. The ground connection is shown as a common point for both branches.



Displays

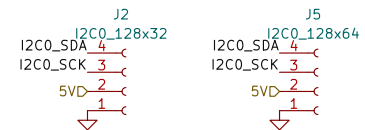
Diagram illustrating the pin connections for two displays, J2 and J5.

J2 Connections:

- I2C0_SDA (4)
- I2C0_SCK (3)
- 5VD (2)
- Ground (1)

J5 Connections:

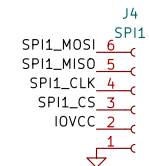
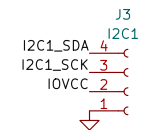
- I2C0_SDA (4)
- I2C0_SCK (3)
- 5VD (2)
- Ground (1)



GPIO Breakout

The diagram illustrates a GPIO Breakout board with three headers:

- JP1 (IO power selector):** A 3-pin header. Pin 1 is labeled 3V3, Pin 2 is labeled IOVCC, and Pin 3 is labeled 5V. A green wire is connected to Pin 2 (IOVCC).
- J3 (I2C1):** A 5-pin header. Pin 1 is connected to ground. Pins 2, 3, and 4 are labeled I2C1_SDA, I2C1_SCK, and IOVCC respectively. Pin 5 is connected to ground.
- J4 (SPI1):** A 6-pin header. Pin 1 is connected to ground. Pins 2, 3, 4, 5, and 6 are labeled SPI1_CS, SPI1_CLK, SPI1_MISO, SPI1_MOSI, and SPI1 respectively.

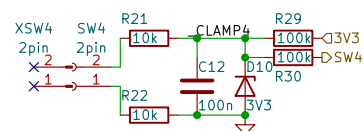
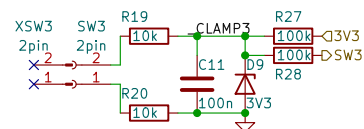
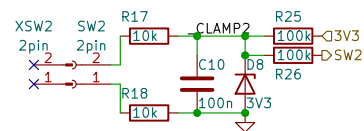
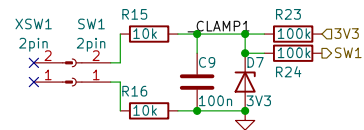


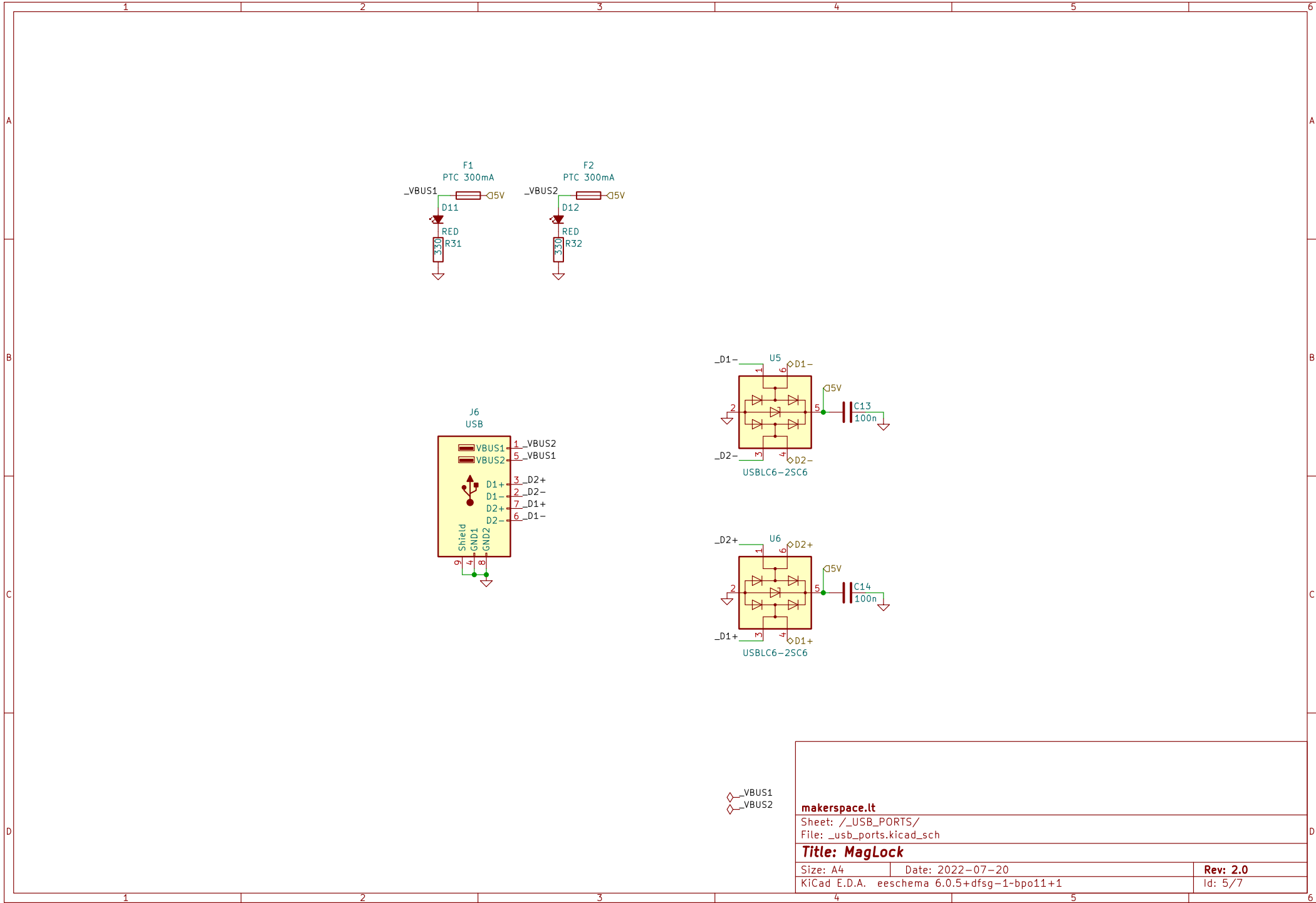
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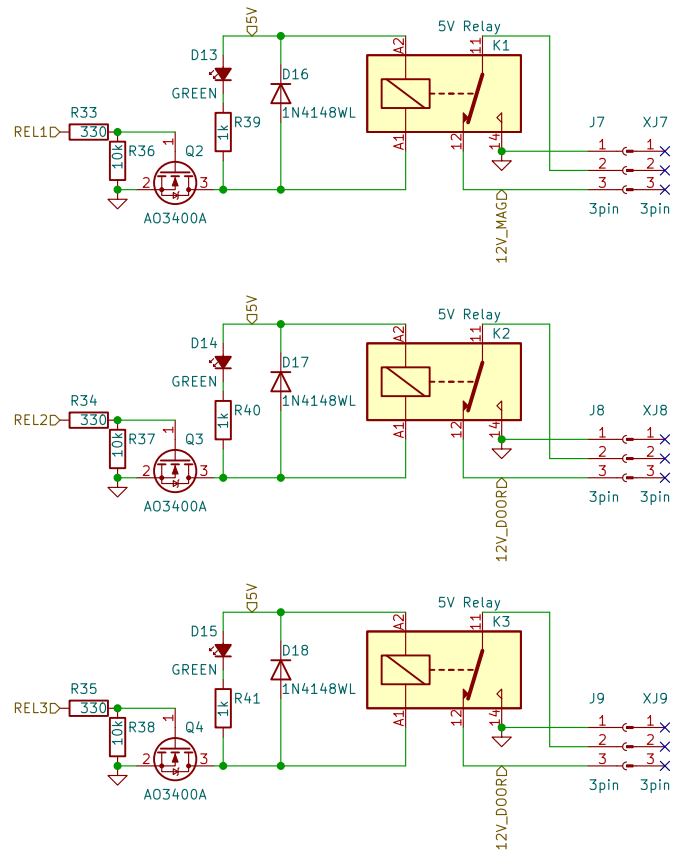
Title: MagLock

Size: A4 Date: 2022-07-20

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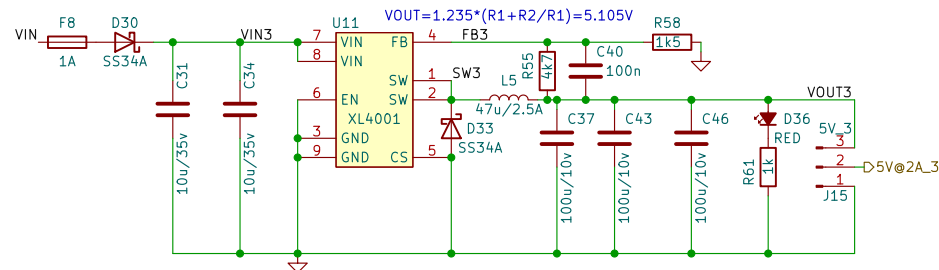
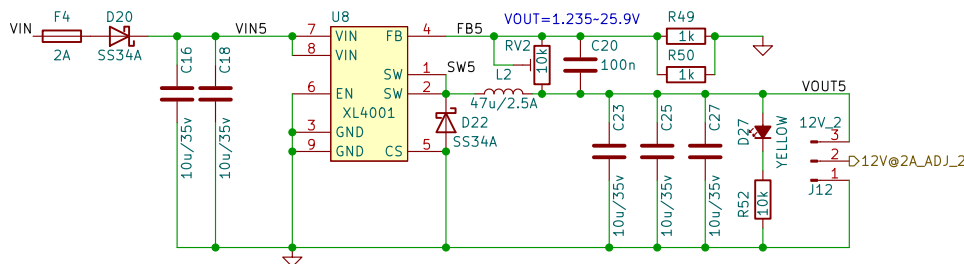
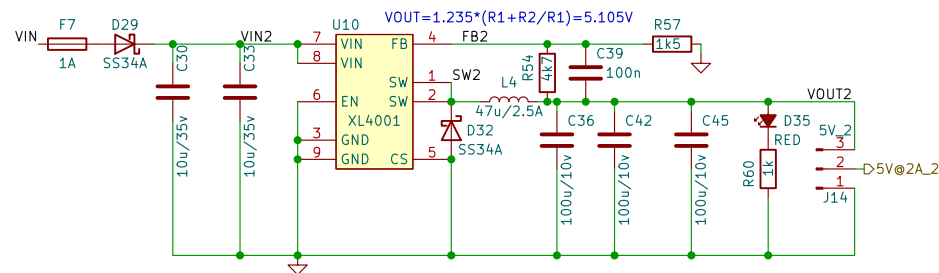
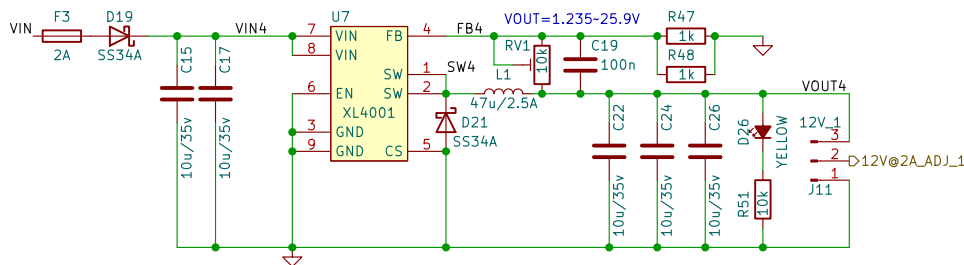
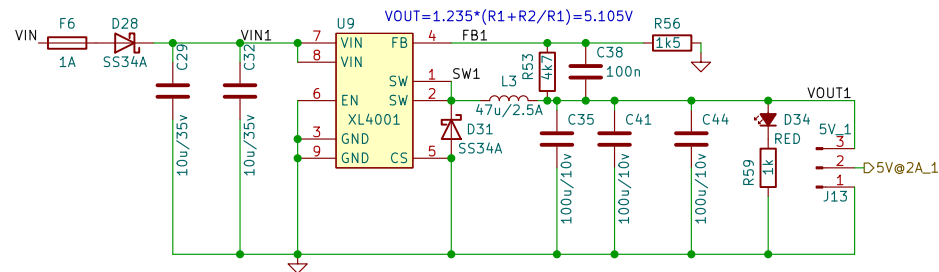
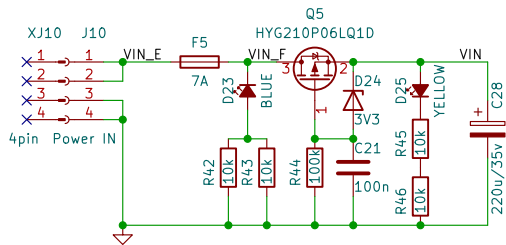
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Power Distribution

VIN=12-30V @ 6.5A - 60W

Magnet - 12V@1A ADJ - 12W
Door - 12V@2A ADJ - 24W
Orange - 5.1V@2A - 10W
USB - 5.1V@1A - 5W
In/Rel - 5.1V@1A - 5W



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