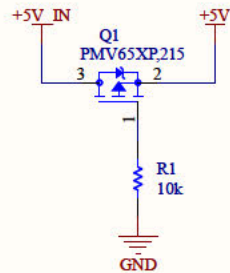
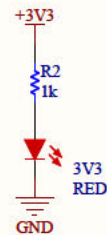
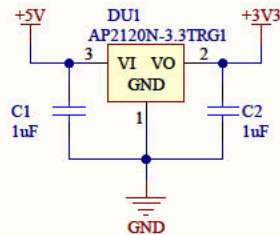


## +3.3V POWER SUPPLY

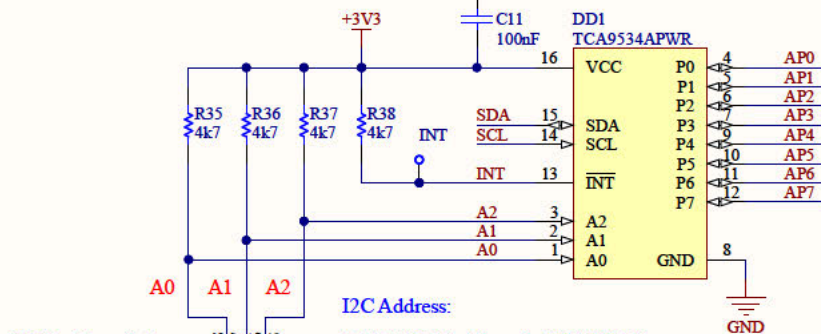
Reverse polarity protection



AP2120N-3.3TRG1  
XC6206-3.3V  
ME6206A33XG  
AP7333-3.3SRG  
MCP1700T-3302E/TT  
LN6206P332MR-G  
ME6206A33M3G



## I2C



I2C Address Selector

I2C Address:

0 1 1 1 A2 A1 A0 x - for TCA9534A

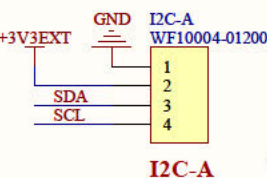
0 1 0 0 A2 A1 A0 x - for TCA9534

TCA9534APWR  
TCA9534PWR  
PCA9534APWR  
PCA9534PWR

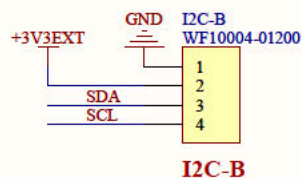
PCA9554(A)PW???

PCA9654EDTR2G???

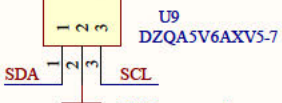
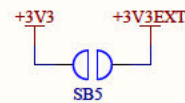
ON - CLOSED - 0, OFF - OPENED - 1



I2C-A



I2C-B

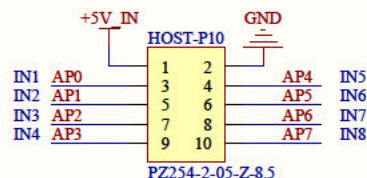
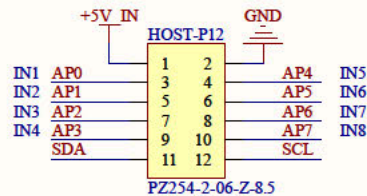


ESD protection

Alternative connectors for HOST-P are the same but 12 pins

Board dimensions: 47.0 x 56.0

## CONNECTORS



PZ254-2-05-Z-8.5  
X6521WV-2x05H-C60D30  
PZ254V-12-10P  
PZ2.54-2\*5

## Isolated Digital Inputs - 8 channels

Max. current consumption (+3V3) =  $8 * (3.3 / 4.7 + (3.3 - 1) / 1) + (3.3 - 1) / 1 \approx 30 \text{ mA}$

### 1) BIPOLAR INPUT

Input (DINx):  $V_i = 0 \dots 2\text{V}$  (Log.0) - optocoupler is closed. Output (INx):  $V_o = 3.0 \dots 3.3\text{V}$  (log.1)

$V_i = 2 \dots 4.2\text{V}$  - undefined range

Input (DINx):  $V_i = 4.2 \dots 36\text{V}$  (Log.1) - optocoupler is opened. Output (INx):  $V_o = 0 \dots 0.3\text{V}$  (log.0)

Input (DINx):  $I_{max} = 3.5\text{mA}$

### 2) UNIPOLAR INPUT

Input (DINx):  $V_i = 0 \dots 1\text{V}$  (Log.0) - optocoupler is closed. Output (INx):  $V_o = 3.0 \dots 3.3\text{V}$  (log.1)

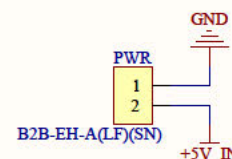
$V_i = 1 \dots 3\text{V}$  - undefined range

Input (DINx):  $V_i = 3 \dots 36\text{V}$  (Log.1) - optocoupler is opened. Output (INx):  $V_o = 0 \dots 0.3\text{V}$  (log.0)

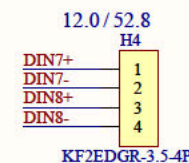
Input (DINx):  $I_{max} = 3.5\text{mA}$

Attention!

1. I2C (DD1, C7, R35...R38, SW1, U9, I2C-A, I2C-B) - optional
2. Mount HOST-P12 or HOST-P10 or PWR



B2B-EH-A(LF)(SN)\_2.54  
B2B-EH-A(LF)(SN) - pin length - 3.3 mm  
B2B-EH(LF)(SN) - pin length - 4 mm



12.0 / 52.8

H4

1: DIN7+

2: DIN7-

3: DIN8+

4: DIN8-

KF2EDGR-3.5-4P



12.0 / 52.8

H4

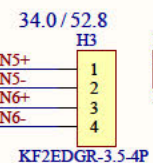
1: DIN7+

2: DIN7-

3: DIN8+

4: DIN8-

KF2EDGR-3.5-4P



34.0 / 52.8

H3

1: DIN5+

2: DIN5-

3: DIN6+

4: DIN6-

KF2EDGR-3.5-4P



12.0 / 52.8

H4

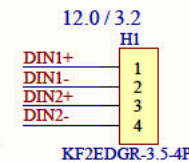
1: DIN7+

2: DIN7-

3: DIN8+

4: DIN8-

KF2EDGR-3.5-4P



12.0 / 3.2

H1

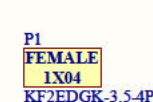
1: DIN1+

2: DIN1-

3: DIN2+

4: DIN2-

KF2EDGR-3.5-4P



12.0 / 3.2

H1

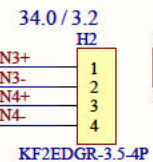
1: DIN1+

2: DIN1-

3: DIN2+

4: DIN2-

KF2EDGR-3.5-4P



34.0 / 3.2

H2

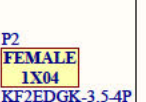
1: DIN3+

2: DIN3-

3: DIN4+

4: DIN4-

KF2EDGR-3.5-4P



12.0 / 3.2

H1

1: DIN1+

2: DIN1-

3: DIN2+

4: DIN2-

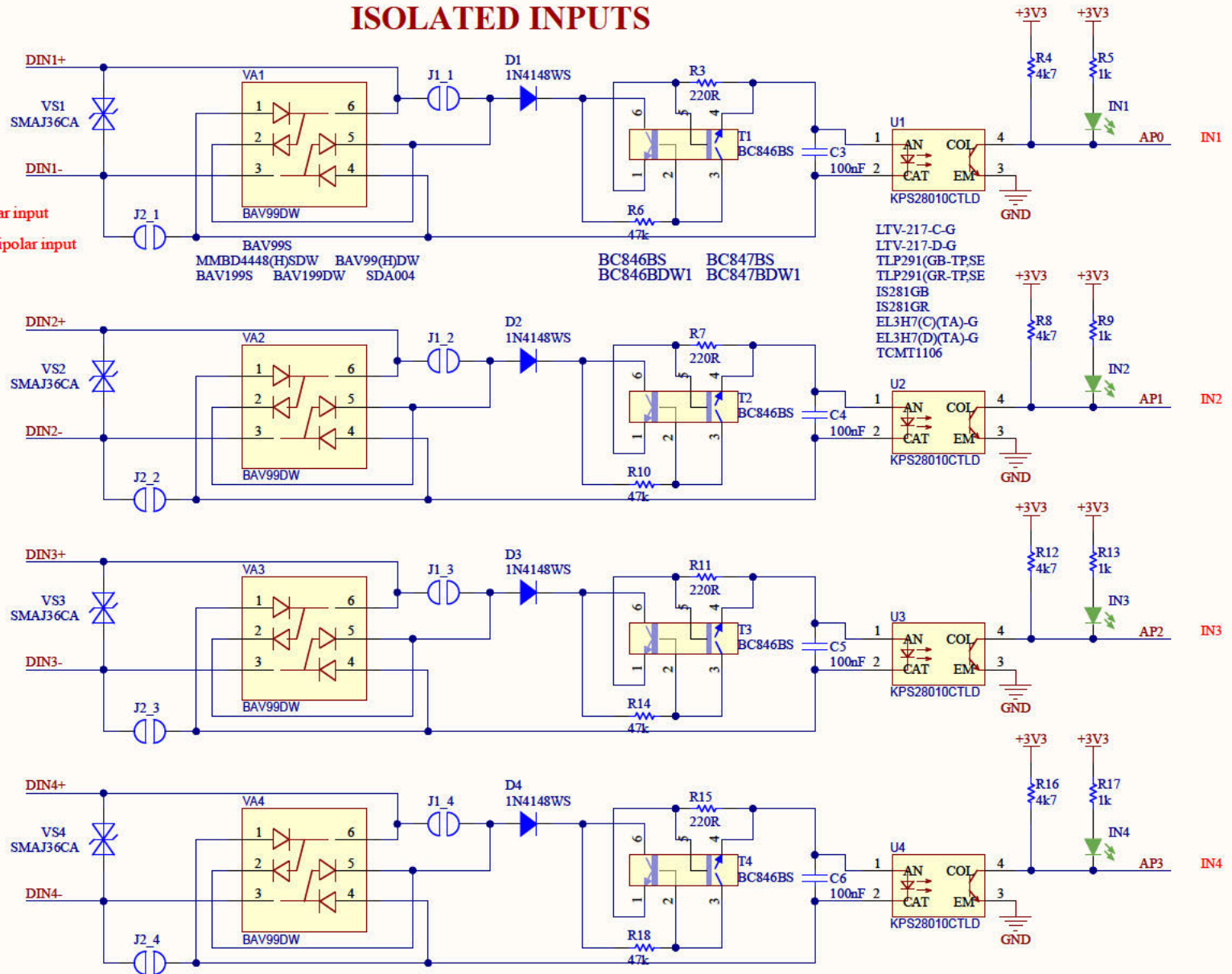
KF2EDGR-3.5-4P

# ISOLATED INPUTS

J1\_x, J2\_x - normally open

1) J1\_x, J2\_x - opened, VAx - mounted - bipolar input

2) J1\_x, J2\_x - closed, VAx - not mounted - unipolar input



## ISOLATED INPUTS

