ECE 411

Practicum Week 7 HW Part 1

Design Reviews

Team 5: Jianyu Hao, Jaeyoon Lee, Kelly Makinster, Sean Schorzman 11/20/19

From Group 17:

- AVCC needs power + cap to ground
- AREF needs cap to ground
- LCD Pin 10 needs Volts
- Give names to J2

From Eric

- Make sure Team name, date, version is on silk screen
- Thicken 5V
- Trace 0 ground supplies Fix it
- Refer to AREF datasheet
- Power regulator needs to take in 5 volts
- 6 pin header and Display LCD should be separated a little

From Andrew:

- 100 nF cap AREF cap to ground
- Bypass AVCC cap to ground
- Ground symbol for each LCD
- Via 'GND' stitch two ground planes
- Din +doc dimensions
- J2 Blue wire change top larger (Red)
- PD2 serial adapter delete Pin 1 on J2
- Bypass cap literally next to Pin C6
- Drill holes too small Make larger