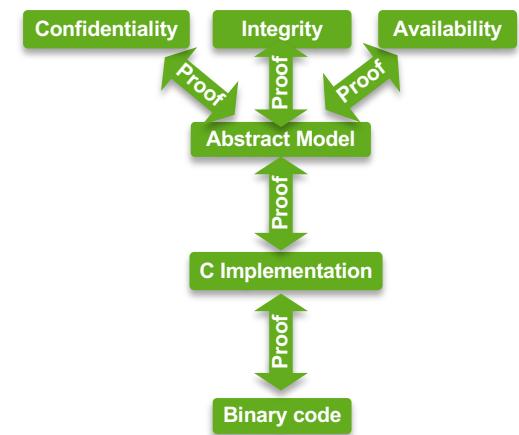


School of Computer Science & Engineering
COMP9242 Advanced Operating Systems

2022 T2 Week 10 Part 1

Formal Verification and seL4

@GernotHeiser



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Today's Lecture

- Assurance and verification
 - Common Criteria
 - Formal verification
- seL4
 - Functional correctness
 - Translation correctness
 - Security enforcement
 - Verification limitations
 - WCET analysis
 - Cost of verification
- Security impact of OS design

Assurance and Verification

Refresher: Assurance and Formal Verification

- **Assurance:**

- systematic evaluation and testing
- essentially an intensive and onerous form of quality assurance

- **Formal verification:**

- mathematical proof

Assurance and formal verification aim to establish correctness of

- mechanism design
- mechanism implementation

- **Certification:** independent examination

- confirming that the assurance or verification was done right

Assurance: Substantiating Trust

- Specification
 - Unambiguous description of desired behaviour
- System design
 - Justification that it meets specification
- Implementation
 - Justification that it implements the design
- Maintenance
 - Justifies that system use meets assumptions

Informal (English)
or formal (maths)

Compelling argument
or formal proof

Code inspection,
rigorous testing,
proof

Common Criteria

- *Common Criteria for IT Security Evaluation* [ISO/IEC 15408, 99]
 - ISO standard, for general use
 - Evaluates QA used to ensure systems meet their requirements
 - Developed out of the famous US DOD “Orange Book”:
Trusted Computer System Evaluation Criteria [1985]
- Terminology:
 - *Target of evaluation* (TOE): Evaluated system
 - *Security target* (ST): Defines requirements
 - *Protection profile* (PP): Standardised ST template
 - *Evaluation assurance level* (EAL): Defines thoroughness of evaluation
 - PPs have maximum EAL they can be used for

CC: Evaluation Assurance Levels

Thoroughness, cost



Level	Requirements	Specification	Design	Implementation
EAL1	not evaluated	Informal	not eval	not evaluated
EAL2	not evaluated	Informal	Informal	not evaluated
EAL3	not evaluated	Informal	Informal	not evaluated
EAL4	not evaluated	Informal	Informal	not evaluated
EAL5	not evaluated	Semi-Formal	Semi-Formal	Informal
EAL6	Formal	Semi-Formal	Semi-Formal	Informal
EAL7	Formal	Formal	Formal	Informal

Common Criteria: Protection Profiles (PPs)

- *Controlled Access PP* (CAPP)
 - standard OS security, up to EAL3
- *Single Level Operating System PP*
 - superset of CAPP, up to EAL4+
- *Labelled Security PP* (LSPP)
 - MAC for COTS OSes
- *Multi-Level Operating System PP*
 - superset of CAPP, LSPP, up to EAL4+
- *Separation Kernel Protection Profile* (SKPP)
 - strict partitioning, for EAL6-7

COTS OS Certifications

- EAL3:
 - 2010 Mac OS X (10.6)
- EAL4:
 - 2003: Windows 2000
 - 2005: SuSE Enterprise Linux
 - 2006: Solaris 10 (EAL4+)
 - against CAPP (an EAL3 PP!)
 - 2007: Red Hat Linux (EAL4+)
- EAL6:
 - 2008: Green Hills INTEGRITY-178B (EAL6+)
 - against SKPP, relatively simple PPC-based hardware platform in TOE
- EAL7:
 - 2019: Prove & Run PROVENCORE



Get regularly hacked!

SKPP on Commodity Hardware

- SKPP: OS provides only separation
- One Box One Wire (OB1) Project
 - Use INTEGRITY-178B to isolate VMs on commodity desktop hardware
 - Leverage existing INTEGRITY certification
 - by “porting” it to commodity platform

NSA subsequently dis-endorsed SKPP,
discontinued certifying ≥EAL5

Conclusion [NSA, March 2010]:

- SKPP validation for commodity hardware platforms
infeasible due to their complexity
- SKPP has limited relevance for these platforms

Common Criteria Limitations...

Effectively dead in
5-Eyes defence

- Very expensive
 - rule of thumb: EAL6+ costs \$1K/LOC [Green Hills]
design-implementation-evaluation-certification
- Too much focus on development process
 - rather than the product that was delivered
- Lower EALs of little practical use for OSes
 - c.f. COTS OS EAL4 certifications
- Commercial Licensed Evaluation Facilities licenses rarely revoked
 - Leads to potential “race to the bottom” [Anderson & Fuloria, 2009]

Formal Verification

- Prove properties about a mathematical model of a system

Model checking / abstract interpretation:

- Systematic exploration of system state space
 - Cannot generally prove code correct
 - Proves specific properties
 - Generally have to
 - over-approximate (false positives), or
 - under-approximate (false negatives, unsound)
 - Suffers state-space explosion
 - ✓ Automatic
 - ✓ May scale to large code bases

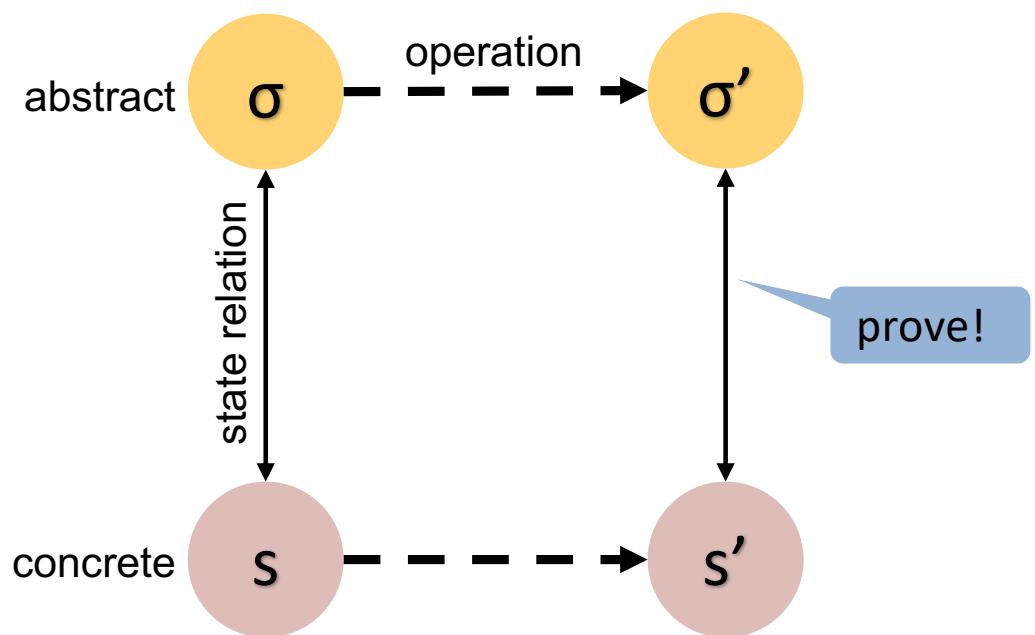
Theorem proving:

- Proofs about state spaces
 - ✓ Can deal with large (even infinite) state spaces
 - ✓ Can prove functional correctness against a spec
 - Very labour-intensive

Recent work automatically proved functional correctness of simple systems using SMT solvers
[Hyperkernel, SOSP'17]

Theorem Proving

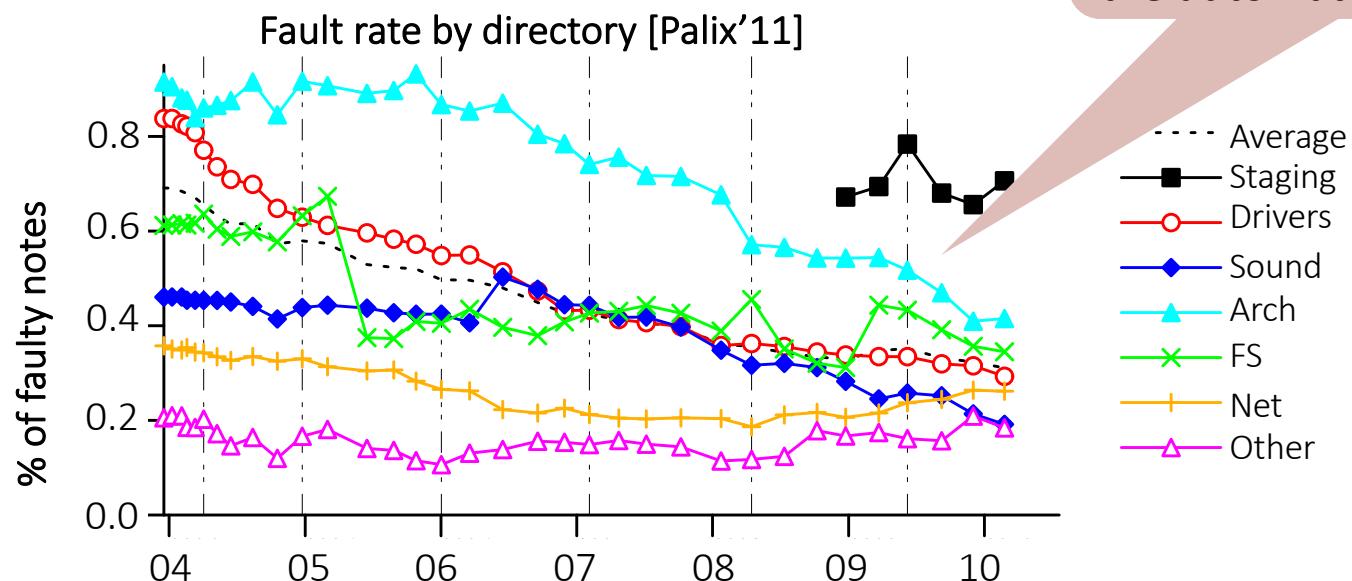
“Forward simulation”:
Prove state correspondence
of abstract and concrete levels



Model Checking and Linux: A Sad Story

- Static analysis of Linux source [Chou & al, 2001]
 - Found high density of bugs, especially in device drivers
- Re-analysis 10 years later [Palix & al, 2011]

Disappointing rate of improvement for bugs that are automatically detectable!



And the Result?

The screenshot shows a news article from Ars Technica. The header features the Ars Technica logo in an orange circle and a black bar with navigation links: BIZ & IT (which is underlined in red), TECH, SCIENCE, POLICY, CARS, and GAMING & CL. Below the header, the article title is "RISK ASSESSMENT — Unsafe at any clock speed: Linux kernel security needs a rethink". The main text begins with "Ars reports from the Linux Security Summit—and finds much work that needs to be done." A blue 'X' icon is overlaid on the right side of the article. At the bottom, there is a snippet of text: "The Linux kernel today faces an unprecedented safety crisis. Much like when".

ars TECHNICA

RISK ASSESSMENT —

Unsafe at any clock speed: Linux kernel security needs a rethink

Ars reports from the Linux Security Summit—and finds much work that needs to be done.

J.M. PORUP (UK) - 9/27/2016, 10:57 PM

The Linux kernel today faces an unprecedented safety crisis. Much like when

BlueBorne



August 2009

The screenshot shows the homepage of Slashdot. At the top, there's a banner with the text "A NICTA bejelentette a világ első, formális módszerekkel igazolt," which is in Hungarian. Below the banner, the main navigation bar includes "Stories", "Recent", "Popular", and "Search". A sidebar on the left says "Slashdot is powered by your submissions!" and features a "Technology: World's First" section. This section includes a post by "Soulskill" from Thursday August 27th, 2009, from the "wait-for-it dept.". An anonymous reader writes about a formal verification proof for a microkernel. The text discusses the Isabelle theorem prover and its matches between executable and kernel code. A link to "We're pleased to say that we now have a para-virtualized ver" is present.

A NICTA bejelentette a világ első, formális módszerekkel igazolt,



New Scientist

Saturday 29/8/2009

Page: 21

Section: General News

Region: National

Type: Magazines Science / Technology

Size: 196.31 sq.cms.

Published: -----S-

The ultimate way to keep your computer safe from harm

"Operating systems usually have flaws in their code, and so forth are known by almost everyone to prove that a particular OS kernel is not formally verified, and as such it is not safe," says Klein. Researchers used an executable version of the Isabelle theorem prover to generate a formal proof that the executable matches the executable and the kernel.

"The kernel is the part of the operating system that sits at the heart of modern computers and leaves them prone to occasional malfunction and vulnerable to attack by worms and viruses. So the development of a secure general-purpose microkernel could pave the way to a much more reliable and secure computer system."

His team formulated a model with more than 200,000 logical steps which allowed them to prove that the program would always behave as its

Does it run Linux? "We're pleased to say that we now have a para-virtualized ver

eramenyekeppen peág egy olyan megbízhatóságot kapnak a szoftvertől, amely e

10 BREAKTHROUGH TECHNOLOGIES

[Share](#)

2011

Crash-Proof Code

Making critical software safer

7 comments

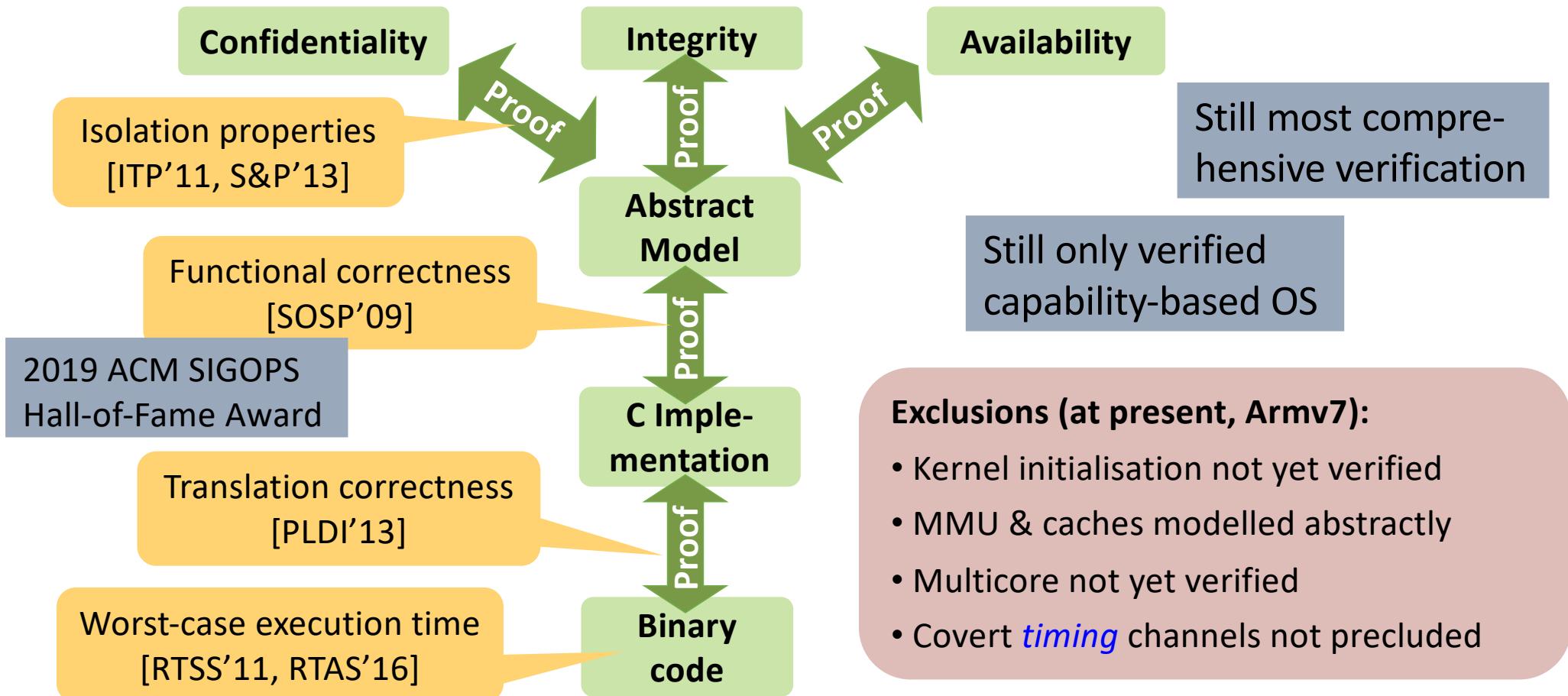
WILLIAM BULKELEY

May/June 2011





Proving Security and Safety (Armv6/7)



Security Is No Excuse For Bad Performance!

World's fastest
microkernel!

Latency (in cycles) of a round-trip cross-address-space IPC on x64

Source	seL4	Fiasco.OC	Zircon
Mi et al, 2019	986	2717	8157
seL4.systems, Jul'22	763	N/A	N/A

Within 10% of
hardware limit!

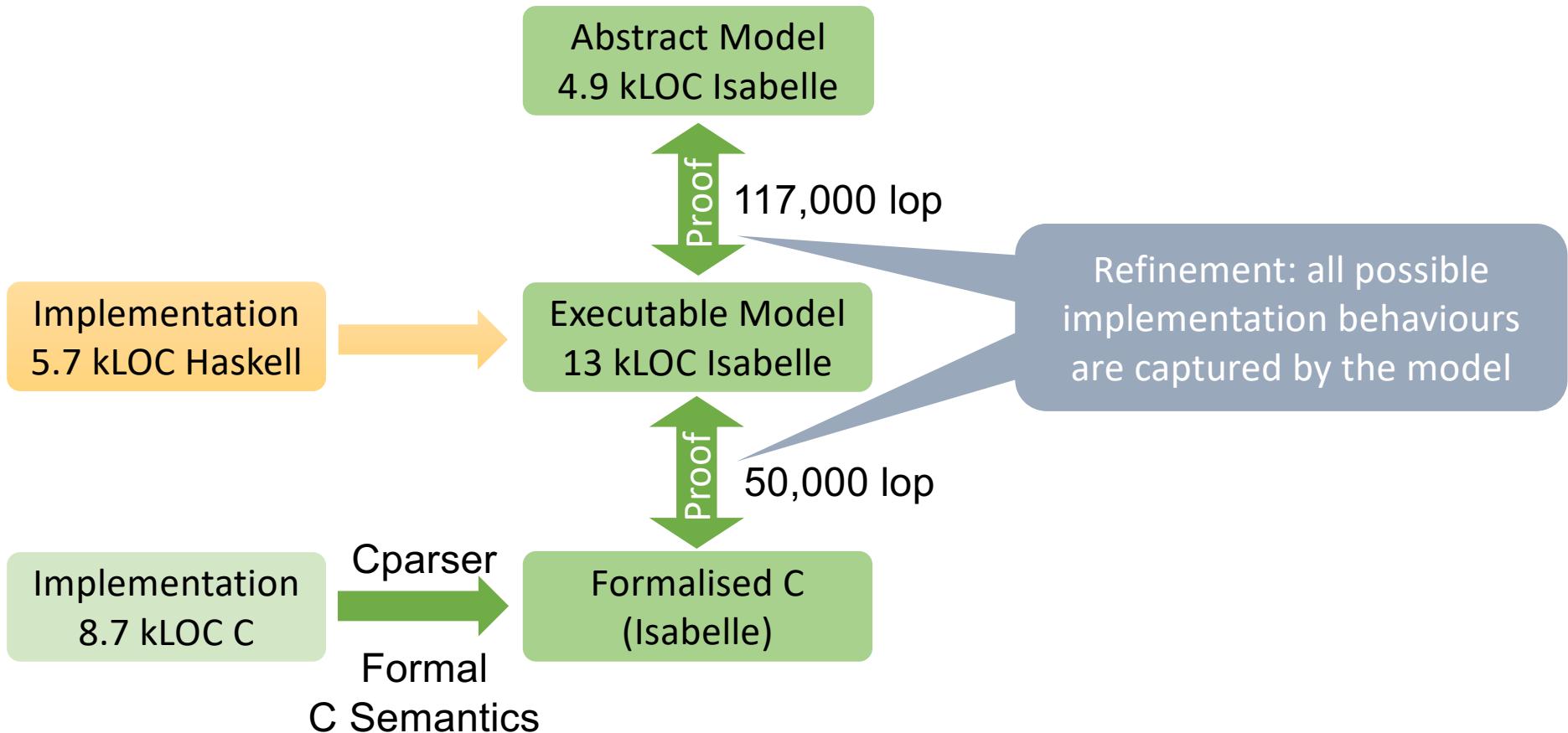
Sources:

- Zeyu Mi, Dingji Li, Zihan Yang, Xinran Wang, Haibo Chen: "SkyBridge: Fast and Secure Inter-Process Communication for Microkernels", EuroSys, April 2020
- seL4 Performance, <https://sel4.systems/About/Performance/>, accessed 2022-07-31

Functional Correctness



Proving Functional Correctness



Functional Correctness Summary

Kinds of properties proved

- Behaviour of C code is fully captured by abstract model
- Behaviour of C code is fully captured by executable model
- Kernel never fails, behaviour is always well-defined
 - assertions never fail
 - will never de-reference null pointer
 - will never access array out of bounds
 - cannot be subverted by misformed input
- All syscalls terminate, reclaiming memory is safe, ...
- Well typed references, aligned objects, kernel always mapped...
- Access control is decidable

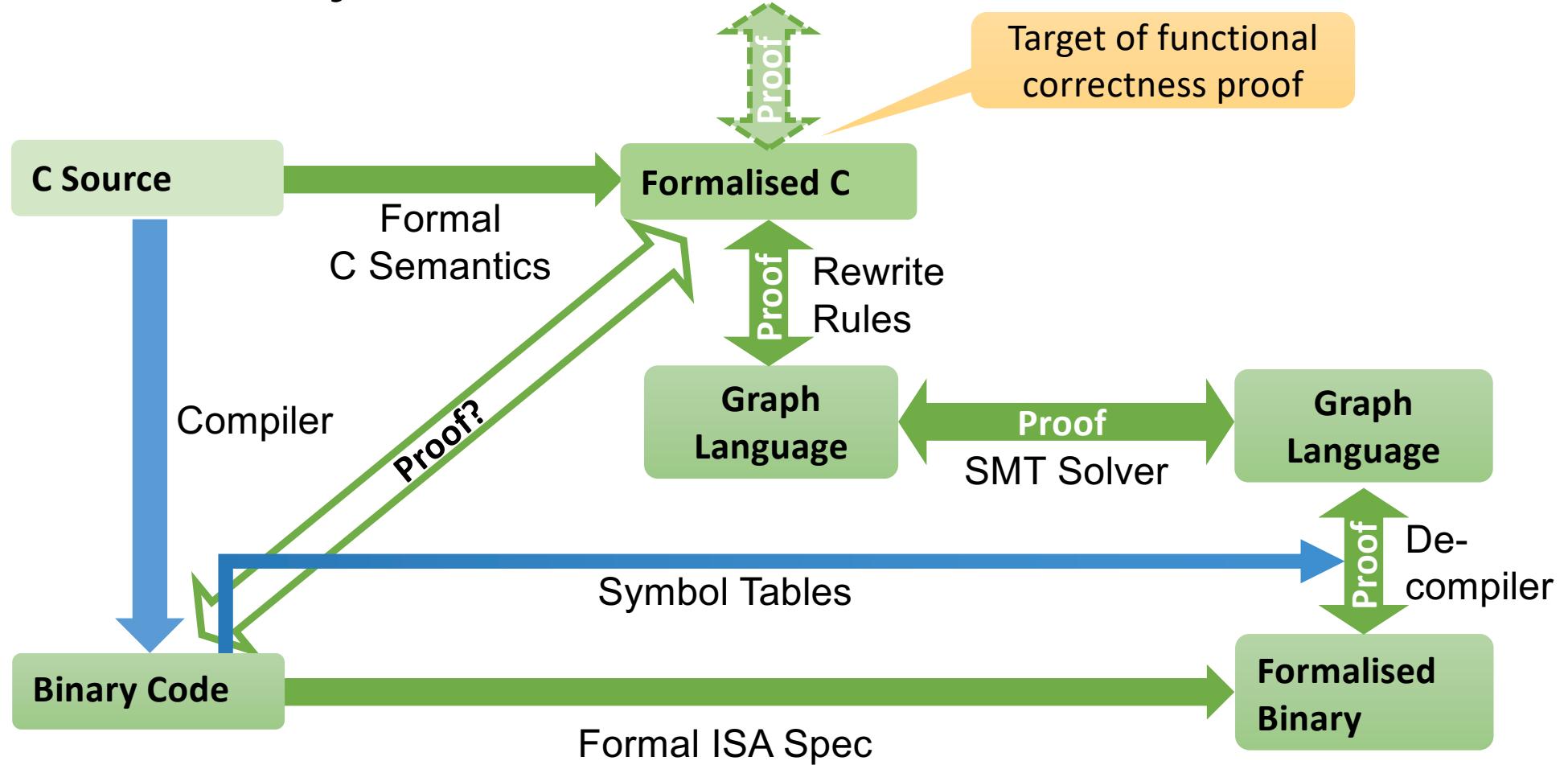
Can prove further properties on abstract level!

Bugs found:

- 16 in (shallow) testing
- 460 in verification
 - 150 in spec,
 - 150 in design,
 - 160 in C

Translation Correctness

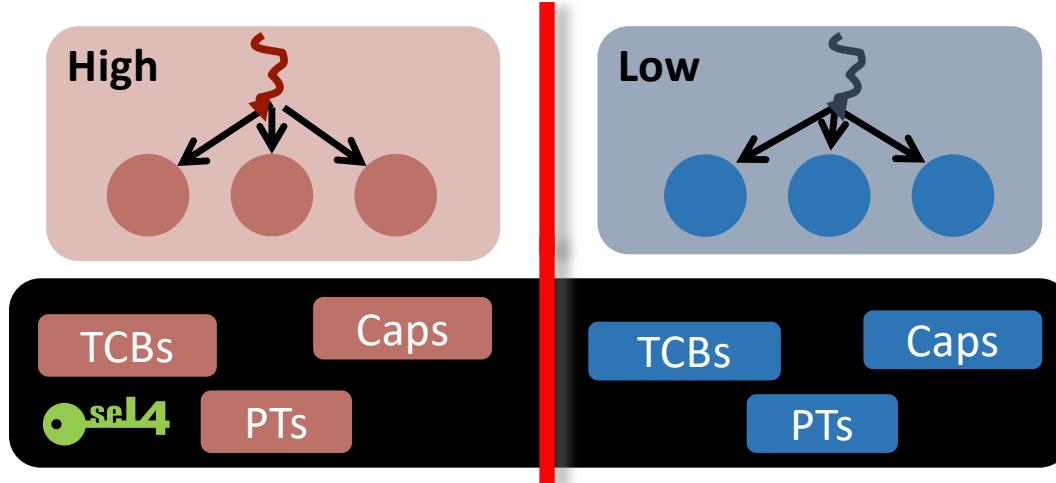
•sel4 Binary Verification: Translation Validation



Security Enforcement

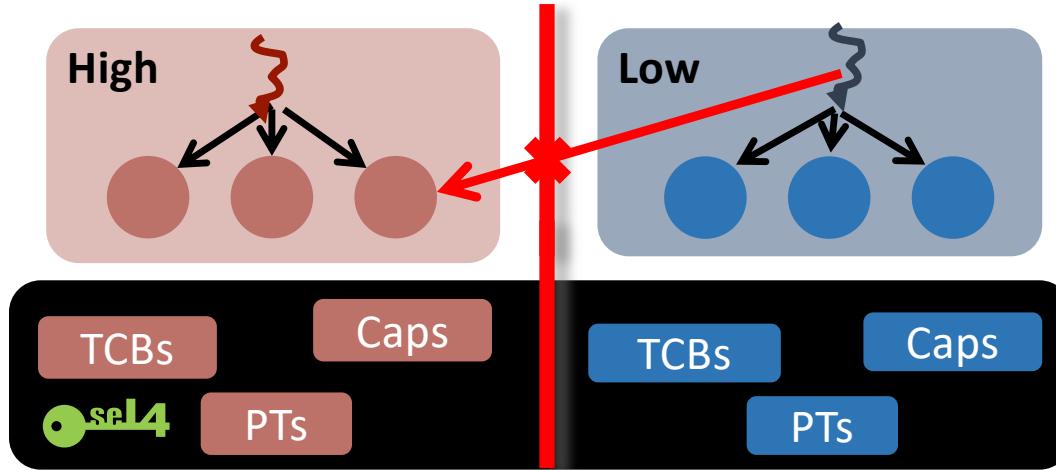


Isolation Goes Deep





Integrity: Control Write Access



To prove:

Low has no *write* capabilities to High objects
⇒ no action of Low will modify High state

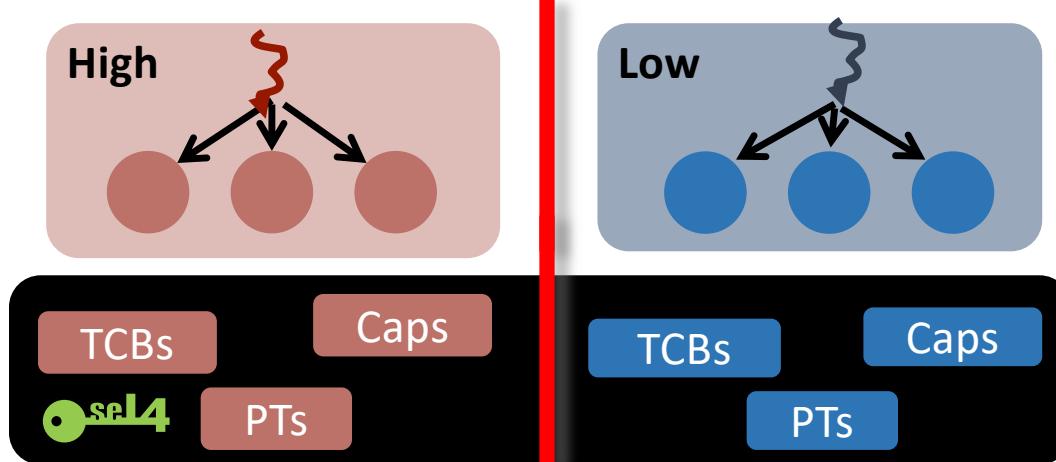
Specifically, *kernel does not modify on Low's behalf!*

Event-based kernel always operates on behalf of well-defined user:

- Prove kernel only modifies data if presented write cap



Availability: Ensuring Resource Access

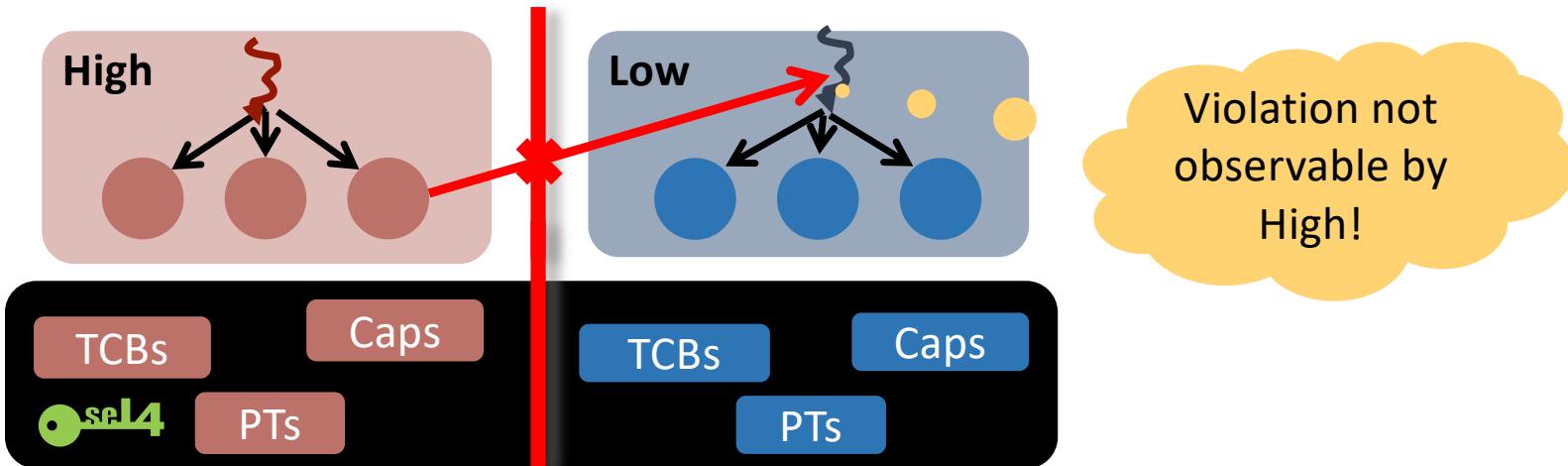


Nothing to do, implied
by other properties!

Strict separation of kernel resources
⇒ Low cannot deny High access to resources



Confidentiality: Control Information Flow



Non-interference proof:

- Evolution of Low does not depend on High state
- Also shows absence of covert *storage channels*

To prove:

Low has no *read* capabilities to High objects
⇒ no action will reveal High state to Low



Confidentiality Proof Challenge

Spec

```
bool a();
```

Idiotic but valid refinement

Implementation

```
bool a() {  
    return !secret;  
}
```

Solution:

- Remove non-determinism where it affects confidentiality
- Eg: scheduler strictly round-robin

Non-determinism
breaks confidentiality
under refinement!

Infoflow is very strong
property, requiring
restrictions rarely met
in real world

Limitations



Verification Assumptions

1. Hardware behaves as expected

- Formalised hardware-software contract (ISA)
- Hardware implementation free of bugs, Trojans, ...

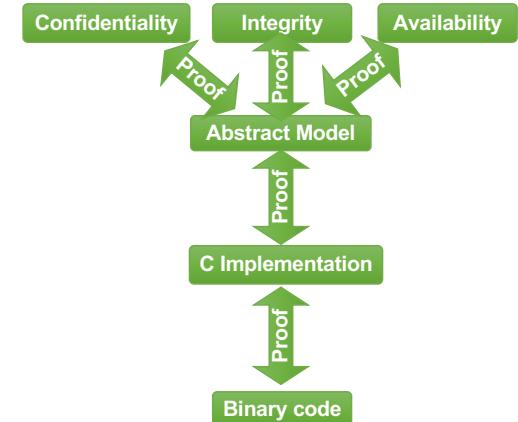
2. Spec matches expectations

- Can only prove “security” if specify what “security” means
- Spec may not be what we think it is

3. Proof checker is correct

- Isabel/HOL checking core that validates proofs against logic

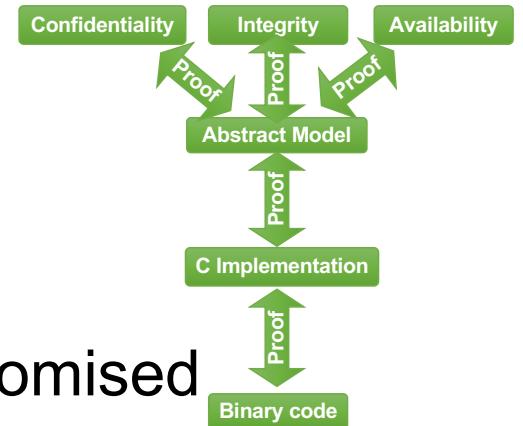
With binary verification do
not need to trust C compiler!





Present Verification Limitations

- Not verified boot code
 - **Assume** it leaves kernel in safe state
- Caches/MMU presently modeled at high level / axiomised
 - MMU model finished by recent PhD
- Not proved any temporal properties
 - Presently not proved scheduler observes priorities, properties needed for RT
 - WCET analysis applies only to dated ARM11/A8 cores
 - No proofs about timing channels (yet)

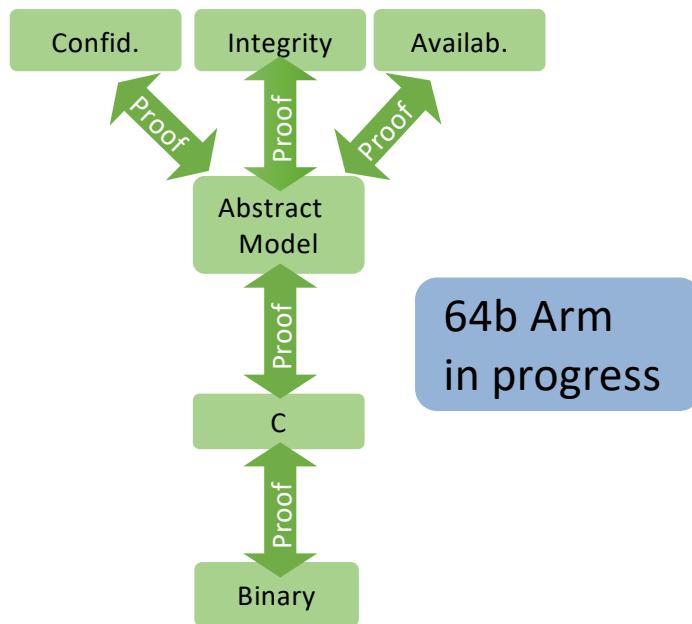


Present research!



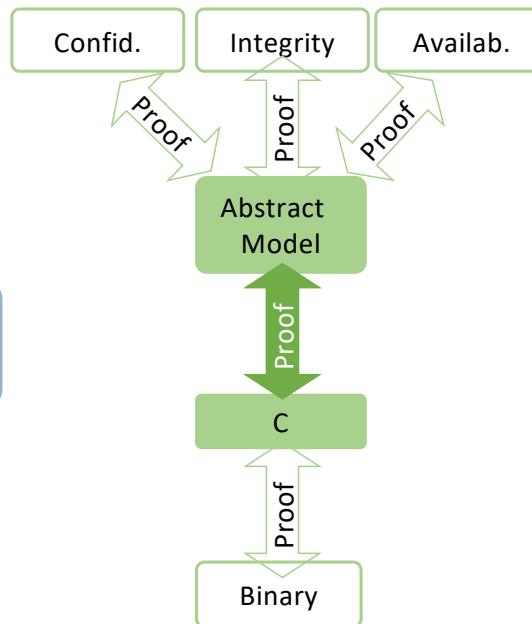
Present Status

32b Arm

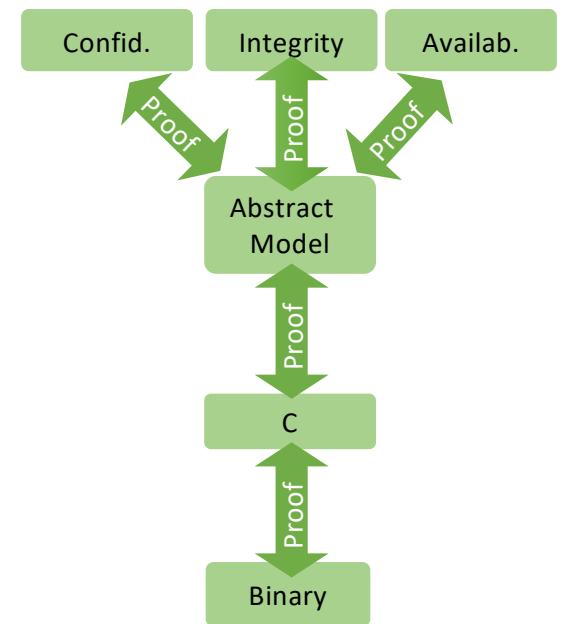


64b Arm
in progress

64b x86



64b RISC-V





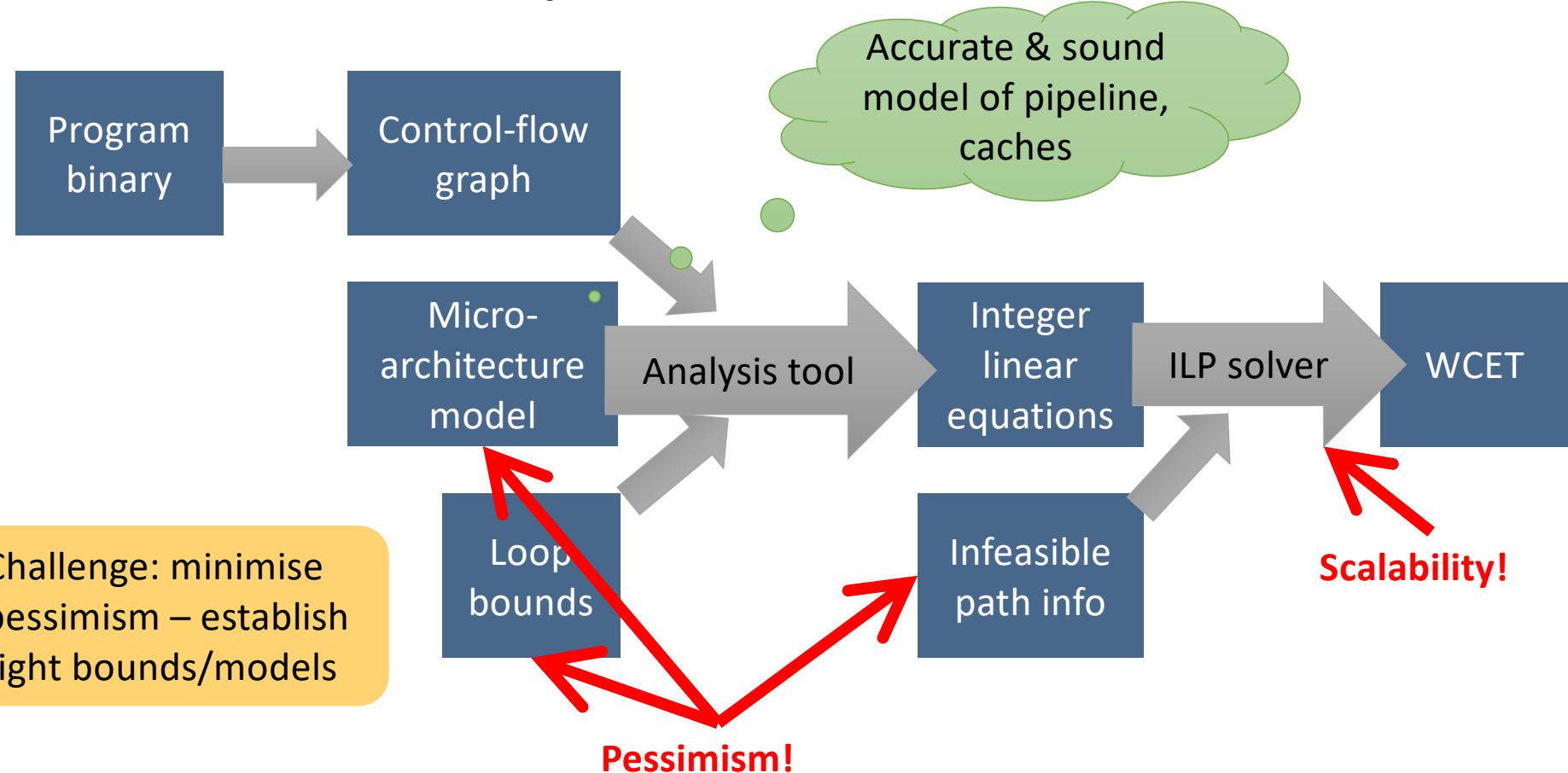
Common Criteria?

Level	Requirements	Specification	Design	Implementation
EAL1	not evaluated	Informal	not eval	not evaluated
EAL2	not evaluated	Informal	Informal	not evaluated
EAL3	not evaluated	Informal	Informal	not evaluated
EAL4	not evaluated	Informal	Informal	not evaluated
EAL5	not evaluated	Semi-Formal	Semi-Formal	Informal
EAL6	Formal	Semi-Formal	Semi-Formal	Informal
EAL7	Formal	Formal	Formal	Informal
seL4	Formal	Formal	Formal	Formal

WCET Analysis



WCET Analysis



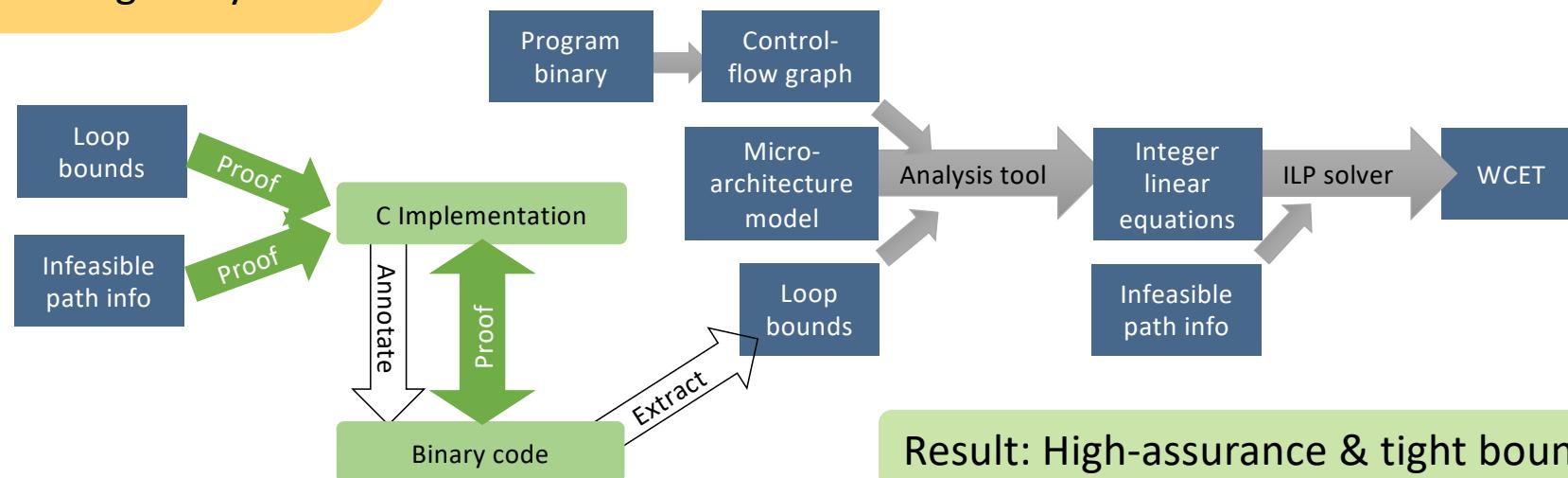


Loop Bounds & Infeasible Paths

Tight loop bounds and infeasible path refutations infeasible to obtain from binary – lack of semantic information, especially pointer aliasing analysis.

Idea:

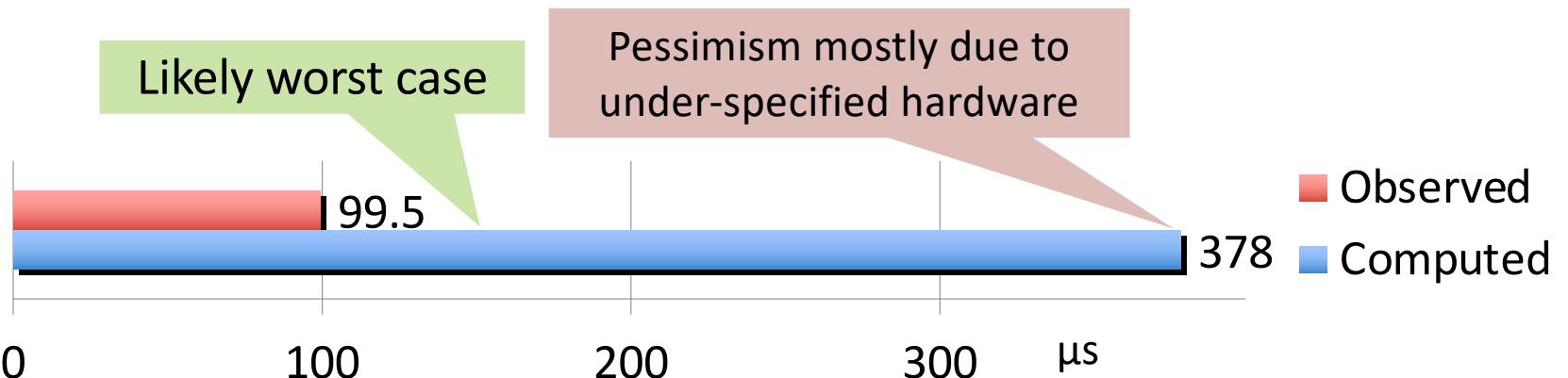
- prove on C level
- transfer to binary using translation-validation toolchain



Result: High-assurance & tight bounds!



WCET Analysis on ARM11



WCET presently limited by verification practicalities

- without regard to verification achieved 50 μs
- 10 μs seem achievable
- BCET ~ 1μs
- [Blackham'11, '12] [Sewell'16]

Problem: Latency information no longer published by Arm!



Cost of Verification



Verification Cost Breakdown

Verification		Abstract Spec
Haskell design	2 py	
C implementation	0.15 py	
Debugging/Testing	0.15 py	
Abstract spec refinement	8 py	
Executable spec refinement	3 py	
Fastpath verification	0.4 py	
Formal frameworks	9 py	
Total	24 py	
Non-reusable verification	11.5 py	
Traditional engineering	4–6 py	

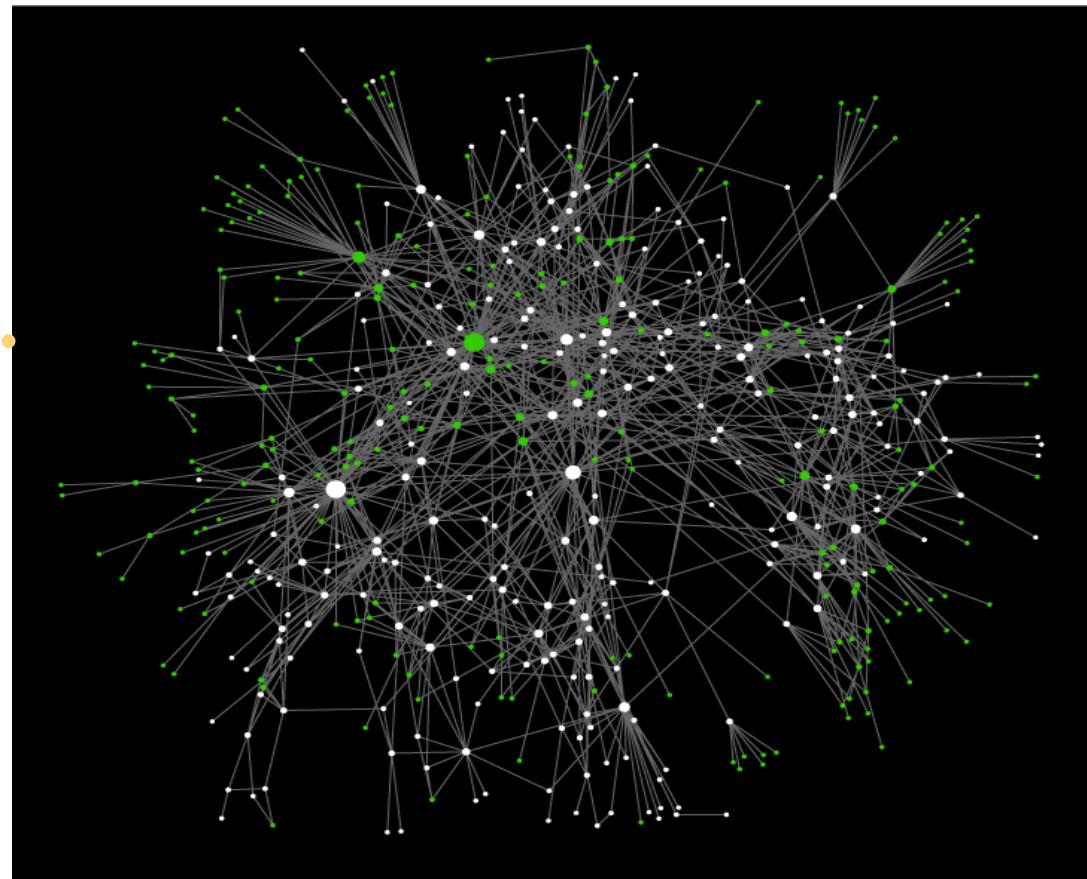
Reusable!

The diagram illustrates the verification process flow. It starts with the 'Abstract Spec' at the top, which is connected by a green double-headed arrow labeled 'Proof' to the 'Executable Spec' in the middle. Below that, another green double-headed arrow labeled 'Proof' connects the 'Executable Spec' to the 'C Implementation' at the bottom. A large green box labeled 'Verification' encloses the first seven rows of the table, spanning from 'Haskell design' to 'Formal frameworks'. A green callout bubble pointing towards this box contains the text 'Reusable!'. A blue double-headed arrow labeled 'Proof' connects the 'Abstract Spec' to the 'Total' row in the table.



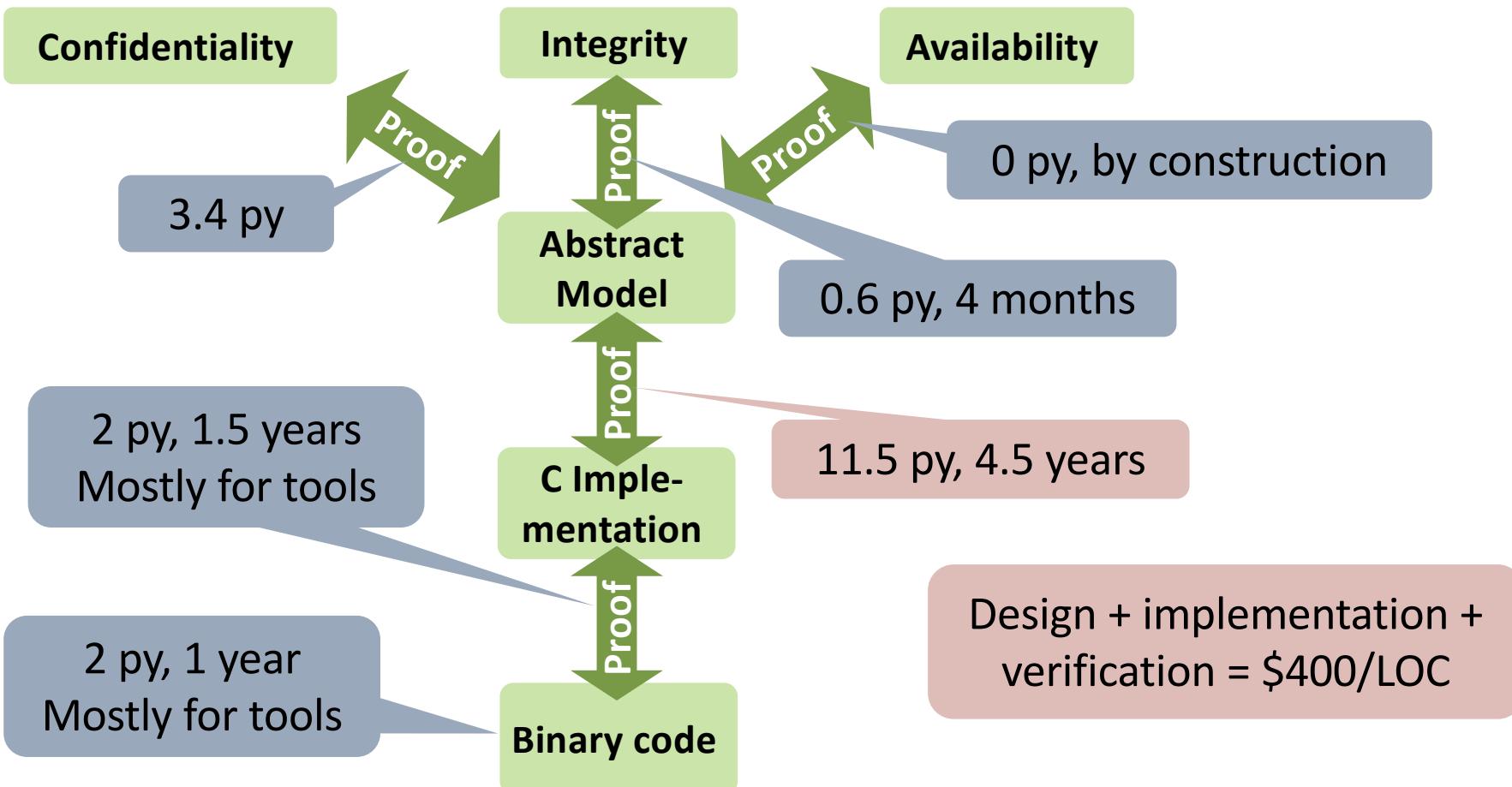
Why So Hard for 9,000 LOC?

seL4 call
graph



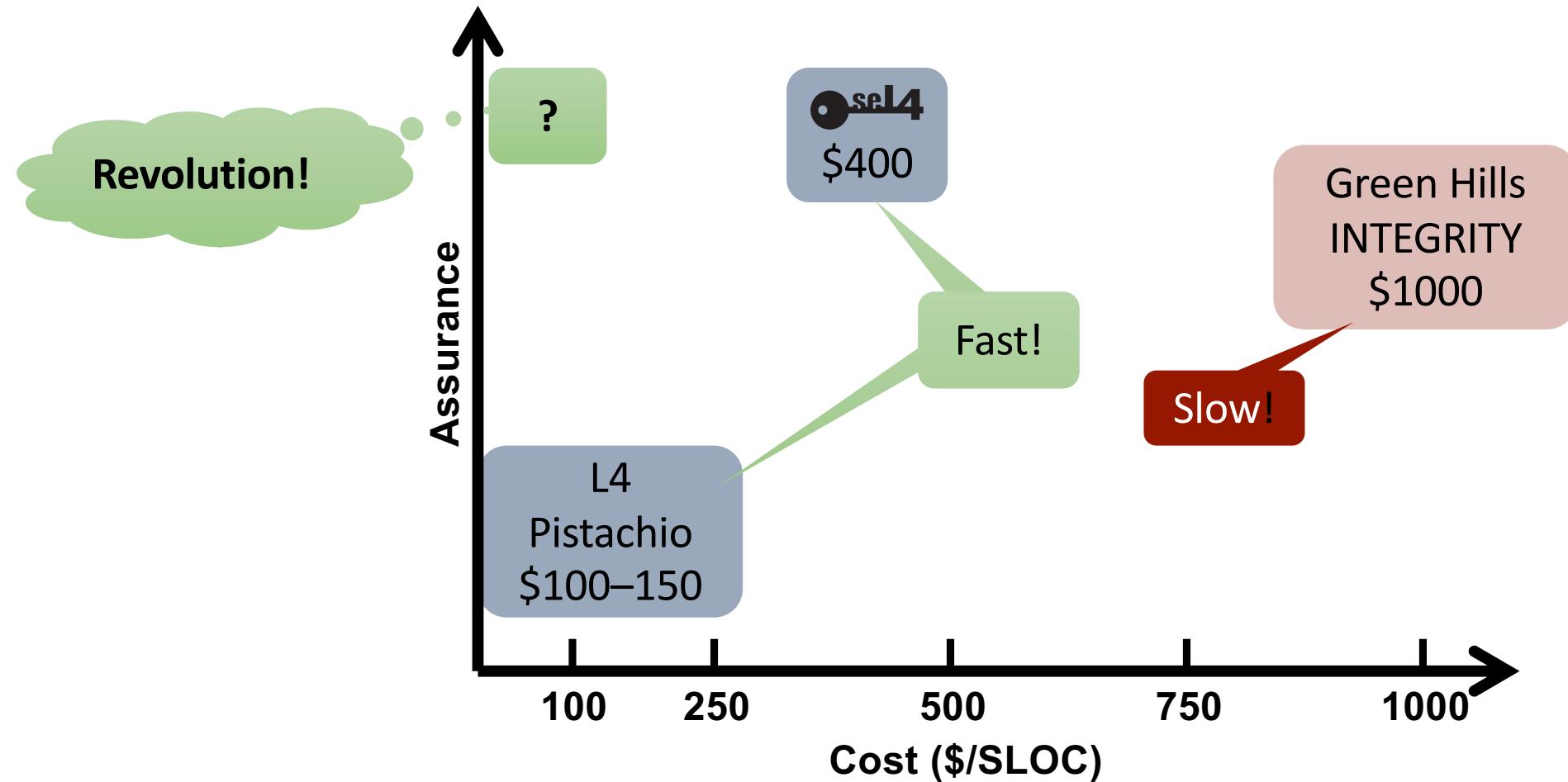


Verification Cost





Microkernel Life-Cycle Cost in Context



Security Impact of OS Design

Quantifying OS-Design Security Impact

Approach:

- Examine all ***critical*** Linux CVEs (vulnerabilities & exploits database)

- easy to exploit
- high impact
- no defence available
- confirmed

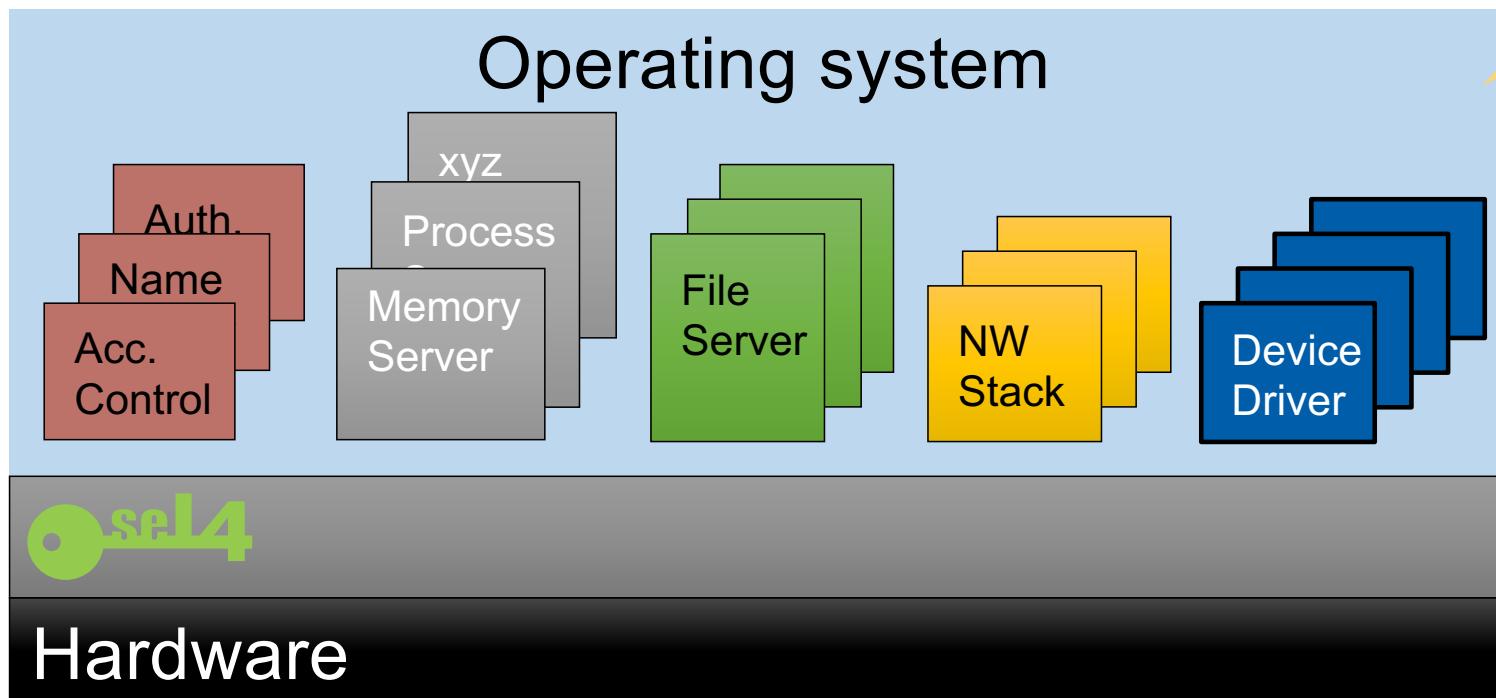
115 critical
Linux CVEs to
Nov'17

- For each establish how microkernel-based design would change impact

• seL4 Hypothetical seL4-based OS

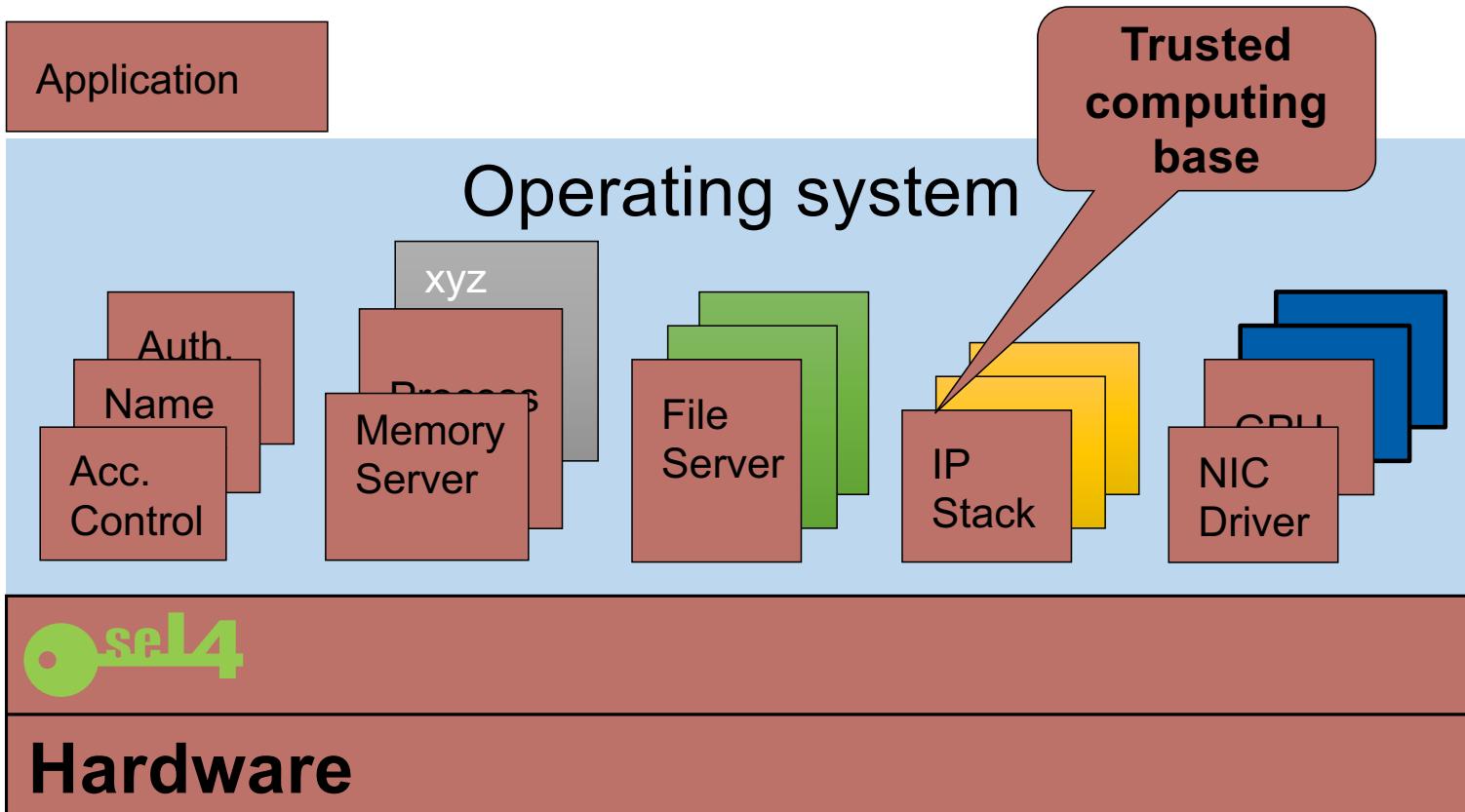
OS structured in *isolated* components, minimal inter-component dependencies, *least privilege*

Functionality comparable to Linux





Hypothetical Security-Critical App

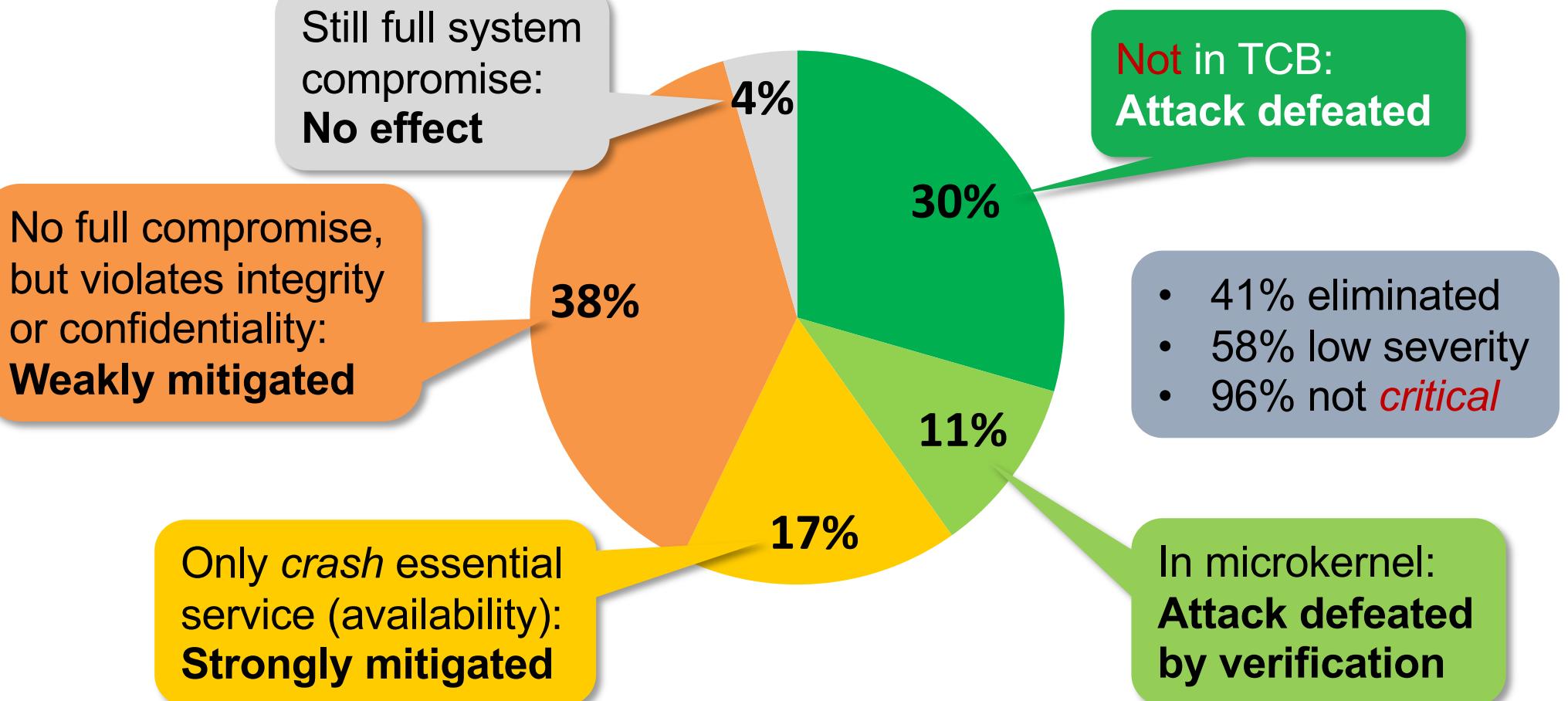


App requires:

- IP networking
- File storage
- Display output



All Critical Linux CVEs to 2017



Conclusion: OS Structure Matters

- Microkernels definitely improve security
- Microkernel verification improves further
- Monolithic OS design is *fundamentally flawed from security point of view*

[Biggs et al., APSys'18]

Use of a monolithic OS in security- or safety-critical scenarios is professional malpractice!

