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Mini Project 4

University of British Columbia

Electrical and Computer Engineering

ELEC 301

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0 Introduction

For this project, we will be using Multisim to simulate active filters and oscillators.

1 Part A

1.0.1 Part 1

For this part, we will be designing a 2nd order Butterworth low pass active filter using the UA741 operational amplifier. Here is the circuit that we will be using for this part:

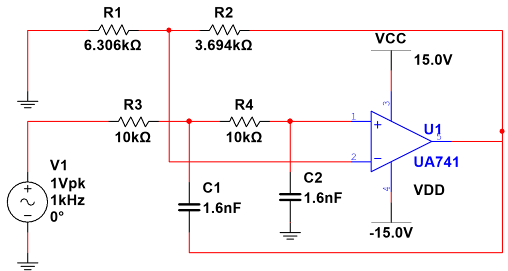

Figure 1: Second Order Butterworth Filter

The calculations to find the resistance R_1,R_2 and capacitance C, we will be using the formulas from the class notes [1]. The formulas can also be found from no. 1 in the Appendix. From the formulas, we can see that

$$\boxed{R_1 = 6.306k\Omega, R_2 = 3.694k\Omega, C = 1.6nF, A_m = 1.59\frac{V}{V}}$$

Below is the phase and magnitude plot for our filter:

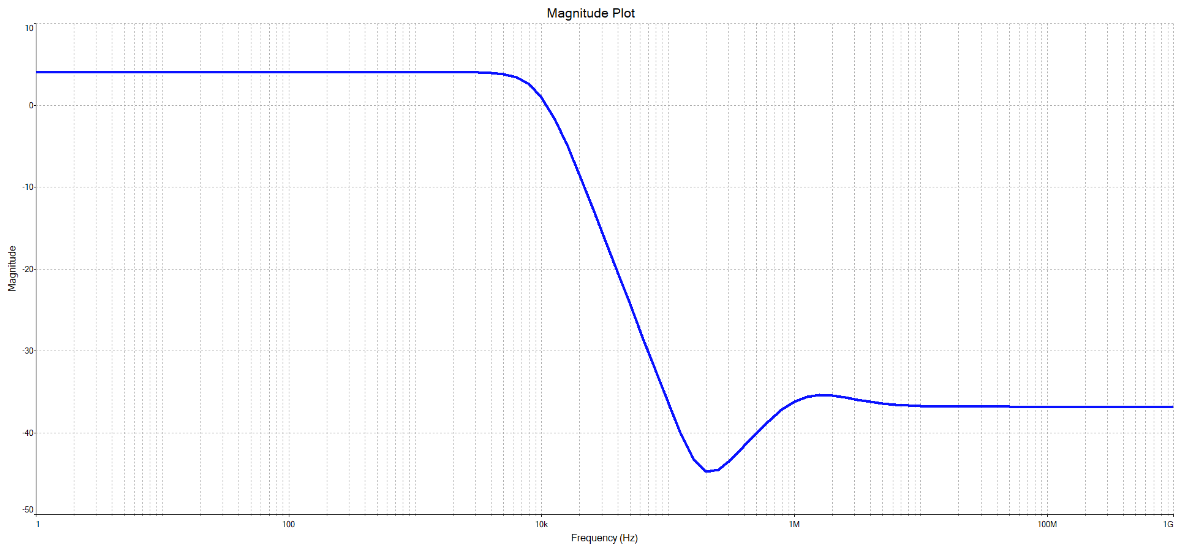

Figure 2: Bode Magnitude Plot

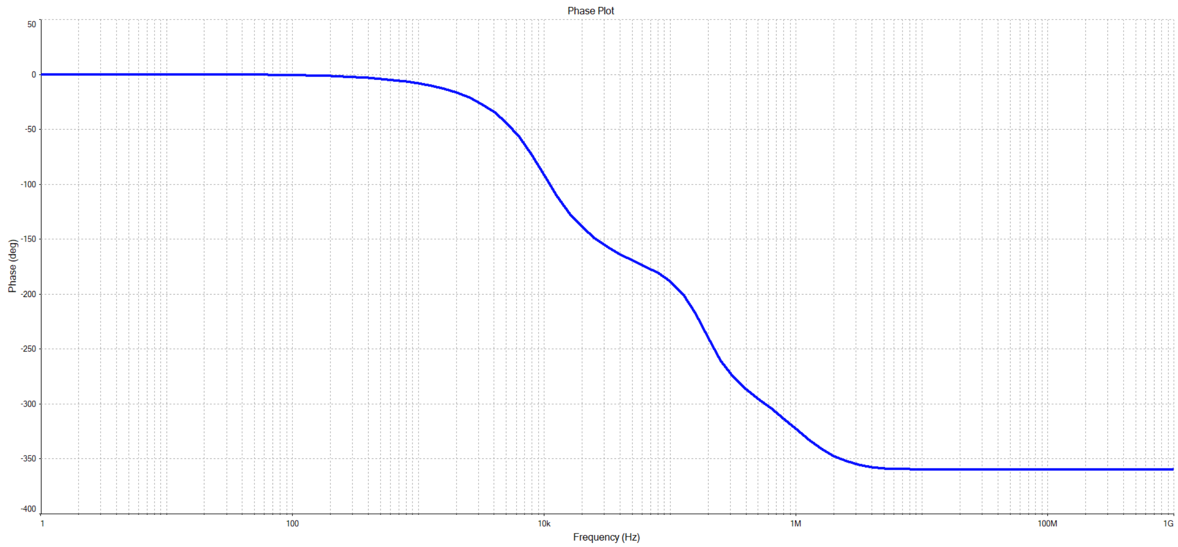

Figure 3: Bode Phase Plot

1.0.2 Part 2

For this part, we will be grounding the input, and measuring the output of the OpAmp. To determine the value of A_m when the circuit begins to oscillate, we need the transfer fucntion. The function is shown below, where R = $10k\Omega$ and C is the value found previously:

$$H(s) = A_M \frac{\frac{1}{(RC)^2}}{s^2 + s\frac{3-A_M}{RC} + \frac{1}{(RC)^2}}$$

Changing the values of the resistances, we find that the oscillations occour when the resistor values are around$R_1 = 3k\Omega$ and $R_2 = 7k\Omega$. The oscillation is shown below:

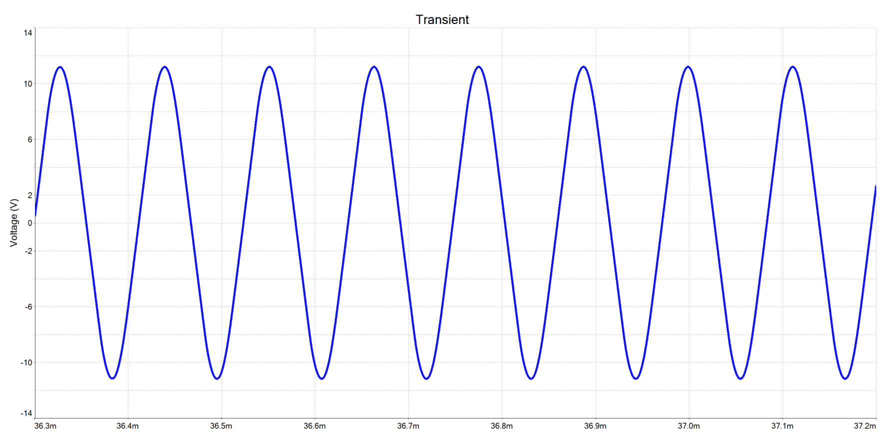

Figure 4: Oscillating Output

Using the cursors and measuring the differences between the crests of the plot, we find that the oscillating frequency is $\boxed{f_o = 8.93kHz}$. Below is the root locus plot:

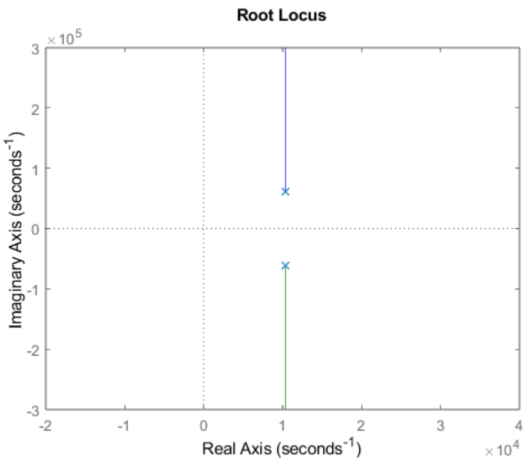

Figure 5: Unstable Root Locus Plot

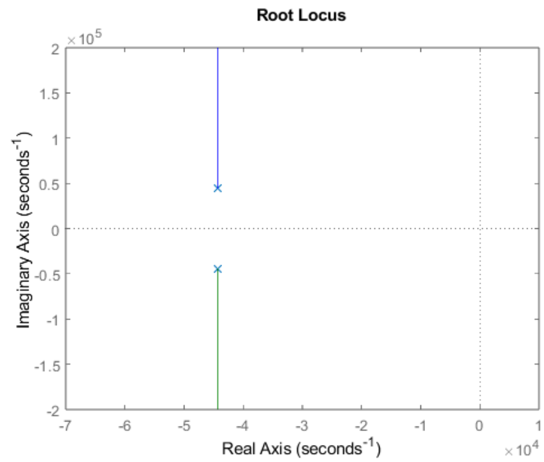

Figure 6: Stable Root Locus Plot

The root locus plot where $A_M > 3$ and $A_M < 3$ is Figure 5 and Figure 6 respectively. We can see from the plot that when the oscillations occour, A_M is greater than 3. This would then cause the system to be unstable as shown in Figure 5, since the poles are on the right side on the jw axis. When the poles are on the other side of the jw axis, the system is stable, and doesn't cause the output to oscillate. This happens when $A_M < 3$. The reason why it has to be less than 3 for the system to be stable is because of the characteristic equation in the transfer function. If A_M is equal or greater than 3, it causes one of the coefficients in the characteristic equation to be negative, causing instability in the system and thus, the oscillations occour.

2 Part B

Below is our phase shift oscillator circuit:

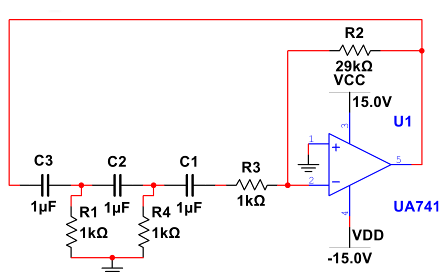

Figure 7: Phase Shift Oscillator Circuit

To find the proper value of the 29R resistor, we will simulate the output response for a very large amount of time (in this case, 1000 seconds was used). Simulating with just $29k\Omega$ we find that the output amplitude eventually decays to zero. Increasing the resistor to $29.1k\Omega$ makes the output amplitude to be sustained indefinitely.

Plotting the output of the circuit with the values given in the project document [2] and the increased 29R resistor , we find that the output oscillates as shown:

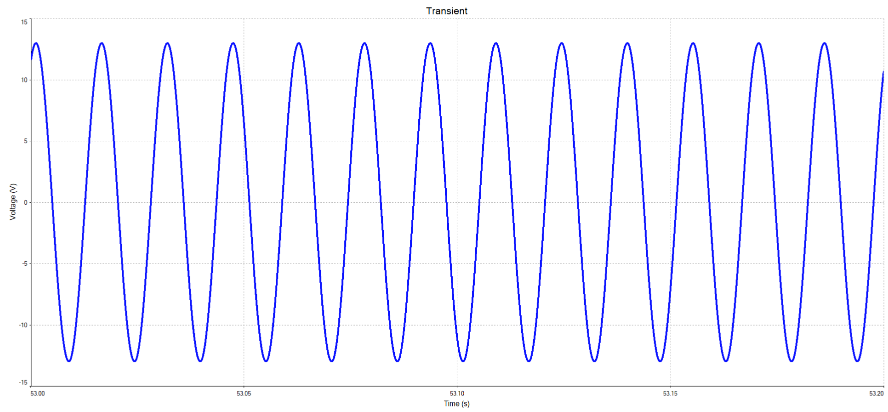

Figure 8: Circuit Output With Unchanged Circuit Elements

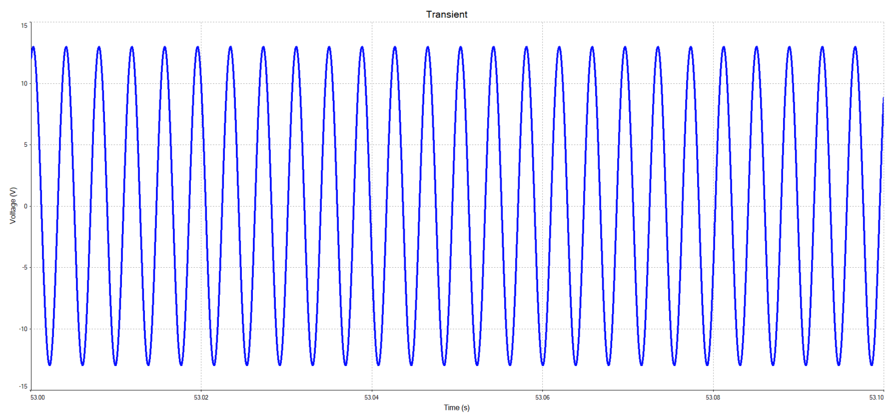

Figure 9: Circuit Output With Half Circuit Elements

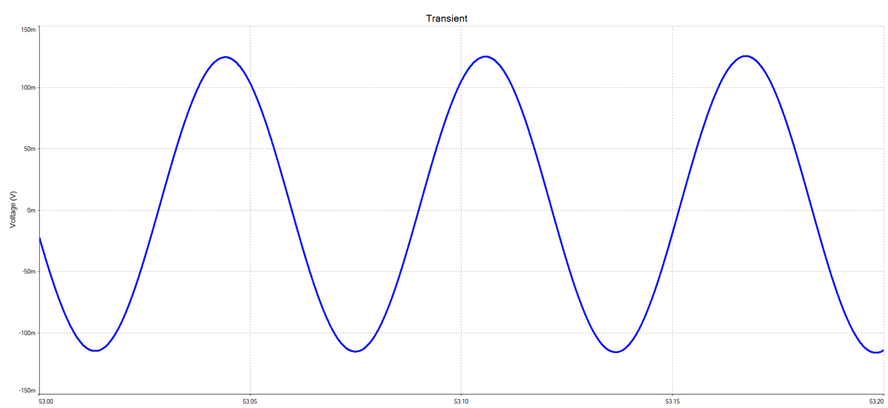

Figure 10: Circuit Output With Double Circuit Elements

From class notes[1], we can calculate the output frequency with:

$$f = \frac{1}{2\pi RC\sqrt{6}}$$

Comparing the calculated and measured frequencies for each modified circuit:

R and C Multiplier	0.5x	1x	2x
Calculated $f_o(Hz)$	259.899	64.975	16.244
Measured $f_o(Hz)$	259.067	64.872	16.221
% Error	0.32	0.15	0.14

Table 1: Calculated and Measured Frequencies

We can observe that the calculated and measured values are very close to each other.

3 Part C

Below is our Feedback Circuit:

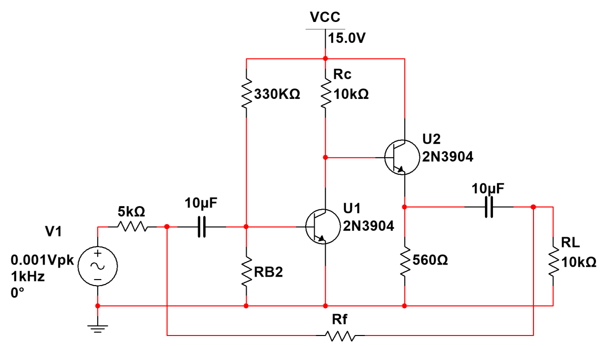

Figure 11: Feedback Circuit

Since we want to find the value of R_{B2} that creates the largest open-loop gain at 1kHz, we will be setting our input voltage at 1kHz at 1mV. To find the value, we will be doing a parameter sweep with a transient response to find which resistance value results in the largest gain. We find that after doing the parameter sweep, the value R_{B2} to be around $\boxed{20k\Omega}$. Resistance values above $20k\Omega$, we find that the output amplitude decreases. The paramater sweep is shown below:

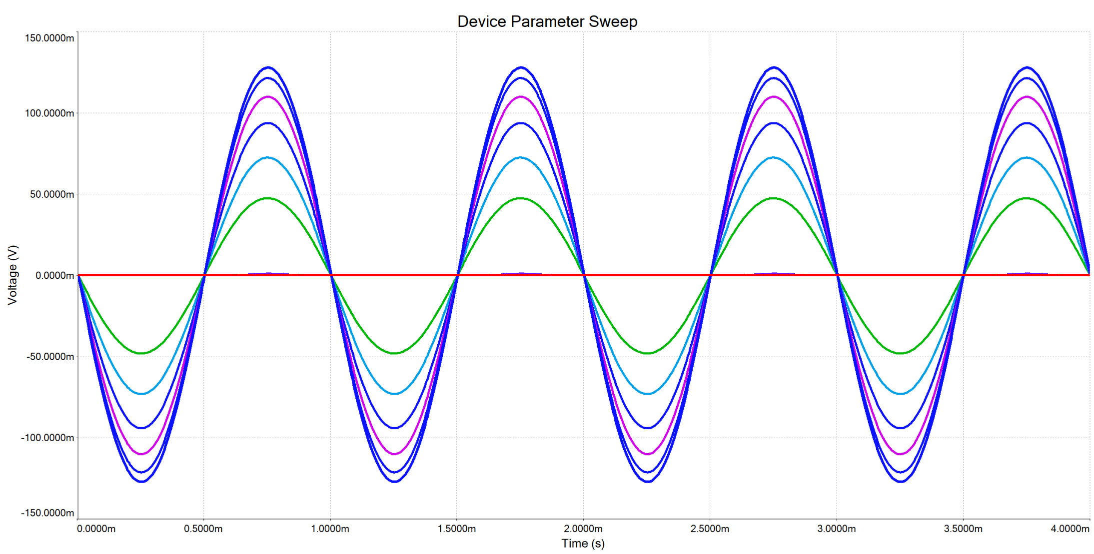

Figure 12: Parameter Sweep with R_{B2} and Transient Response

3.1 Part 1

To measure the DC Operating points, we will be using the DC Circuit below:

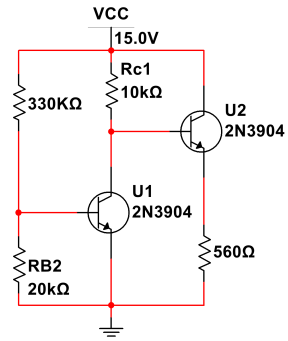

Figure 13: Feedback DC Circuit

For this project, we will be assuming that V_T= 0.025V. We can use these formulas to calculate our transistor parameters:

$$r_\pi = \frac{V_T}{I_B}, \beta = \frac{I_C}{I_B} \text{ and } g_m = \frac{\beta}{r_\pi}$$

Here are the DC Operating points, with our measured r_π, g_m, and h_{FE}:

	I_C	I_B	I_E	V_C	V_B	V_E	g_m	r_π	h_{FE}
Q1	1.29mA	10.8uA	1.31mA	1.90V	0.654V	0V	0.05	2.315kΩ	119
Q2	2.19mA	15.4uA	2.21mA	15V	1.90V	1.23V	0.09	1.623kΩ	142

Table 2: DC Operating Points

3.2 Part 2

Here is the open loop frequency response plot: Measuring with the cursors in our simulation software,

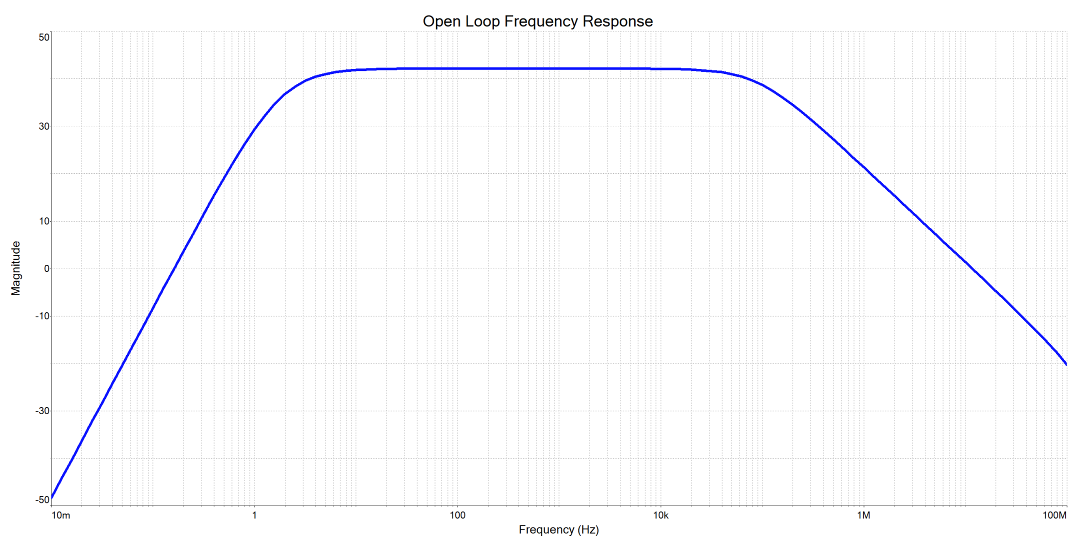

Figure 14: Open Loop Frequency Response

we find that the 3dB points are:

$$\boxed{w_{L3dB} = 2.9219 * 2\pi[\frac{rad}{s}], w_{H3dB} = 91.3083 * 2\pi * 10^3[\frac{rad}{s}]}$$

We can also find our mid-band gain $\boxed{A_m = 127.557\frac{V}{V}}$. In order to find the input and output resistance at 1kHz, we will be measuring the voltage and current at the input and output. We will measure the short circuit current and open circuit voltage for the output impedance. After measuring, we find:

$$R_{in} = \frac{240\mu V}{93.4nA} = \boxed{2.569k\Omega}, R_{out} = \frac{1.24V}{1.39mA} = \boxed{892.1\Omega}$$

4 Appendix

$$k = 3 - \sqrt(2) \tag{1}$$

$$\tag{2}$$

5 References

1. ELEC 301 Class notes
2. Mini Project 4 Document
3. Standard Resistor and Capacitor Values (Canvas)
4. Circuit Maker SPICE Model