An experimental analysis of loop pipelining techniques on SIMD-like architectures *

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Abstract

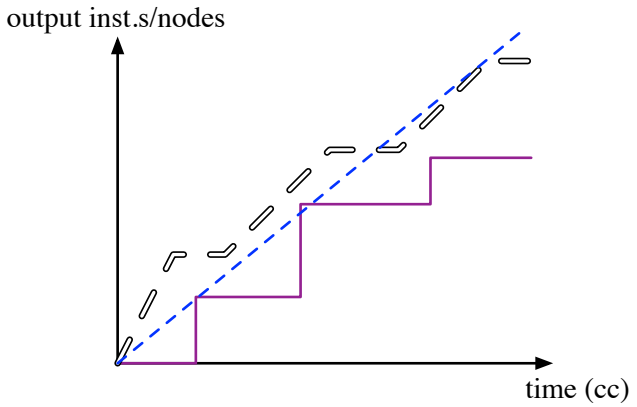

Figure 1: Burstiness

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