Base Instructions	SIMD&FP Instructions	<u>SVE</u> <u>Instructions</u>	SME Instructions	Index by Encoding
GCSB DSYNC				
Guarded Control Stack Barrier. This instruction generates a Guarded control stack data synchronization event.  If FEAT_GCS is not implemented, this instruction executes as a NOP.				
System (FEAT_GCS)				
31 30 29 28 27 26 25 1 1 0 1 0 1 0	24 23 22 21 20 19 18 1 1 0 0 0 0 0 0 0 0	1 1 0 0 1 0 0 0	0 9 8 7 6 5 4 3 0 1 0 0 1 1 1 1 Rm op2	2 1 0 1 1 1
GCSB DSYNC				
<pre>if !IsFeatureImplemented(FEAT_GCS) then EndOfInstruction();</pre>				
Operation				
<pre>GCSSynchronizationBarrier();</pre>				

Internal version only: is a v33.64, AdvSIMD v29.12, pseudocode no\_diffs\_2023\_09\_RC2, sve v2023-06\_rel ; Build timestamp: 2023-09-18T17:56

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