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## LDUMAXH, LDUMAXAH, LDUMAXALH, LDUMAXLH

Atomic unsigned maximum on halfword in memory atomically loads a 16-bit halfword from memory, compares it against the value held in a register, and stores the larger value back to memory, treating the values as unsigned numbers. The value initially loaded from memory is returned in the destination register.

- If the destination register is not WZR, LDUMAXAH and LDUMAXALH load from memory with acquire semantics.
- LDUMAXLH and LDUMAXALH store to memory with release semantics.
- LDUMAXH has neither acquire nor release semantics.

For more information about memory ordering semantics, see *Load-Acquire*, *Store-Release*.

For information about memory accesses, see *Load/Store addressing modes*. This instruction is used by the alias <u>STUMAXH</u>, <u>STUMAXLH</u>.

```
Integer (FEAT_LSE)

31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

0 1 1 1 1 1 0 0 0 A R 1 Rs 0 1 1 0 0 0 Rn Rt

size opc

LDUMAXAH (A == 1 && R == 0)

LDUMAXAH (A == 1 && R == 1)

LDUMAXALH (Ws>, <Wt>, [<Xn | SP>]
```

```
LDUMAXH (A == 0 && R == 0)
```

```
LDUMAXH <Ws>, <Wt>, [<Xn | SP>]
```

```
LDUMAXLH (A == 0 \&\& R == 1)
```

```
LDUMAXLH <Ws>, <Wt>, [<Xn SP>]

if !IsFeatureImplemented(FEAT_LSE) then UNDEFINED;

integer t = UInt(Rt);
integer n = UInt(Rn);
integer s = UInt(Rs);
```

```
boolean acquire = A == '1' && Rt != '11111';
boolean release = R == '1';
boolean tagchecked = n != 31;
```

#### **Assembler Symbols**

<ws></ws>	Is the 32-bit name of the general-purpose register holding the data value to be operated on with the contents of the memory location, encoded in the "Rs" field.
<wt></wt>	Is the 32-bit name of the general-purpose register to be loaded, encoded in the "Rt" field.
<xn sp></xn sp>	Is the 64-bit name of the general-purpose base register or

stack pointer, encoded in the "Rn" field.

# Alias Conditions

Alias	Is preferred when			
STUMAXH, STUMAXLH	A == '0' && Rt == '11111'			

### **Operation**

## **Operational information**

If PSTATE.DIT is 1, the timing of this instruction is insensitive to the value of the data being loaded or stored.

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Internal version only: isa v33.64, AdvSIMD v29.12, pseudocode no diffs 2023 09 RC2, sve v2023-06 rel ; Build timestamp: 2023-09-18T17:56

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