GITS_SGIR, ITS SGI Register

The GITS_SGIR characteristics are:

Purpose

Written by software to signal a virtual SGI for translation by the ITS.

Configuration

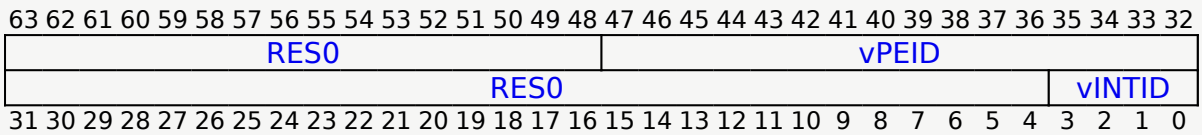
This register is present only when FEAT_GICv4p1 is implemented. Otherwise, direct accesses to GITS_SGIR are res0.

This register is provided only in FEAT_GICv4p1 implementations.

Attributes

GITS_SGIR is a 64-bit register.

Field descriptions



Bits [63:48]

Reserved, res0.

vPEID, bits [47:32]

ID of target vPEID.

The size of this field is implementation defined, and is specified by the GICD_TYPER2.VIL and GICD_TYPER2.VID fields. Unimplemented bits are res0.

Bits [31:4]

Reserved, res0.

vINTID, bits [3:0]

INTID of virtual SGI.

Accessing GITS_SGIR

64-bit access only.

GITS_SGIR can be accessed through the memory-mapped interfaces:

Component	Offset
GIC ITS control	0x20020

Accesses on this interface are **WO**.

28/03/2023 16:01; 72747e43966d6b97dcbd230a1b3f0421d1ea3d94