CTICHOUTSTATUS, CTI Channel Out Status register

The CTICHOUTSTATUS characteristics are:

Purpose

Provides the status of the ECT channel outputs from the CTI.

Configuration

CTICHOUTSTATUS is in the Debug power domain.

Attributes

CTICHOUTSTATUS is a 32-bit register.

Field descriptions

31 30 29 28 27 26 25 24 23 22 CHOUT31CHOUT30CHOUT29CHOUT28CHOUT27CHOUT26CHOUT25CHOUT24CHOUT23CHOUT22C

CHOUT<n>, bit [n], for n = 31 to 0

Output channel <n> status.

Bits [31:N] are RAZ. N is the number of ECT channels implemented as defined by the CTIDEVID.NUMCHAN field.

Possible values of this bit are:

| CHOUT <n></n> | Meaning |
|---------------|-------------------------------------|
| 0b0 | Output channel <n> is inactive.</n> |
| 0b1 | Output channel <n> is active.</n> |

If the ECT channels do not support multicycle events then it is implementation defined whether an output channel can be observed as active.

Note

The value in CTICHOUTSTATUS is after gating by the channel gate. For more information, see CTIGATE.

Accessing CTICHOUTSTATUS

CTICHOUTSTATUS can be accessed through the external debug interface:

| Component | Offset | Instance | |
|-----------|--------|----------------|--|
| CTI | 0x13C | CTICHOUTSTATUS | |

Accesses on this interface are **RO**.

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