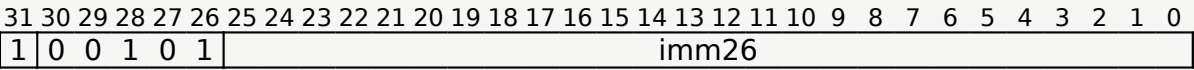


**BL**

Branch with Link branches to a PC-relative offset, setting the register X30 to PC+4. It provides a hint that this is a subroutine call.



op

**BL** [<label>](#)

```
bits(64) offset = SignExtend(imm26:'00', 64);
```

**Assembler Symbols**

[<label>](#) Is the program label to be unconditionally branched to. Its offset from the address of this instruction, in the range +/-128MB, is encoded as "imm26" times 4.

**Operation**

```
if IsFeatureImplemented(FEAT_GCS) && GCSPCREnabled(PSTATE.EL) then
    AddGCSRecord(PC64 + 4);
X[30, 64] = PC64 + 4;

BranchTo(PC64 + offset, BranchType\_DIRCALL, FALSE);
```