

## AMCIDR3, Activity Monitors Component Identification Register 3

The AMCIDR3 characteristics are:

### Purpose

Provides information to identify an activity monitors component.

For more information, see 'About the Component identification scheme'.

### Configuration

It is implementation defined whether AMCIDR3 is implemented in the Core power domain or in the Debug power domain.

Implementation of this register is optional.

This register is present only when FEAT\_AMUv1 is implemented.

### Attributes

AMCIDR3 is a 32-bit register.

### Field descriptions

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RES0																PRMBL 3															

#### Bits [31:8]

Reserved, res0.

#### PRMBL\_3, bits [7:0]

Preamble.

Reads as 0xB1.

Access to this field is **RO**.

# Accessing AMCIDR3

AMCIDR3 can be accessed through the memory-mapped interfaces:

Component	Offset	Instance
AMU	0xFFC	AMCIDR3

Accesses on this interface are **RO**.

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