

## EDPIDR3, External Debug Peripheral Identification Register 3

The EDPIDR3 characteristics are:

### Purpose

Provides information to identify an external debug component.

For more information, see 'About the Peripheral identification scheme'.

### Configuration

When FEAT\_DoPD is implemented, EDPIDR3 is in the Core power domain. Otherwise, EDPIDR3 is in the Debug power domain.

Implementation of this register is optional.

This register is required for CoreSight compliance.

### Attributes

EDPIDR3 is a 32-bit register.

### Field descriptions

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RES0																REVAND				CMOD											

#### Bits [31:8]

Reserved, res0.

#### REVAND, bits [7:4]

Part minor revision. Parts using [EDPIDR2](#).REVISION as an extension to the Part number must use this field as a major revision number.

This field has an implementation defined value.

Access to this field is **RO**.

#### CMOD, bits [3:0]

Customer modified. Indicates someone other than the Designer has modified the component.

This field has an implementation defined value.

Access to this field is **RO**.

## Accessing EDPIDR3

**EDPIDR3 can be accessed through the external debug interface:**

Component	Offset	Instance
Debug	0xFEC	EDPIDR3

This interface is accessible as follows:

- When FEAT\_DoPD is not implemented or IsCorePowered(), accesses to this register are **RO**.
- Otherwise, accesses to this register generate an error response.

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