AArch32AArch64AArch32AArch64Index byExternalRegistersRegistersInstructionsInstructionsEncodingRegisters

GICR_TYPER, Redistributor Type Register

The GICR TYPER characteristics are:

Purpose

Provides information about the configuration of this Redistributor.

Configuration

A copy of this register is provided for each Redistributor.

Attributes

GICR TYPER is a 64-bit register.

Field descriptions

6362616059 58 57 56 55545352515049484746454443424140 39 38 37 36 35 34 33

Affinity_Value

PPInum VSGICommonLPIAfProcessor_Number RVPEIDMPAMDPGSLastDirectLPIDirtyVLPIS

3130292827 26 25 24 2322212019181716151413121110 9 8 7 6 5 4 3 2 1

Affinity Value, bits [63:32]

The identity of the PE associated with this Redistributor.

Bits [63:56] provide Aff3, the Affinity level 3 value for the Redistributor.

Bits [55:48] provide Aff2, the Affinity level 2 value for the Redistributor.

Bits [47:40] provide Aff1, the Affinity level 1 value for the Redistributor.

Bits [39:32] provide Aff0, the Affinity level 0 value for the Redistributor.

PPInum, bits [31:27] When FEAT GICv3p1 is implemented:

The value derived from this field specifies the maximum PPI INTID that a GIC implementation can support. An implementation might not implement all PPIs up to this maximum.

PPInum	Meaning
0000000	Maximum PPI INTID is 31.
0b00001	Maximum PPI INTID is 1087.
0b00010	Maximum PPI INTID is 1119.

All other values are reserved.

Otherwise:

Reserved, res0.

VSGI, bit [26] When FEAT_GICv4p1 is implemented:

Indicates whether vSGIs are supported.

VSGI	Meaning
0b0	Direct injection of SGIs not
	supported.
0b1	Direct injection of SGIs
	supported.

Otherwise:

Reserved, res0.

CommonLPIAff, bits [25:24]

Indicates the scope of the CommonLPIAff group.

CommonLPIAff	Meaning
0000	All Redistributors are
	members of the same CommonLPIAff group.
0b01	All Redistributors with the same Aff3 value are members of the same CommonLPIAff
0b10	group. All Redistributors with the same Aff3.Aff2 value are members of the same CommonLPIAff group.

0b11	All Redistributors with the same
	Aff3.Aff2.Aff1 value
	are members of the
	same CommonLPIAff
	group.

Redistributors in the same CommonLPIAff group must use the same copy of the LPI Configuration table, and if GICv4.1 is implemented the same copy of the vPE Configuration table.

Processor Number, bits [23:8]

A unique identifier for the PE. When <u>GITS_TYPER</u>.PTA == 0, an ITS uses this field to identify the interrupt target.

When affinity routing is disabled for a Security state, this field indicates which <u>GICD_ITARGETSR<n></u> corresponds to this Redistributor.

RVPEID, bit [7] When FEAT GICv4p1 is implemented:

Indicates how the resident vPE is specified.

RVPEID	Meaning
0b0	GICR_VPENDBASER records
	the address of the vPE's
	Virtual Pending Table.
0b1	GICR_VPENDBASER records
	vPEID.

Otherwise:

Reserved, res0.

MPAM, bit [6] When FEAT GICv3p1 is implemented:

MPAM

MPAM	Meaning
0b0	MPAM not supported.
0b1	MPAM supported.

Otherwise:

Reserved, res0.

DPGS, bit [5]

Sets support for GICR CTLR.DPG* bits.

DPGS	Meaning
0b0	GICR_CTLR.DPG* bits are not
	supported.
0b1	<pre>GICR_CTLR.DPG* bits are</pre>
	supported.

Last, bit [4]

Indicates whether this Redistributor is the highest-numbered Redistributor in a series of contiguous Redistributor pages.

Last	Meaning
0b0	This Redistributor is not the
	highest-numbered Redistributor in
	a series of contiguous
	Redistributor pages.
0b1	This Redistributor is the highest-
	numbered Redistributor in a
	series of contiguous Redistributor
	pages.

DirectLPI, bit [3]

Indicates whether this Redistributor supports direct injection of LPIs.

DirectLPI	Meaning
0b0	This Redistributor does not support direct injection of LPIs. The
	GICR_SETLPIR, GICR_CLRLPIR, GICR_INVLPIR, GICR_INVALI
	and GICR SYNCR registers are either not implemented, or hav
	implementation defined purpose.
0b1	This Redistributor supports direct injection of LPIs. The
	GICR SETLPIR, GICR CLRLPIR, GICR INVLPIR, GICR INVAL
	and <u>GICR_SYNCR</u> registers are implemented.

Dirty, bit [2]

Controls the functionality of **GICR VPENDBASER**.Dirty.

Dirty	Meaning
0b0	GICR_VPENDBASER.Dirty is
	unknown when
	\underline{GICR} $\underline{VPENDBASER}$. $\underline{Valid} == 1$.

0b1	GICR VPENDBASER .Dirty
	indicates when the Virtual
	Pending Table has been parsed
	when GICR VPENDBASER . Valid
	is written from 0 to 1.

When $GICR_TYPER.VLPIS == 0$, this field is res0.

Note

In GICv4p1 implementations this field is res1.

VLPIS, bit [1]

Indicates whether the GIC implementation supports virtual LPIs and the direct injection of virtual LPIs.

VLPIS	Meaning
0b0	The implementation does not
	support virtual LPIs or the
	direct injection of virtual LPIs.
0b1	The implementation supports
	virtual LPIs and the direct
	injection of virtual LPIs.

Note

In GICv3 implementations this field is res0.

PLPIS, bit [0]

Indicates whether the GIC implementation supports physical LPIs.

PLPIS	Meaning		
0b0	The implementation does not		
	support physical LPIs.		
0b1	The implementation supports		
	physical LPIs.		

Accessing GICR_TYPER

GICR_TYPER can be accessed through the memory-mapped interfaces:

Component	Frame	Offset	Instance
GIC Redistributor	RD_base	0x0008	GICR_TYPER

Accesses on this interface are **RO**.

AArch32AArch64AArch32AArch64Index byExternalRegistersRegistersInstructionsInstructionsEncodingRegisters

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