 	<u>Base</u>	SIMD&FP	<u>SVE</u>	SME	<u>Index by</u>
	<u>Instructions</u>	Instructions	<u>Instructions</u>	Instructions	<u>Encoding</u>

Pseu

Sh Pseu

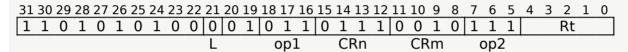
TRCIT

Trace Instrumentation generates an instrumentation trace packet that contains the value of the provided register.

This is an alias of SYS. This means:

- The encodings in this description are named to match the encodings of SYS.
- The description of <u>SYS</u> gives the operational pseudocode, any constrained unpredictable behavior, and any operational information for this instruction.

System (FEAT_ITE)



TRCIT <Xt>

is equivalent to

and is always the preferred disassembly.

Assembler Symbols

< Xt >

Is the 64-bit name of the general-purpose source register, encoded in the "Rt" field.

Operation

The description of <u>SYS</u> gives the operational pseudocode for this instruction.

<u>Base</u>	SIMD&FP	<u>SVE</u>	<u>SME</u>	Index by
Instructions	Instructions	Instructions	Instructions	Encoding

Internal version only: isa v33.64, AdvSIMD v29.12, pseudocode no diffs 2023 09 RC2, sve v2023-06 rel ; Build timestamp: 2023-09-18T17:56

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