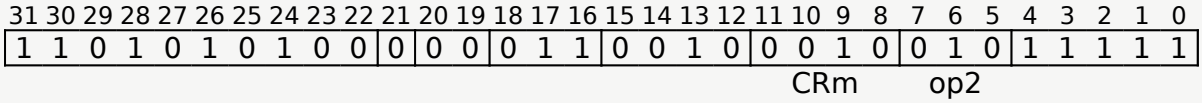
TSB CSYNC

Trace Synchronization Barrier. This instruction is a barrier that synchronizes the trace operations of instructions, see *Trace Synchronization Buffer (TSB CSYNC)*.

If *FEAT_TRF* is not implemented, this instruction executes as a `NOP`.

System
(FEAT_TRF)



TSB CSYNC

```
if !IsFeatureImplemented(FEAT_TRF) then EndOfInstruction();
```

Operation

```
TraceSynchronizationBarrier();
```


Internal version only: isa v33.64, AdvSIMD v29.12, pseudocode no_diffs_2023_09_RC2, sve v2023-06_rel ; Build timestamp: 2023-09-18T17:56