

# AMDEVTYPE, Activity Monitors Device Type Register

The AMDEVTYPE characteristics are:

## Purpose

Indicates to a debugger that this component is part of a PE's performance monitor interface.

## Configuration

It is implementation defined whether AMDEVTYPE is implemented in the Core power domain or in the Debug power domain.

Implementation of this register is optional.

This register is present only when FEAT\_AMUv1 is implemented.

## Attributes

AMDEVTYPE is a 32-bit register.

## Field descriptions

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RES0																								SUB		MAJOR					

### Bits [31:8]

Reserved, res0.

### SUB, bits [7:4]

Subtype.

SUB	Meaning
0b0001	Component within a PE.

Access to this field is **RO**.

### MAJOR, bits [3:0]

Major type.

MAJOR	Meaning
0b0110	Performance monitor component

Access to this field is **RO**.

## Accessing AMDEVTTYPE

AMDEVTTYPE can be accessed through the memory-mapped interfaces:

Component	Offset	Instance
AMU	0xFCC	AMDEVTTYPE

Accesses on this interface are **RO**.

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<a href="#">AArch32 Registers</a>	<a href="#">AArch64 Registers</a>	<a href="#">AArch32 Instructions</a>	<a href="#">AArch64 Instructions</a>	<a href="#">Index by Encoding</a>	<a href="#">External Registers</a>
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