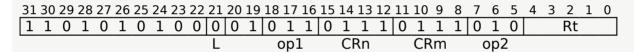## GCSSS1

Guarded Control Stack Switch Stack 1 validates that the stack being switched to contains a Valid cap entry, stores an In-progress cap entry to the stack that is being switched to, and sets the current Guarded control stack pointer to the stack that is being switched to.

This is an alias of [SYS](). This means:

- The encodings in this description are named to match the encodings of [SYS]().
- The description of [SYS]() gives the operational pseudocode, any constrained unpredictable behavior, and any operational information for this instruction.

System
(FEAT_GCS)

31 30 29 28 27 26 25 24 23 22	21	20 19	18 17 16	15 14 13 12	11 10 9 8	7 6 5	4 3 2 1 0
1 1 0 1 0 1 0 1 0 0	0	0 1	0 1 1	0 1 1 1	0 1 1 1	0 1 0	Rt
	L	op1	CRn	CRm	op2		

        GCSSS1 **<Xt>**

   is equivalent to

        SYS #3, C7, C7, #2, **<Xt>**

   and is always the preferred disassembly.

Assembler Symbols

<Xt>   Is the 64-bit name of the general-purpose source register, encoded in the "Rt" field.

Operation

The description of [SYS]() gives the operational pseudocode for this instruction.

Internal version only: isa v33.64, AdvSIMD v29.12, pseudocode no_diffs_2023_09_RC2, sve v2023-06_rel ; Build timestamp: 2023-09-18T17:56