<u>Base</u>	SIMD&FP	<u>SVE</u>	<u>SME</u>	Index by
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## **BRK**

## BRK #<imm>

```
if IsFeatureImplemented(FEAT_BTI) then
SetBTypeCompatible(TRUE);
```

## **Assembler Symbols**

<imm>

Is a 16-bit unsigned immediate, in the range 0 to 65535, encoded in the "imm16" field.

## **Operation**

```
AArch64.SoftwareBreakpoint(imm16);
```

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 $Internal\ version\ only: is a\ v33.64,\ AdvSIMD\ v29.12,\ pseudocode\ no\_diffs\_2023\_09\_RC2,\ sve\ v2023-06\_rel\ ;\ Build\ timestamp:\ 2023-09-18T17:56$ 

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Sh Pseu

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