

PMDEVTYPE, Performance Monitors Device Type register

The PMDEVTYPE characteristics are:

Purpose

Indicates to a debugger that this component is part of a PE's performance monitor interface.

Configuration

This register is present only when FEAT_PMUv3_EXT32 is implemented and an implementation implements PMDEVTYPE. Otherwise, direct accesses to PMDEVTYPE are res0.

If FEAT_DoPD is implemented, this register is in the Core power domain. If FEAT_DoPD is not implemented, this register is in the Debug power domain.

Attributes

PMDEVTYPE is a 32-bit register.

This register is part of the [PMU](#) block.

Field descriptions

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RES0																SUB				MAJOR											

Bits [31:8]

Reserved, res0.

SUB, bits [7:4]

Subtype. Indicates this is a component within a PE.

Reads as 0b0001.

Access to this field is **RO**.

MAJOR, bits [3:0]

Major type. Indicates this is a performance monitor component.

Reads as 0b0110.

Access to this field is **RO**.

Accessing PMDEVTYPE

Accesses to this register use the following encodings:

Accessible at offset 0xFCC from PMU

- When FEAT_DoPD is implemented and !IsCorePowered(), accesses to this register generate an error response.
- Otherwise, accesses to this register are **RO**.

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