ERRPIDR2, Peripheral Identification Register2

The ERRPIDR2 characteristics are:

Purpose

Provides discovery information about the component.

For more information, see 'About the Peripheral identification scheme'.

Configuration

Implementation of this register is optional.

ERRPIDR2 is implemented only as part of a memory-mapped group of error records.

Attributes

ERRPIDR2 is a 32-bit register.

Field descriptions

When the component uses a 12-bit part number:

31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

RESO | REVISION| | EDEC | DES_1 |

Bits [31:8]

Reserved, res0.

REVISION, bits [7:4]

Component major revision. ERRPIDR2.REVISION and <u>ERRPIDR3</u>.REVAND together form the revision number of the component, with ERRPIDR2.REVISION being the most significant part and <u>ERRPIDR3</u>.REVAND the least significant part. When a component is changed, ERRPIDR2.REVISION or <u>ERRPIDR3</u>.REVAND are increased to ensure that software can differentiate the different revisions of the component. <u>ERRPIDR3</u>.REVAND should be set to <code>0b0000</code> when ERRPIDR2.REVISION is increased.

This field has an implementation defined value.

Access to this field is **RO**.

JEDEC, bit [3]

JEDEC-assigned JEP106 implementer code is used.

Reads as 0b1.

Access to this field is **RO**.

DES 1, bits [2:0]

Designer, JEP106 identification code, bits [6:4]. <u>ERRPIDR1</u>.DES_0 and ERRPIDR2.DES_1 together form the JEDEC-assigned JEP106 identification code for the designer of the component. The parity bit in the JEP106 identification code is not included. The code identifies the designer of the component, which might not be not the same as the implementer of the device containing the component. To obtain a number, or to see the assignment of these codes, contact JEDEC http://www.jedec.org.

Note

For a component designed by Arm Limited, the JEP106 identification code is 0x3B.

This field has an implementation defined value.

Access to this field is **RO**.

When the component uses a 16-bit part number:

<u>31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8</u>	7	6	5	4	3	2	1	0
RES0		PAF	RT	2	JEDEC	DE	ES	1

Bits [31:8]

Reserved, res0.

PART 2, bits [7:4]

Part number, bits [15:12].

The part number is selected by the designer of the component. The designer chooses whether to use a 12-bit or a 16-bit part number:

 If a 12-bit part number is used, then it is stored in <u>ERRPIDR1</u>.PART 1 and <u>ERRPIDR0</u>.PART 0. There are 8 bits, ERRPIDR2.REVISION and <u>ERRPIDR3</u>.REVAND, available to define the revision of the component.

• If a 16-bit part number is used, then it is stored in ERRPIDR2.PART_2, <u>ERRPIDR1</u>.PART_1 and <u>ERRPIDR0</u>.PART_0. There are 4 bits, <u>ERRPIDR3</u>.REVISION, available to define the revision of the component.

This field has an implementation defined value.

Access to this field is **RO**.

JEDEC, bit [3]

JEDEC-assigned JEP106 implementer code is used.

Reads as 0b1.

Access to this field is **RO**.

DES 1, bits [2:0]

Designer, JEP106 identification code, bits [6:4]. <u>ERRPIDR1</u>.DES_0 and ERRPIDR2.DES_1 together form the JEDEC-assigned JEP106 identification code for the designer of the component. The parity bit in the JEP106 identification code is not included. The code identifies the designer of the component, which might not be not the same as the implementer of the device containing the component. To obtain a number, or to see the assignment of these codes, contact JEDEC http://www.jedec.org.

Note

For a component designed by Arm Limited, the IEP106 identification code is 0x3B.

This field has an implementation defined value.

Access to this field is **RO**.

Accessing ERRPIDR2

ERRPIDR2 can be accessed through the memory-mapped interfaces:

Component	Offset	Instance
RAS	0xFE8	ERRPIDR2

Accesses on this interface are **RO**.

AArch32AArch64AArch32AArch64Index byExternalRegistersRegistersInstructionsInstructionsEncodingRegisters

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