# MSMON\_CFG\_MBWU\_FLT, MPAM Memory System Monitor Configure Memory Bandwidth Usage Monitor Filter Register

The MSMON CFG MBWU FLT characteristics are:

## **Purpose**

Controls PARTID and PMG to measure or count in the MBWU monitor selected by MSMON CFG MON SEL.

MSMON\_CFG\_MBWU\_FLT\_s sets filter conditions for the Secure memory bandwidth usage monitor instance selected by the Secure instance of <a href="MSMON\_CFG\_MON\_SEL">MSMON\_CFG\_MBWU\_CTL\_ns</a> sets filter conditions for the Non-secure memory bandwidth usage monitor instance selected by the Non-secure instance of <a href="MSMON\_CFG\_MON\_SEL">MSMON\_CFG\_CSU\_FLT\_rt</a> sets the filter conditions for the Root PARTID selected by the Root instance of <a href="MSMON\_CFG\_MON\_SEL">MSMON\_CFG\_CSU\_FLT\_rl</a> sets the filter conditions for the Realm PARTID selected by the Realm instance of <a href="MSMON\_CFG\_MON\_SEL">MSMON\_CFG\_CSU\_FLT\_rl</a> sets the filter conditions for the Realm PARTID selected by the Realm instance of <a href="MSMON\_CFG\_MON\_SEL">MSMON\_CFG\_MON\_SEL</a>.

If <u>MPAMF\_IDR</u>.HAS\_RIS is 1, the monitor instance filter configuration accessed is for the resource instance currently selected by <u>MSMON\_CFG\_MON\_SEL</u>.RIS and the monitor instance of that resource instance selected by <u>MSMON\_CFG\_MON\_SEL</u>.MON\_SEL.

## **Configuration**

This register is present only when FEAT\_MPAM is implemented, MPAMF\_IDR.HAS\_MSMON == 1 and MPAMF\_MSMON\_IDR.MSMON\_MBWU == 1. Otherwise, direct accesses to MSMON\_CFG\_MBWU\_FLT are res0.

The power and reset domain of each MSC component is specific to that component.

### **Attributes**

MSMON\_CFG\_MBWU\_FLT is a 32-bit register.

# Field descriptions

# When FEAT\_MPAMv0p1 is implemented or FEAT\_MPAMv1p1 is implemented:

31 30 29 2	28 27 26 25 24	23 22 21 20 19 18 17 16	15 14 13 12 11 10 9	8	7 6	5 5	4	3	2	1	0
RWBW	RES0	PMG	F	AR	TID						

RW filtering.

# RWBW, bits [31:30] When MPAMF MBWUMON IDR.HAS RWBW == 1:

Read/write bandwidth filter. Configures the selected monitor instance to count all bandwidth, only read bandwidth or only write bandwidth.

RWBW	Meaning
0b00	Monitor instance counts read
	bandwidth and write
	bandwidth.
0b01	Monitor instance counts write
	bandwidth only.
0b10	Monitor instance counts read
	bandwidth only.
0b11	Reserved.

#### Otherwise:

Reserved, res0.

#### Bits [29:24]

Reserved, res0.

#### PMG, bits [23:16]

Performance monitoring group to filter memory bandwidth usage monitoring.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PMG == 0, this field is not used to match memory bandwidth to a PMG and the contents of this field is ignored.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PMG == 1, the monitor selected by <u>MSMON\_CFG\_MON\_SEL</u> measures or counts memory bandwidth labeled with PMG equal to this field.

#### **PARTID**, bits [15:0]

Partition ID to filter memory bandwidth usage monitoring.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PARTID == 0, this field is not used to match memory bandwidth to a PARTID and the contents of this field is ignored.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PARTID == 1, the monitor selected by <u>MSMON\_CFG\_MON\_SEL</u> measures or counts memory bandwidth labeled with PARTID equal to this field.

#### Otherwise:

31 30 29 28 27 26 25 24	23 22 21 20 19 18 17 16	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 (
RES0	PMG	PARTID

#### Bits [31:24]

Reserved, res0.

#### PMG, bits [23:16]

Performance monitoring group to filter memory bandwidth usage monitoring.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PMG == 0, this field is not used to match memory bandwidth to a PMG and the contents of this field is ignored.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PMG == 1, the monitor selected by <u>MSMON\_CFG\_MON\_SEL</u> measures or counts memory bandwidth labeled with PMG equal to this field.

#### **PARTID**, bits [15:0]

Partition ID to filter memory bandwidth usage monitoring.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PARTID == 0, this field is not used to match memory bandwidth to a PARTID and the contents of this field is ignored.

If <u>MSMON\_CFG\_MBWU\_CTL</u>.MATCH\_PARTID == 1, the monitor selected by <u>MSMON\_CFG\_MON\_SEL</u> measures or counts memory bandwidth labeled with PARTID equal to this field.

# **Accessing MSMON CFG MBWU FLT**

This register is within the MPAM feature page memory frames.

In a system that supports Secure, Non-secure, Root, and Realm memory maps, there must be MPAM feature pages in all four address maps:

- MSMON\_CFG\_MBWU\_FLT\_s must only be accessible from the Secure MPAM feature page.
- MSMON\_CFG\_MBWU\_FLT\_ns must only be accessible from the Non-secure MPAM feature page.
- MSMON\_CFG\_MBWU\_FLT\_rt must only be accessible from the Root MPAM feature page.
- MSMON\_CFG\_MBWU\_FLT\_rl must only be accessible from the Realm MPAM feature page.

MSMON\_CFG\_MBWU\_FLT\_s, MSMON\_CFG\_MBWU\_FLT\_ns, MSMON\_CFG\_MBWU\_FLT\_rt, and MSMON\_CFG\_MBWU\_FLT\_rl must be separate registers:

- The Secure instance (MSMON\_CFG\_MBWU\_FLT\_s) accesses the PARTID and PMG matching for a memory bandwidth usage monitor used for Secure PARTIDs.
- The Non-secure instance (MSMON\_CFG\_MBWU\_FLT\_ns) accesses the PARTID and PMG matching for a memory bandwidth usage monitor used for Non-secure PARTIDs.
- The Root instance (MSMON\_CFG\_MBWU\_FLT\_rt) accesses the PARTID and PMG matching for a memory bandwidth usage monitor used for Root PARTIDs.
- The Realm instance (MSMON\_CFG\_MBWU\_FLT\_rl) accesses the PARTID and PMG matching for a memory bandwidth usage monitor used for Realm PARTIDs.

When RIS is implemented, loads and stores to MSMON\_CFG\_MBWU\_FLT access the monitor configuration settings for the bandwidth resource instance selected by MSMON\_CFG\_MON\_SEL.RIS and the memory bandwidth usage monitor instance selected by MSMON\_CFG\_MON\_SEL.MON\_SEL.

When RIS is not implemented, loads and stores to MSMON\_CFG\_MBWU\_FLT access the monitor configuration settings for the memory bandwidth usage monitor instance selected by MSMON\_CFG\_MON\_SEL.MON\_SEL.

# MSMON\_CFG\_MBWU\_FLT can be accessed through the memory-mapped interfaces:

Component	Frame	Offset	Instance
MPAM	MPAMF_BASE_s	0x0820	MSMON_CFG_MBWU_FLT_s

Accesses on this interface are RW.

Component	Frame	Offset	Instance
MPAM	MPAMF_BASE_ns	0x0820	MSMON_CFG_MBWU_FLT_ns

Accesses on this interface are RW.

Component	Frame	Offset	Instance
MPAM	MPAMF_BASE_rt	0x0820	MSMON_CFG_MBWU_FLT_rt

When FEAT RME is implemented, accesses on this interface are RW.

Component	Frame	Offset	Instance
MPAM	MPAMF_BASE_rl	0x0820	MSMON_CFG_MBWU_FLT_rl

When FEAT RME is implemented, accesses on this interface are RW.

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