AMCIDR3, Activity Monitors Component Identification Register 3

The AMCIDR3 characteristics are:

Purpose

Provides information to identify an activity monitors component.

For more information, see 'About the Component identification scheme'.

Configuration

It is implementation defined whether AMCIDR3 is implemented in the Core power domain or in the Debug power domain.

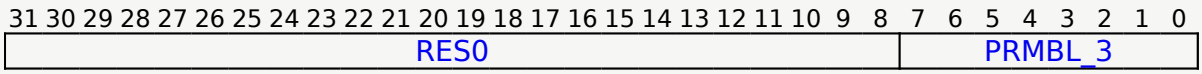
Implementation of this register is optional.

This register is present only when FEAT_AMUv1 is implemented.

Attributes

AMCIDR3 is a 32-bit register.

Field descriptions



Bits [31:8]

Reserved, res0.

PRMBL_3, bits [7:0]

Preamble.

Reads as `0xB1`.

Access to this field is **RO**.

Accessing AMCIDR3

AMCIDR3 can be accessed through the memory-mapped interfaces:

Component	Offset	Instance
AMU	0xFFC	AMCIDR3

Accesses on this interface are **RO**.

28/03/2023 16:02; 72747e43966d6b97dcbd230a1b3f0421d1ea3d94