# CTITRIGOUTSTATUS, CTI Trigger Out Status register

The CTITRIGOUTSTATUS characteristics are:

#### **Purpose**

Provides the raw status of the trigger outputs, after processing by any implementation defined trigger interface logic. For output triggers that are self-acknowledging, this is only meaningful if the CTI implements multicycle channel events.

### Configuration

CTITRIGOUTSTATUS is in the Debug power domain.

#### **Attributes**

CTITRIGOUTSTATUS is a 32-bit register.

#### Field descriptions

31 30 29 28 27 26 25 24 23 22 TROUT31TROUT30TROUT29TROUT28TROUT27TROUT26TROUT25TROUT24TROUT23TROUT22TRO

#### TROUT<n>, bit [n], for n = 31 to 0

Trigger output <n> status.

Bits [31:N] are RAZ. N is the value in CTIDEVID.NUMTRIG.

If n < N, and output trigger < n > is implemented and connected, and either the trigger is not self-acknowledging or the CTI implements multicycle channel events, then permitted values for TROUT< n > are:

TROUT <n></n>	Meaning
0b0	Output trigger n is inactive.
0b1	Output trigger n is active.

Otherwise when n < N it is implementation defined whether TROUT<n> behaves as described here or is RAZ.

## **Accessing CTITRIGOUTSTATUS**

# CTITRIGOUTSTATUS can be accessed through the external debug interface:

Component	Offset	Instance	
CTI	0x134	CTITRIGOUTSTATUS	

Accesses on this interface are **RO**.

AArch32	AArch64	AArch32	AArch64	Index by	<u>External</u>
<u>Registers</u>	<u>Registers</u>	<u>Instructions</u>	<u>Instructions</u>	<b>Encoding</b>	Registers

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