
AArch64 System Instructions

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[COSP RCTX](#): Clear Other Speculative Restriction by Context

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[DC CVAC](#): Data or unified Cache line Clean by VA to PoC

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[DC CVAP](#): Data or unified Cache line Clean by VA to PoP

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[DC GZVA](#): Data Cache set Allocation Tags and Zero by VA

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[DC IGSW](#): Invalidate of Allocation Tags by Set/Way

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[DC ISW](#): Data or unified Cache line Invalidate by Set/Way

[DC IVAC](#): Data or unified Cache line Invalidate by VA to PoC

[DC ZVA](#): Data Cache Zero by VA

[DVP RCTX](#): Data Value Prediction Restriction by Context

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[GCSPOPM](#): Guarded Control Stack Pop

[GCSPOPX](#): Guarded Control Stack Pop exception return record

[GCSPUSHM](#): Guarded Control Stack Push

[GCSPUSHX](#): Guarded Control Stack Push exception return record

[GCSSS1](#): Guarded Control Stack Switch Stack 1

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[S1 <op1> <Cn> <Cm> <op2>, SYSP](#)

[S1 <op1> <Cn> <Cm> <op2>](#): IMPLEMENTATION DEFINED maintenance instructions

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