

## TRCIT

Trace Instrumentation generates an instrumentation trace packet that contains the value of the provided register.

This is an alias of [SYS](#). This means:

- The encodings in this description are named to match the encodings of [SYS](#).
- The description of [SYS](#) gives the operational pseudocode, any constrained unpredictable behavior, and any operational information for this instruction.

### System (FEAT\_ITE)

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	1	0	1	0	1	0	1	0	0	0	0	1	0	1	1	0	1	1	1	0	0	1	0	1	1	1					Rt
										L		op1			CRn			CRm			op2										

**TRCIT** [<Xt>](#)

is equivalent to

[SYS](#) #3, C7, C2, #7, [<Xt>](#)

and is always the preferred disassembly.

### Assembler Symbols

[<Xt>](#) Is the 64-bit name of the general-purpose source register, encoded in the "Rt" field.

### Operation

The description of [SYS](#) gives the operational pseudocode for this instruction.