

## TRBDEVID2, Device Configuration Register 2

The TRBDEVID2 characteristics are:

### Purpose

Provides discovery information for the component.

For additional information, see the CoreSight Architecture Specification.

### Configuration

This register is present only when FEAT\_TRBE\_EXT is implemented. Otherwise, direct accesses to TRBDEVID2 are res0.

TRBDEVID2 is in the Core power domain.

### Attributes

TRBDEVID2 is a 32-bit register.

### Field descriptions

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RES0																															

#### Bits [31:0]

Reserved, res0.

### Accessing TRBDEVID2

TRBDEVID2 can be accessed through the external debug interface:

Component	Offset	Instance
TRBE	0xFC0	TRBDEVID2

This interface is accessible as follows:

- When DoubleLockStatus() or !IsCorePowered(), accesses to this register generate an error response.
- Otherwise, accesses to this register are **RO**.

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