# **ERRCIDR2, Component Identification Register**2

The ERRCIDR2 characteristics are:

## **Purpose**

Provides discovery information about the component.

For more information, see 'About the Component Identification scheme'.

## **Configuration**

Implementation of this register is optional.

ERRCIDR2 is implemented only as part of a memory-mapped group of error records.

#### **Attributes**

ERRCIDR2 is a 32-bit register.

## Field descriptions

31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8	7	6	5	4	3	2	1	0_
RES0			PRMBL 2					

#### Bits [31:8]

Reserved, res0.

#### **PRMBL\_2**, bits [7:0]

Component identification preamble, segment 2.

Reads as  $0 \times 05$ .

Access to this field is **RO**.

# **Accessing ERRCIDR2**

#### ERRCIDR2 can be accessed through the memory-mapped interfaces:

Component	Offset	Instance

RAS	0xFF8	ERRCIDR2
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Accesses on this interface are RO.

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External Registers

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