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Pseu

CNT

Count bits counts the number of binary one bits in the value of the source register, and writes the result to the destination register.

Integer (FEAT CSSC)

32-bit (sf == 0)

```
CNT <Wd>, <Wn>
```

64-bit (sf == 1)

```
CNT <Xd>, <Xn>
if !IsFeatureImplemented(FEAT_CSSC) then UNDEFINED;
constant integer datasize = 32 << UInt(sf);
integer n = UInt(Rn);
integer d = UInt(Rd);</pre>
```

Assembler Symbols

<wd></wd>	Is the 32-bit name of the general-purpose destination
	• • 1 1 • • 1 • • 1 • • 1 • • • 1 •

register, encoded in the "Rd" field.

<Wn> Is the 32-bit name of the general-purpose source register,

encoded in the "Rn" field.

<Xd> Is the 64-bit name of the general-purpose destination

register, encoded in the "Rd" field.

<Xn> Is the 64-bit name of the general-purpose source register,

encoded in the "Rn" field.

Operation

```
bits(datasize) operand1 = X[n, datasize];
integer result = BitCount(operand1);
X[d, datasize] = result<datasize-1:0>;
```

Operational information

If PSTATE.DIT is 1:

- The execution time of this instruction is independent of:
 - The values of the data supplied in any of its registers.

- The values of the NZCV flags.
- The response of this instruction to asynchronous exceptions does not vary based on:
 - The values of the data supplied in any of its registers.
 - The values of the NZCV flags.

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