## SWPB, SWPAB, SWPALB, SWPLB

Swap byte in memory atomically loads an 8-bit byte from a memory location, and stores the value held in a register back to the same memory location. The value initially loaded from memory is returned in the destination register.

- If the destination register is not WZR, SWPAB and SWPALB load from memory with acquire semantics.
- SWPLB and SWPALB store to memory with release semantics.
- SWPB has neither acquire nor release semantics.

For more information about memory ordering semantics, see *Load-Acquire*, Store-Release.

For information about memory accesses, see *Load/Store addressing modes*.

## Integer (FEAT LSE)

```
31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
0 0 1 1 1 0 0 0 A R 1
                               Rs
                                       1 0 0 0 0 0
                                                           Rn
size
```

```
SWPAB (A == 1 \&\& R == 0)
```

```
SWPAB <Ws>, <Wt>, [<Xn | SP>]
SWPALB (A == 1 \&\& R == 1)
         SWPALB <Ws>, <Wt>, [<Xn | SP>]
SWPB (A == 0 \&\& R == 0)
         SWPB \langle Ws \rangle, \langle Wt \rangle, [\langle Xn | SP \rangle]
SWPLB (A == 0 \&\& R == 1)
         SWPLB <Ws>, <Wt>, [<Xn | SP>]
```

```
if !IsFeatureImplemented(FEAT_LSE) then UNDEFINED;
integer t = UInt(Rt);
integer n = UInt(Rn);
integer s = UInt(Rs);
boolean acquire = A == '1' && Rt != '11111';
boolean release = R == '1';
boolean tagchecked = n != 31;
```

## **Assembler Symbols**

<Ws> Is the 32-bit name of the general-purpose register to be

stored, encoded in the "Rs" field.

<Wt> Is the 32-bit name of the general-purpose register to be

loaded, encoded in the "Rt" field.

<Xn|SP> Is the 64-bit name of the general-purpose base register or

stack pointer, encoded in the "Rn" field.

## **Operation**

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