GITS_PARTIDR, Set PARTID and PMG Register

The GITS_PARTIDR characteristics are:

Purpose

Sets the PARTID and PMG values used for memory accesses by the ITS.

Configuration

This register is present only when FEAT_GICv3p1 is implemented. Otherwise, direct accesses to GITS_PARTIDR are res0.

A copy of this register is provided for each ITS.

When GITS_TYPER.MPAM==0, this register is res0.

Attributes

GITS_PARTIDR is a 32-bit register.

Field descriptions

31 30 29 28 27 26 25 24	23 22 21 20 19 18 17 16	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
RES0	PMG	PARTID

Bits [31:24]

Reserved, res0.

PMG, bits [23:16]

PMG value used when ITS accesses memory.

Bits not needed to represent PMG values in the range 0 to PMG_MAX are res0.

The reset behavior of this field is:

- On a GIC reset, this field resets to `0`.

PARTID, bits [15:0]

PARTID value used when ITS accesses memory.

Bits not needed to represent PARTID values in the range 0 to PARTID_MAX are res0.

The reset behavior of this field is:

- On a GIC reset, this field resets to `0`.

Accessing GITS_PARTIDR

GITS_PARTIDR can be accessed through the memory-mapped interfaces:

Component	Offset
GIC ITS control	`0x0014`

Accesses on this interface are **RW**.

28/03/2023 16:01; 72747e43966d6b97dcbd230a1b3f0421d1ea3d94