## "Signal to High Prio Thread" benchmark measurements April 04, 2022

Table 1. Armv8a (platform Tx1a). Pattern 10/100, MCS kernel

	Min	Max	Mean	Std Dev	Min	Max	Mean	Std Dev				
10/100	Late processing				Lat	te process	sing w/ de	elay	"Untailed" run: 97 samples			
Run #1	688	772	699	13.57	683	690	685	1.80	691	697	696	2.00
Run #2	688	832	699	17.44	685	692	687	1.53	688	740	696	5.73
Run #3	688	769	695	8.65	689	737	690	4.94	680	688	687	2.04
Run #4	688	811	698	16.31	685	689	685	0.59	680	689	687	2.95
Run #5	685	749	692	8.40	679	686	681	1.97	685	691	689	2.12
Run #6	688	755	696	11.18	679	714	708	3.44	685	727	691	4.24
Run #7	691	898	699	21.39	685	692	687	1.78	680	689	687	2.83
Run #8	688	808	696	17.73	689	696	691	1.85	691	697	695	2.12
Run #9	691	738	698	6.77	679	686	681	1.53	680	689	686	2.39
Run #10	680	720	686	5.15	689	696	691	1.76	683	689	688	2.00
	Stats for the columns											
Min	680	720	686	5	679	686	681	1	680	688	686	2
Max	691	898	699	21	689	737	708	5	691	740	696	6
Mean	688	785	696		684	698	689		684	700	690	

Table 2. Armv8a (platform Tx1a). Pattern 10/100, Traditional kernel

	Min	Max	Mean	Std Dev	М	in	Max	Mean	Std Dev					
10/100	Late processing					Late processing w/ delay					"Untailed" run: 97 samples			
Run #1	589	702	606	19.82		619	628	622	2.48	589	654	605	15.82	
Run #2	589	705	606	19.22		619	628	622	2.40	635	684	642	5.56	
Run #3	589	709	605	18.11		619	628	622	2.41	589	702	605	20.12	
Run #4	589	687	606	18.62		619	628	622	2.50	589	699	605	17.71	
Run #5	589	705	606	19.38		619	628	622	2.38	589	707	604	17.39	
Run #6	589	692	608	22.49		619	628	622	2.52	589	658	605	16.41	
Run #7	589	728	607	24.48		619	628	622	2.44	589	655	604	15.47	
Run #8	589	700	605	18.85		619	628	622	2.39	589	701	606	19.19	
Run #9	589	718	606	23.65		619	628	622	2.40	589	654	605	15.93	
Run #10	589	791	610	32.69		619	627	622	2.31	589	702	606	18.91	
	Stats for the columns													
Min	589	687	605	18		619	627	622	2	589	654	604	6	
Max	589	791	610	33		619	628	622	3	635	707	642	20	
Mean	589	714	607			619	628	622		594	682	609		