

RK805-1

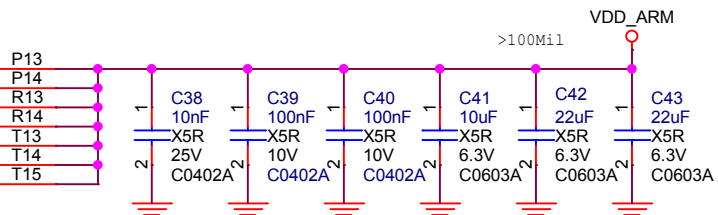
U3N

ARM

VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE
VDD_CORE

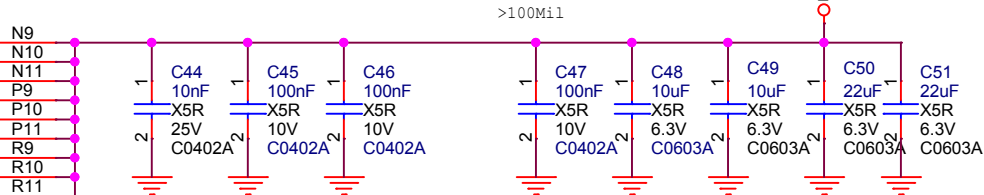
GPU/Logic

VDD_LOGIC
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VDD_LOGIC



PCB LAYOUT Note: All capacitances should be close to SOC,
and one of 22uF capacitance should be placed right under the pin

VDD_LOG

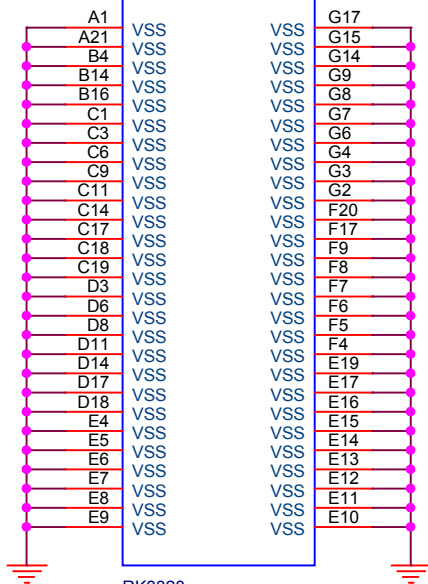


PCB LAYOUT Note: All capacitances should be close to SOC,
and one of 22uF capacitance should be placed right under the pin

RK3328

TFBGA395L-14X14mmA

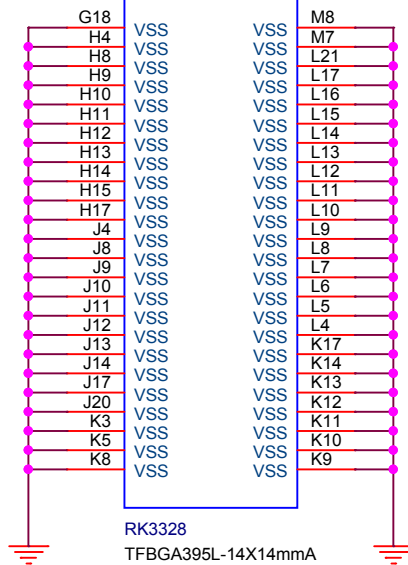
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RK3328

TFBGA395L-14X14mmA

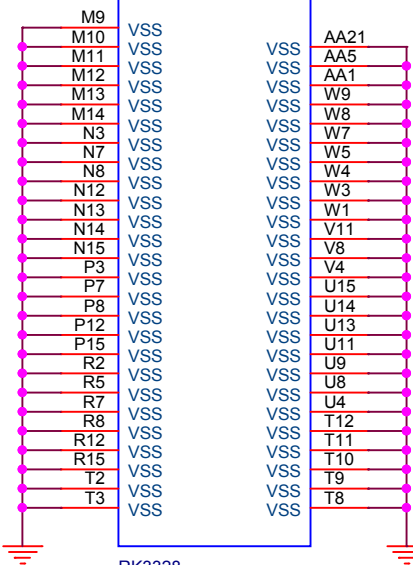
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RK3328


TFBGA395L-14X14mmA

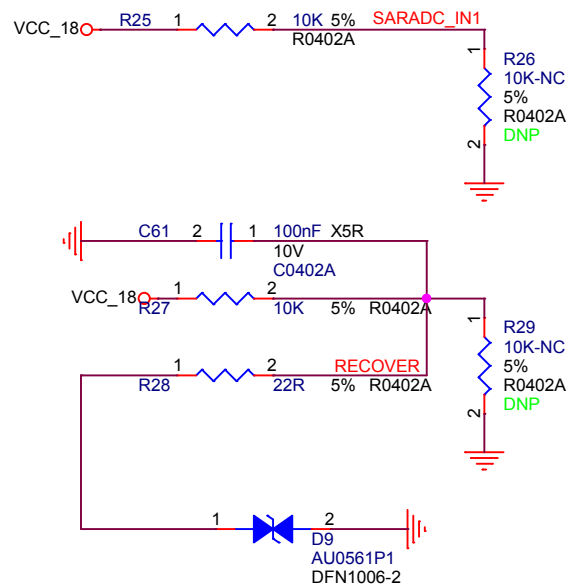
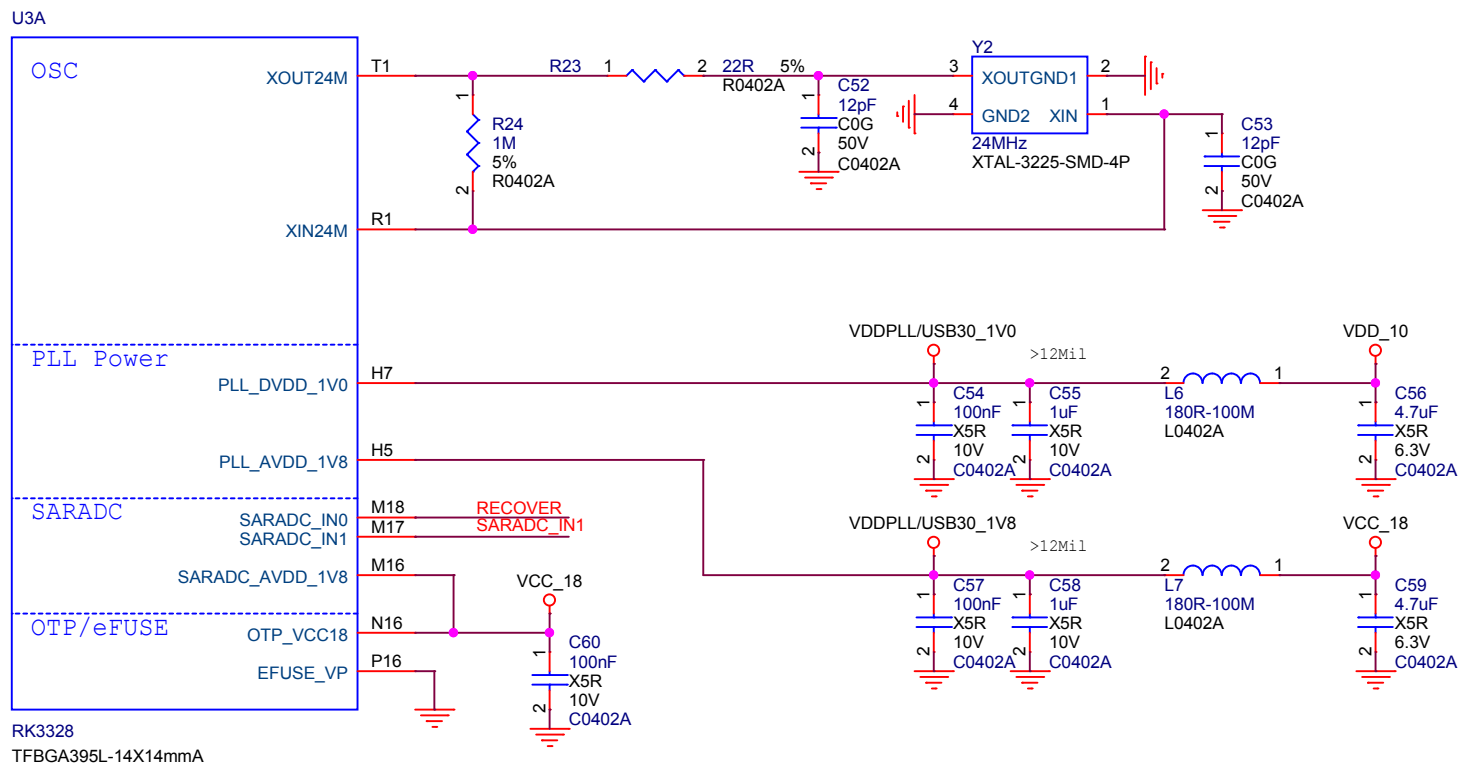
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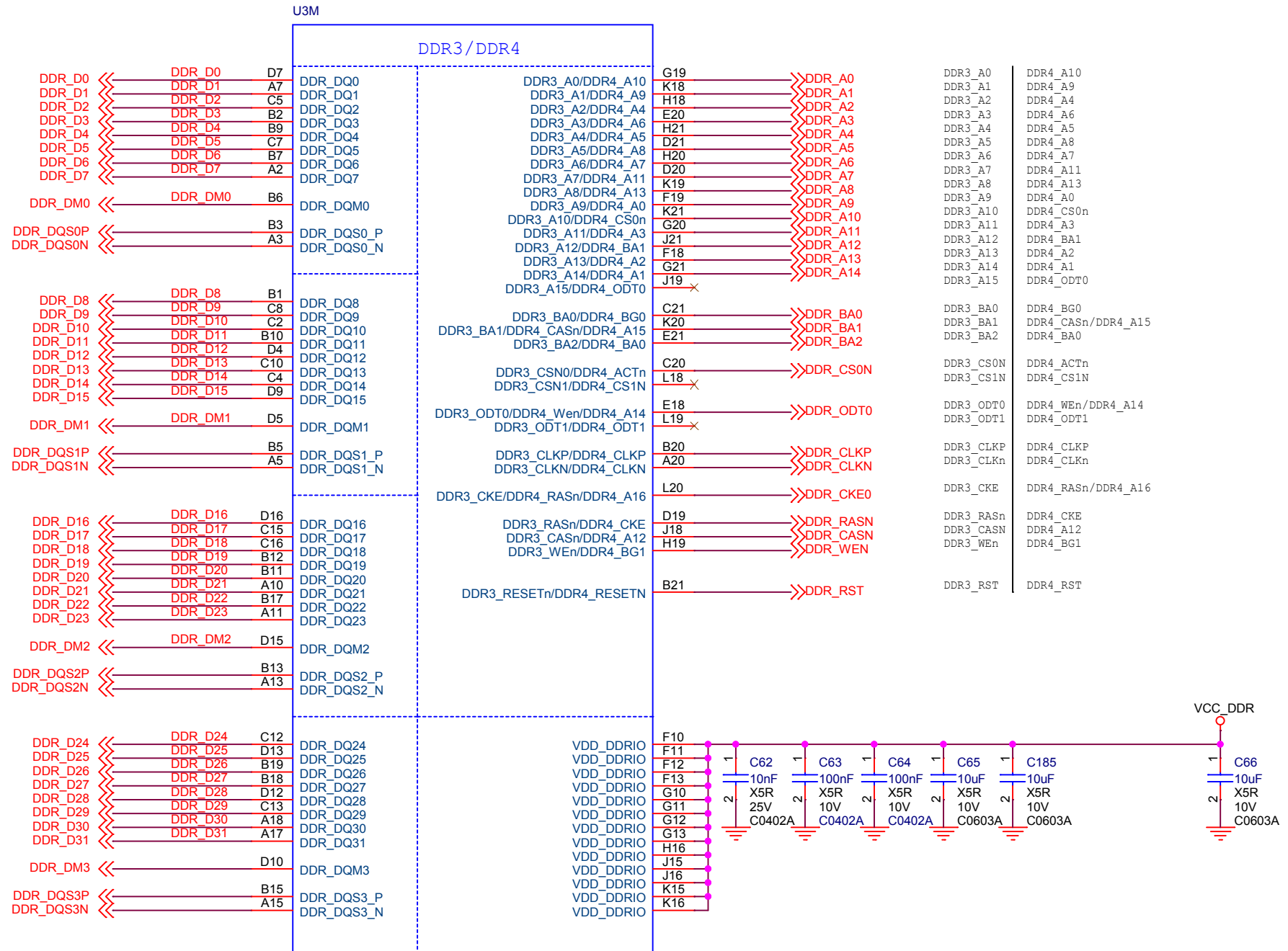
RK3328

TFBGA395L-14X14mmA

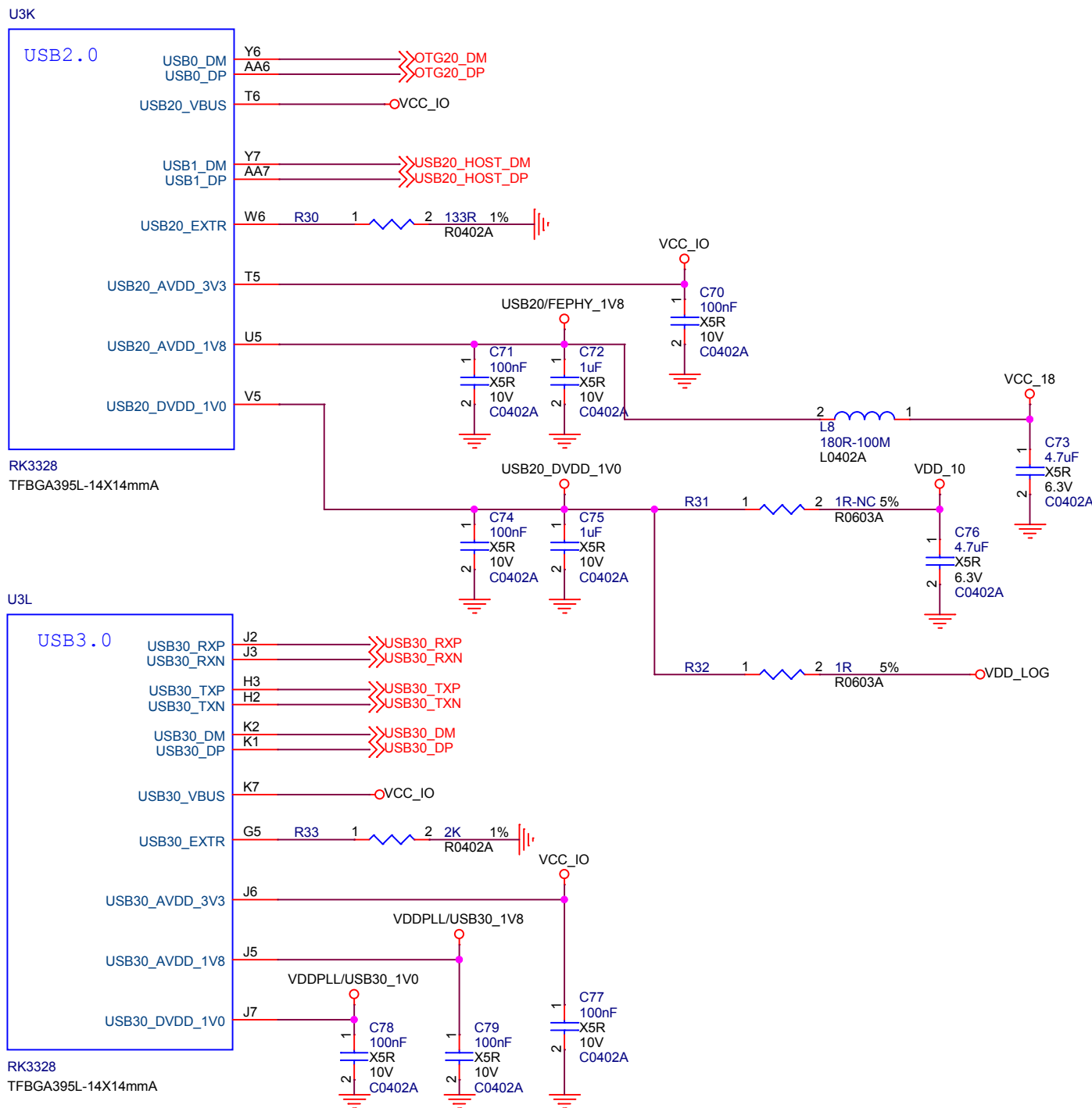
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		FaceBook : https://www.facebook.com/Makerbase.mks/	
		Github : https://github.com/makerbase-mks	
Size	Document Number		Rev
A4	04_RK3328 Power		V1.1.001
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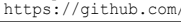


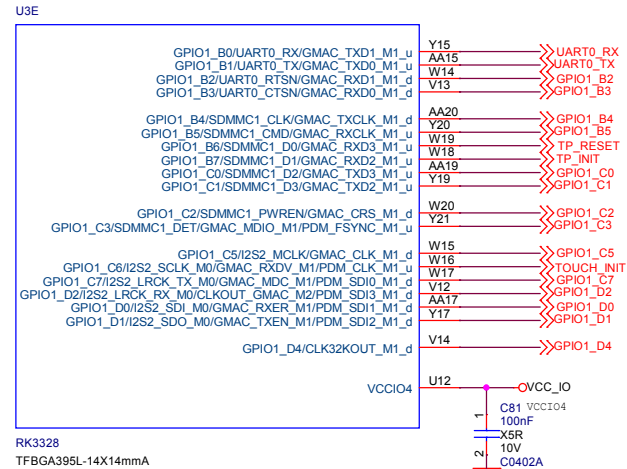
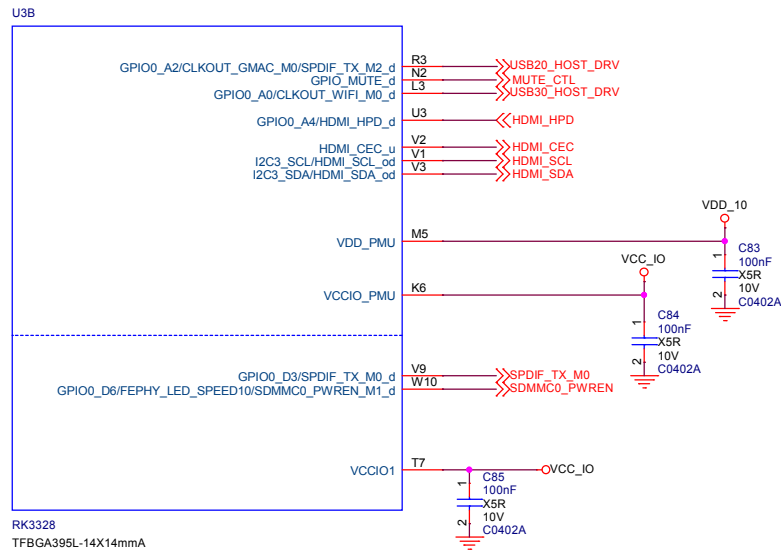
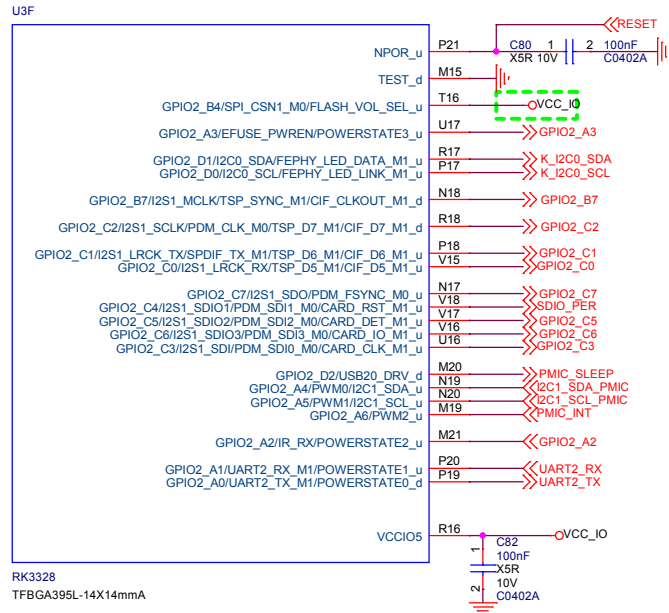
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Recover		DNP	10K	
BOM1		110K	10K	
BOM2		100K	20K	
BOM3		100K	33K	
BOM4		36K	18K	
BOM5		51K	36K	
BOM6		51K	51K	
BOM7		36K	51K	
BOM8		18K	36K	
BOM9		33K	100K	
BOM10		20K	100K	
BOM11		10K	110K	
BOM12		10K	DNP	



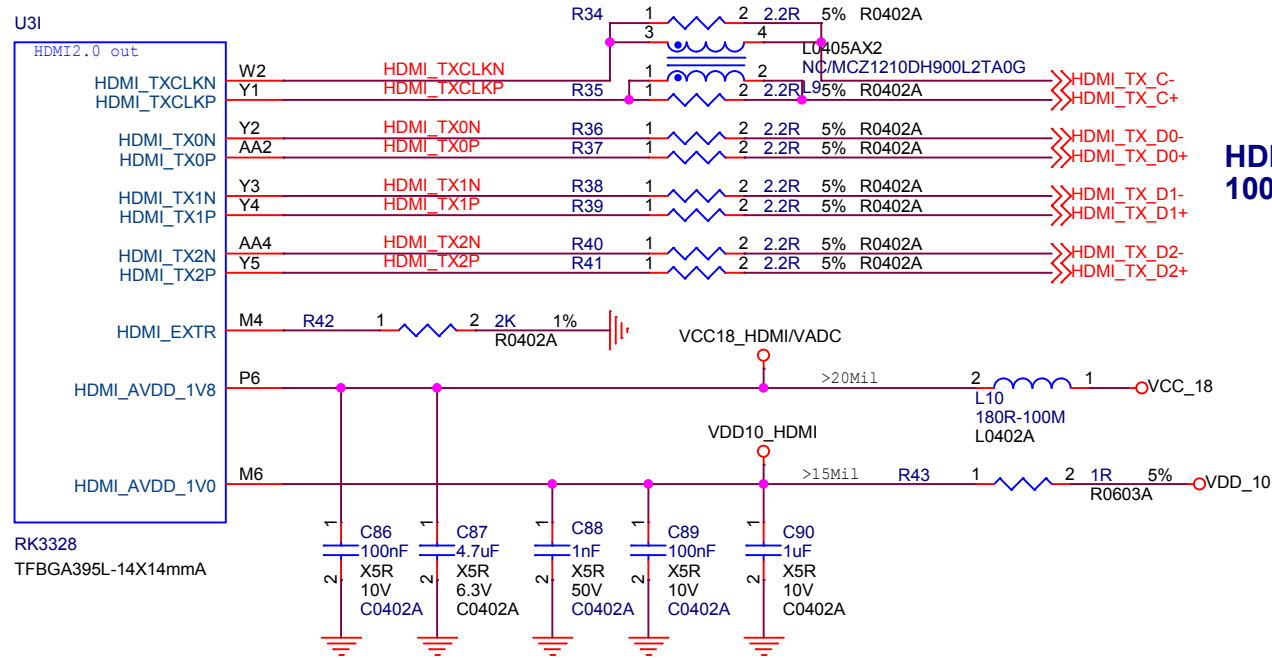
RK3328
TFPGA395L-14X14mm



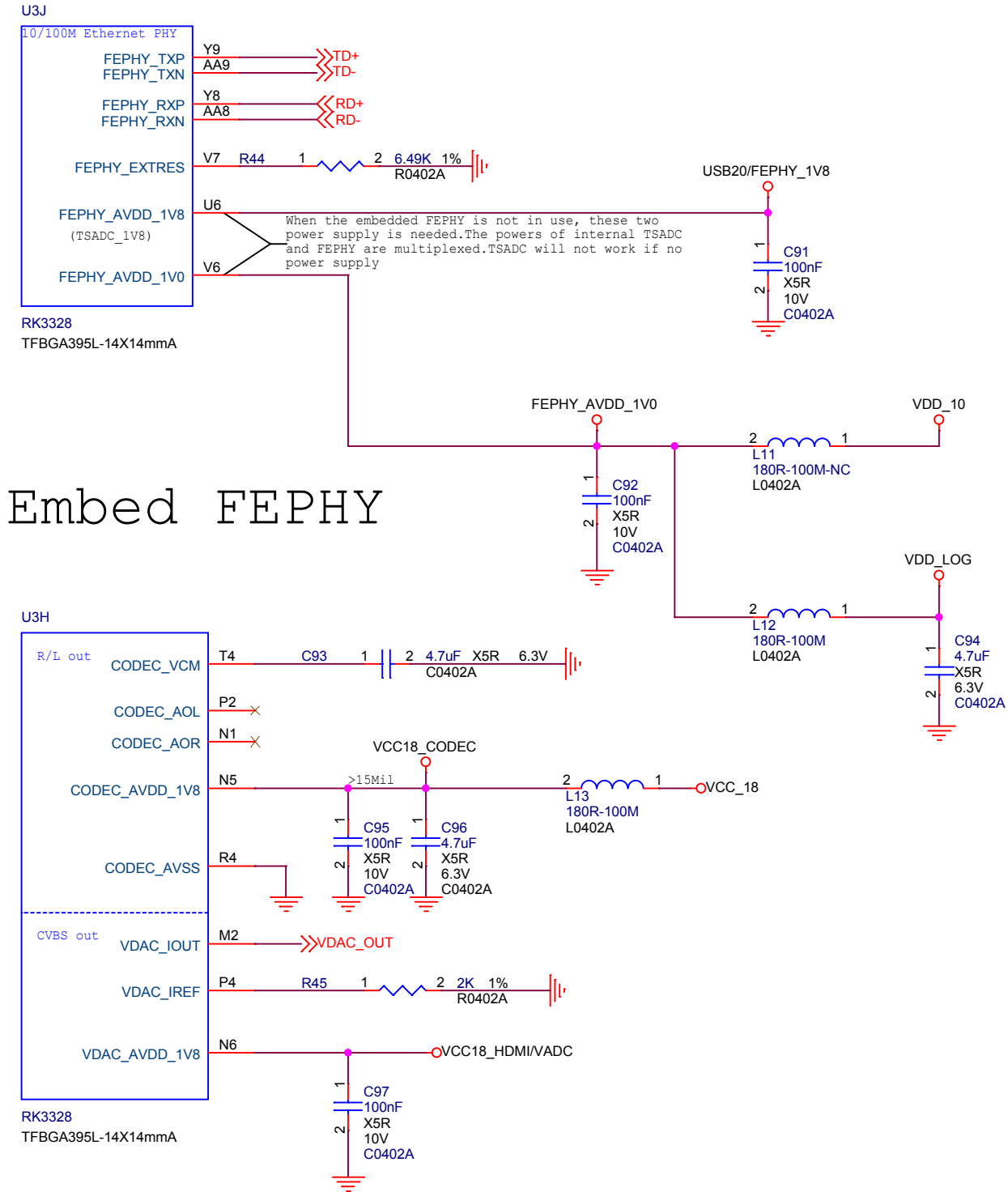
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Size	Document Number						Rev				
A4	08_RK3328 USB2 PHY/USB3 PHY						V1.1		001		
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Close to RK3328

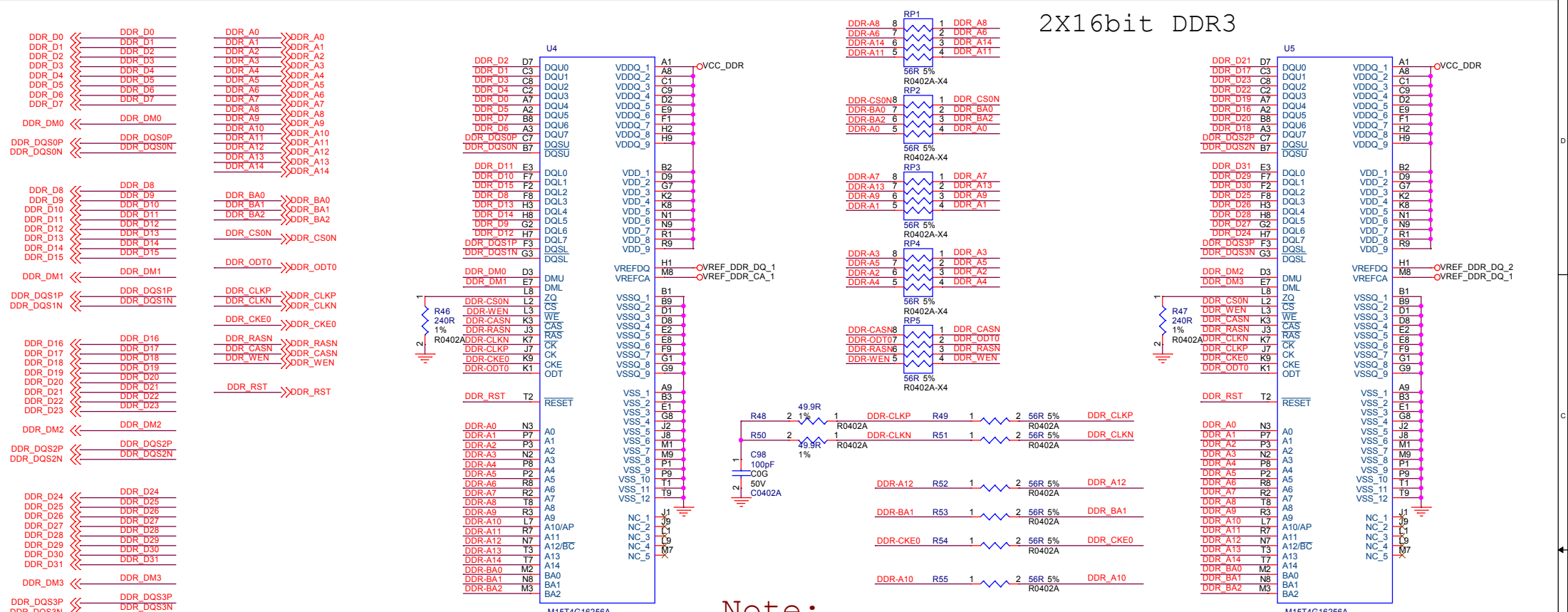


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Date:		Monday, June 27, 2022		Sheet	
Rev		V1.1		10 of 18	
FaceBook :		https://www.facebook.com/Makerbase.mks/		Rev	
Github :		https://github.com/makerbase-mks		1001	
Makerbase		10_RK3328 HDMI PHY		10 of 18	

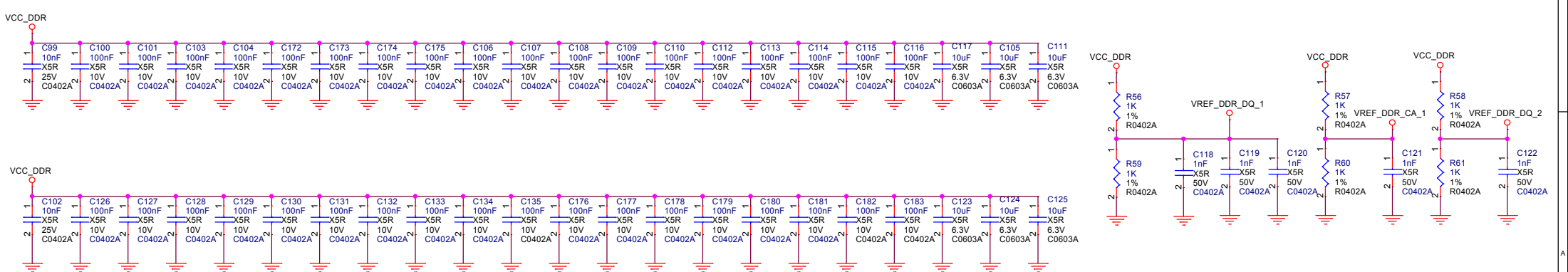


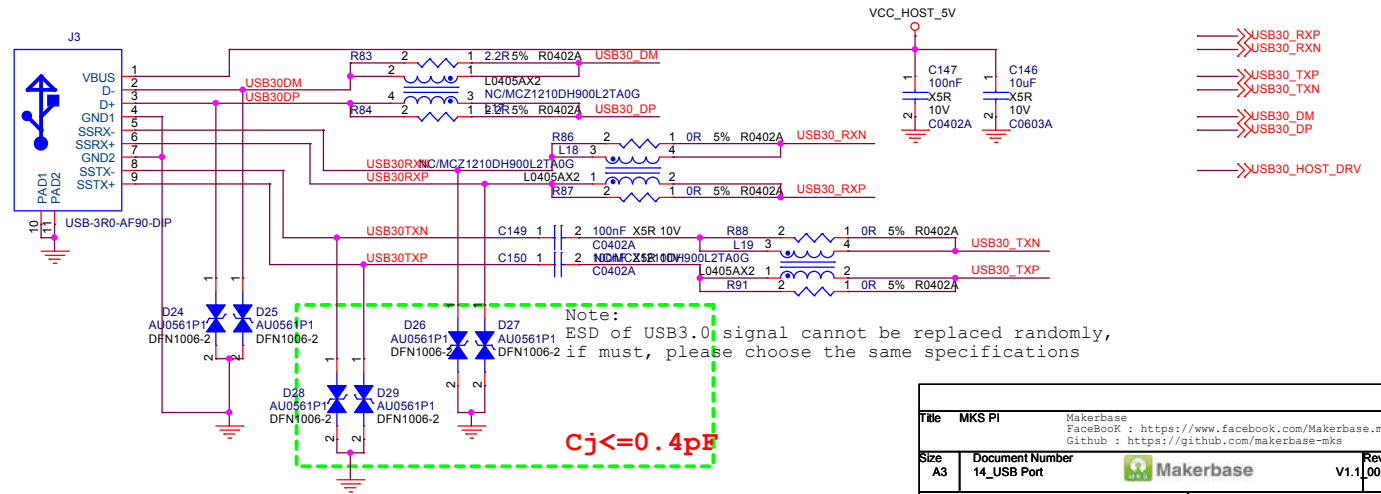
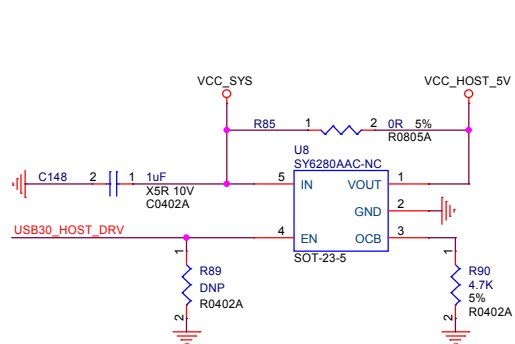
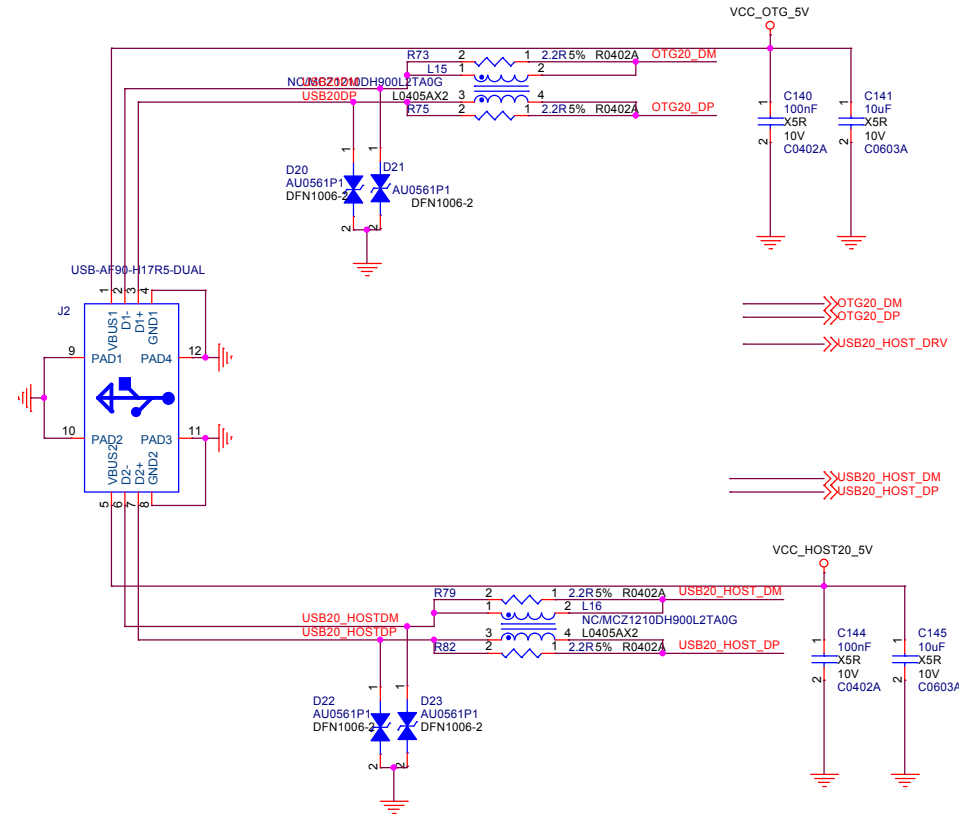
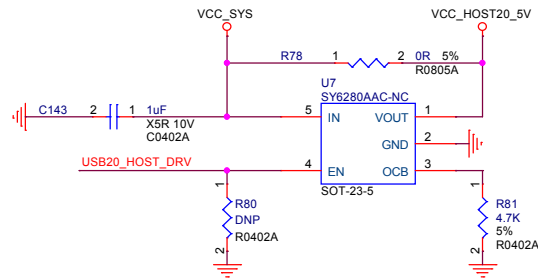
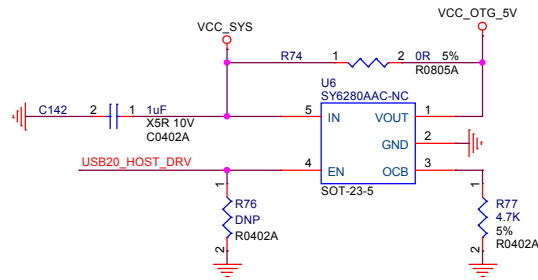
Embed FEPHY

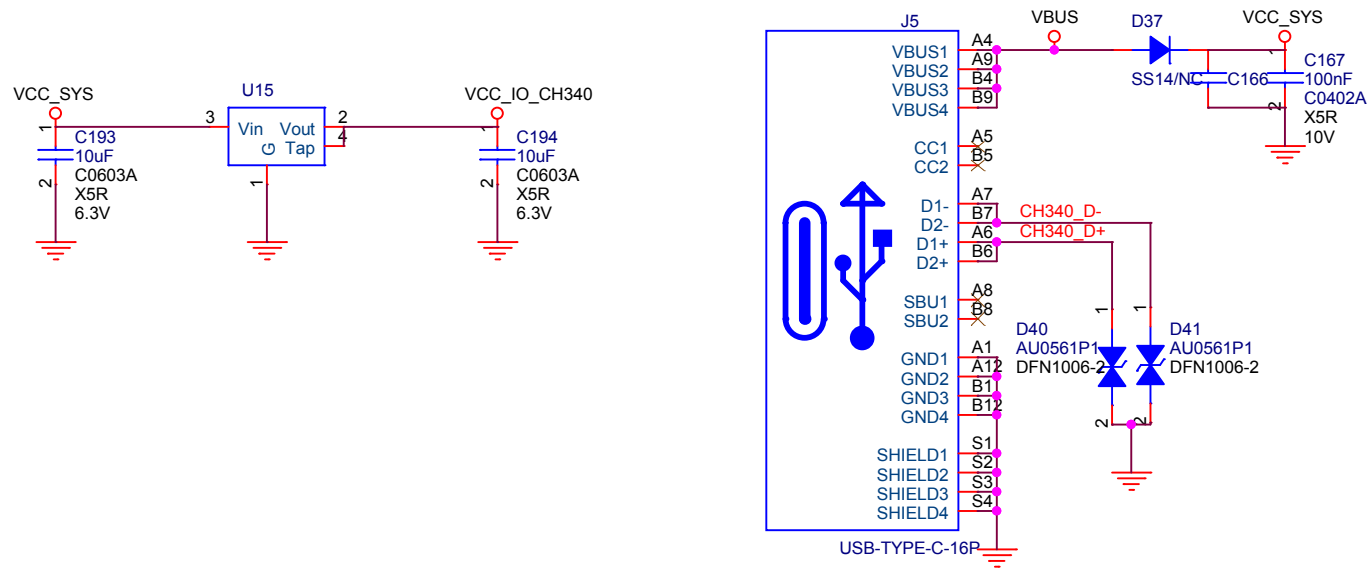
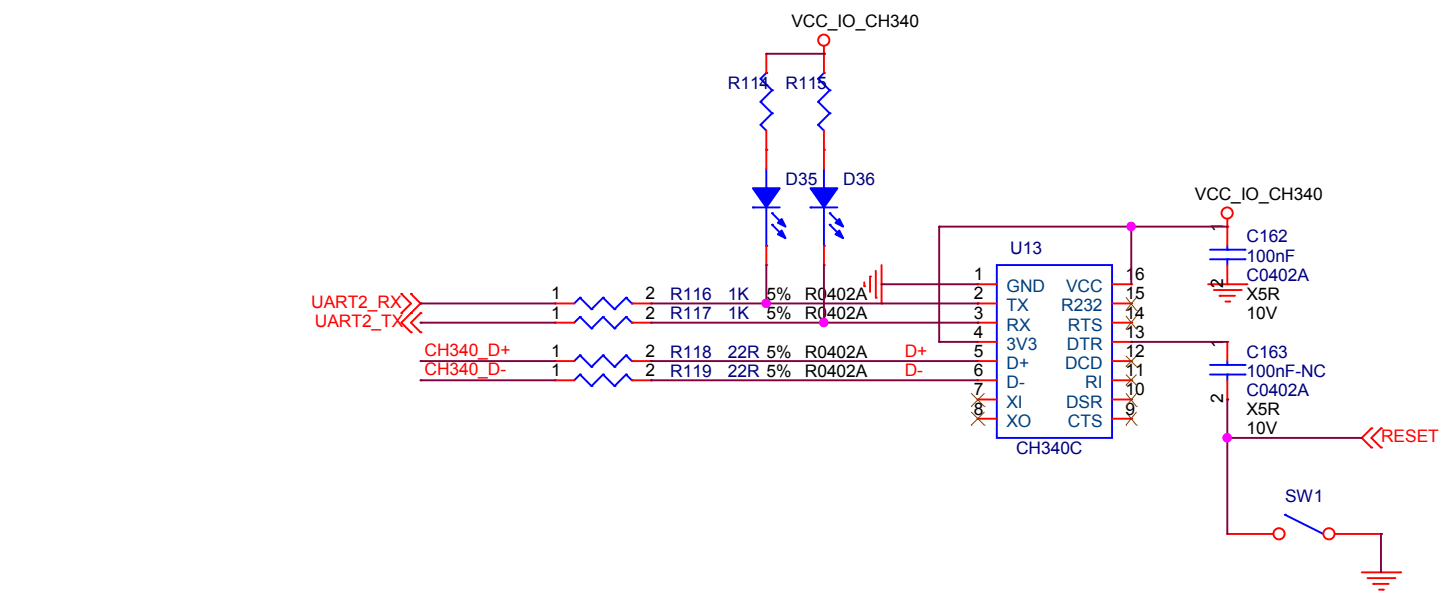
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		Makerbase FaceBook : https://www.facebook.com/Makerbase.mks/ Github : https://github.com/makerbase-mks		
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A4	11_RK3328 FEPHY/AV		V1.1001	
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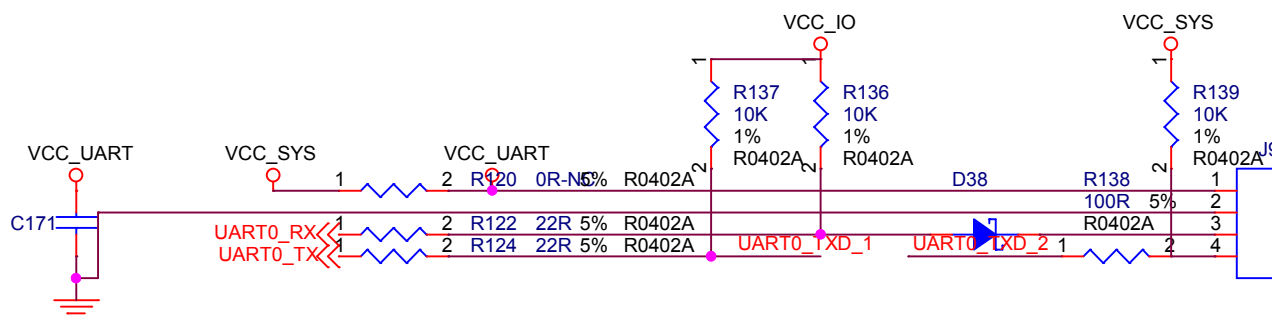
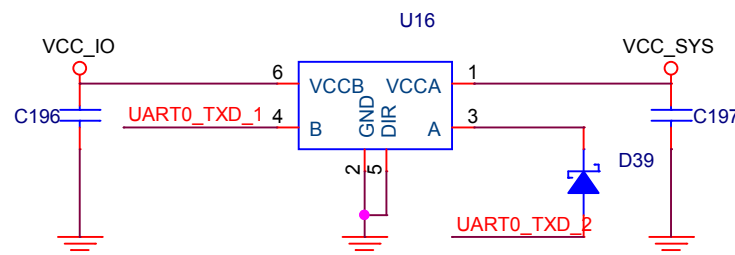
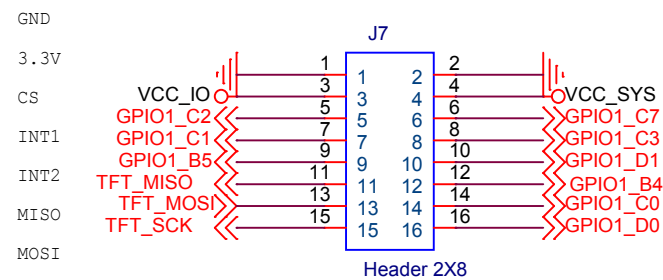
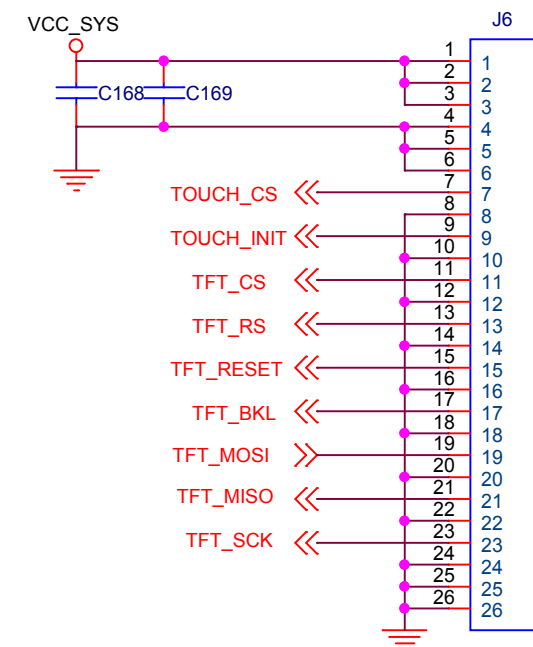


2X16bit DDR3









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A	18_TS35/12C/UART/IO		V1.1_001
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