



Name	Chandra Mohan Dharmapuri.
Designation	Assistant Professor (ECE).
Educational Qualification	M.Tech (VLSI System Design), B.Tech (ECE).
Other Qualification	UGC NET, GATE (2013), JNTUH-FET.
Area of Interest	Low Power VLSI, Communications.
Experience	10 years (Industry: 3 years, Teaching: 7 years).
Papers in Journals	<ul style="list-style-type: none"> • “Realization of Functional Broadside Tests with BIST Technique using a Fixed Hardware Structure” in IJSETR-Vol.04, Issue.38, Sep 2015. ISSN: 2319-8885. • “Analysis of Parallel Prefix Adders” in IJMETMR-Vol.02, Issue.9, Sep 2015. ISSN: 2348-4845. • “Random Number Generation Using Modulo $2n-2k-1$ Adder for Residue Number System” in SSRG-IJECE – volume 1 issue 7 Sep 2014. ISSN: 2348-8549.
Papers in Conferences	<ul style="list-style-type: none"> • “Low Power Multiplier Design Using Reduced Spurious Transition Activity Technique for Signal Processing Module” in the national conference on “Signal & Image Processing “on Apr 06-07, 2012 at Sri Sai Aditya Institute of Technology, Kakinada.
Workshops & FDP	<ul style="list-style-type: none"> • Coordinated “PCB Designing & Fabrication” Workshop held on 25th & 26th Feb, 2015 at CMRIT. • One Week FDP on “ Advanced VLSI Design-Hands on Experience with CAD Tools” from 19th to 24th December, 2014 at CMRCET, Hyderabad. • 2-day FDP on “Role of PLC’s in industrial automation” at CMRIT, Hyderabad 27th & 28th June, 2014. • Workshop on “Matlab-Simulink” conducted by Mathworks at Taj Deccan in 2011.
Courses & Training	<ul style="list-style-type: none"> • NPTEL Online Certification Course on “Introduction to Electromagnetism”, in April, 2015. • 3-day Intensive course on “Electromagnetic Theory and Transmission Lines” from 2nd to 4th March, 2015 at JNTUH College of Engineering, Nachupally, Telangana.
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