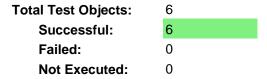
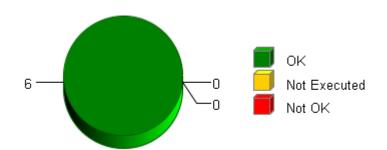


#### **Summary**

# **Overall Test Object Results (including Coverage)**



**Date:** 2014-12-26 **Time:** 17:52:10+0530



#### **Selected Project Items**

Test Object "CBD\_UnitTest/MtrVel\_Digi/CalcCoarseVel" Test Object "CBD\_UnitTest/MtrVel\_Digi/MtrVel\_Init" Test Object "CBD\_UnitTest/MtrVel\_Digi/MtrVel\_Per1" Test Object "CBD\_UnitTest/MtrVel\_Digi/MtrVel\_Per2" Test Object "CBD\_UnitTest/MtrVel\_Digi/MtrVelBlend"

Test Object "CBD\_UnitTest/MtrVel\_Digi/RegressionFit"

#### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

#### **Batch Operation Settings**

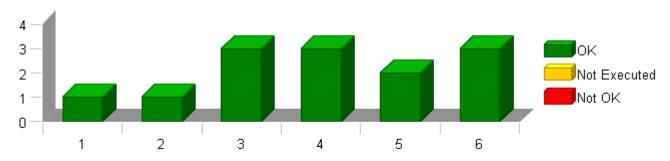
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

#### Test Case Results for Each Test Object (without Coverage)

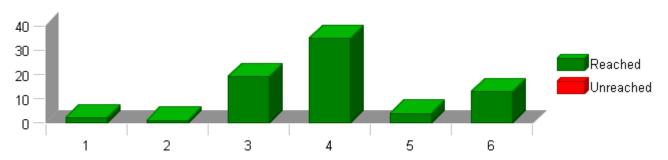


The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results



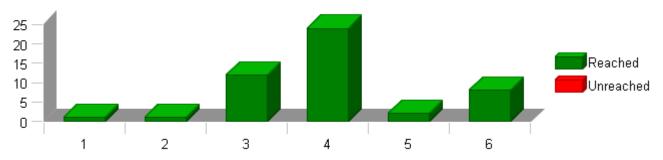
do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

#### Statement (C0) Coverage: Total Statements for Each Test Object



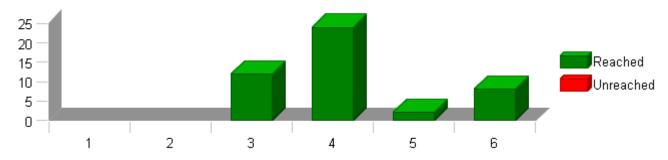
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

# Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

#### **Decision Coverage: Total Decision Outcomes for Each Test Object**

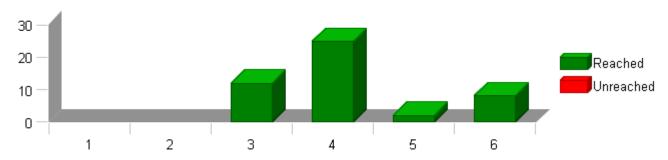


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



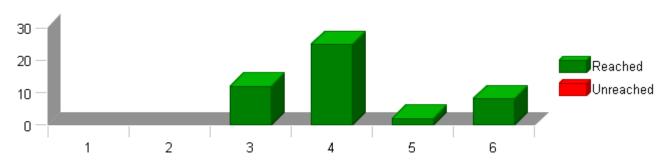
### MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

#### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

# **TEST OVERVIEW REPORT**

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# Test Object List

Project MtrVel\_Digi

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases R	Result
	MtrVel_Digi	100 %	100 %	100 %	100 %	100 %	13 of 13 passed	•
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	13 of 13 passed	•
	MtrVel_Digi	100 %	100 %	100 %	100 %	100 %	13 of 13 passed	•
1	<u>CalcCoarseVel</u>	100 %	100 %	-	-	-	1 of 1 passed	•
2	MtrVel_Init	100 %	100 %	-	-	-	1 of 1 passed	~
3	MtrVel Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
4	MtrVel Per2	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
5	<u>MtrVelBlend</u>	100 %	100 %	100 %	100 %	100 %	2 of 2 passed	<b>✓</b>
6	RegressionFit	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•

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MtrVelBlend

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVelBlend

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	2	
Successful	2	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MftVel_Dig\include -I\$(PROJECTROOT)\MftVel_Dig\include -I\$(Compiler Install Path\)include -I\$(PROJECTROOT)\MftVel_Dig\include -I\$(Compiler Install Path\)include -I\$(PROJECTROOT)\MftVel_Dig\include -I\$(Compiler Install Path\)include -I\$(PROJECTROOT)\MftVel_Dig\include -I\$(

Comments/Description/Specification						
Name	Text					





Module 'MtrVel\_Dig'

Name of Tester:Sunita Kore
Code File(s) Version:5
Module Design Document.WotorVelocity\_MDD.doc
Module Version:Netweer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1934
Total FLASH Used (Bytes):1934
Total FLASH Used (Bytes):42
Total CALS Used (Bytes):42
Total CALS Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""MtrVel\_Per1"", the values of variable ""MechMtrPos2Timestamp\_USec\_u32"" and
""MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2\_uS\_T\_132"" in the range of "1800 - 2200" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit" , the path 'P5' i.e. "'(SumDeltaSqT\_uSaq\_T\_132 > FLT\_EPSILON) = FALSE" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3\_TimeBuffer\_uS\_M\_u16p0[4][8]"") and have been given value as '0' for all array elements.

Note5: ""CBD\_Sandbox\_dbg.map"" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: CPU Cycles:

TS 1.1 612 Cycles TS 1.2 663 Cycles

Description VECTOR DESCRIPTION:

TS 1.1 " Shortest Execution Path: (Interpolation function)
TblInput <= TableX(0)=>True"
TS 1.2 "Longest Execution Path: (Interpolation function)
TblInput <= TableX(0)=>False
TblInput>= TableX(Size - 1)"

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0		
FinMtrVel_MtrRadpS_T_f32	-654.98		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-654.97998	-654.98	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0.95		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	14		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	675.474976	675.475	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	



Test Step 2.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-1350		
FinMtrVel_MtrRadpS_T_f32	-1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-1350	-1350	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 2.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1350		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	1350	1350	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~





Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-36		
FinMtrVel_MtrRadpS_T_f32	-1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-36	-36	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 2.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-58.87		
FinMtrVel_MtrRadpS_T_f32	1350		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-58.8699989	-58.87	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	235		
FinMtrVel_MtrRadpS_T_f32	0		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	235	235	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY u16 u16Xu16Y Cnt	1	IntplVarXY u16 u16Xu16Y Cnt	1	

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	300.07		
FinMtrVel_MtrRadpS_T_f32	235.5		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	10		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	300.070007	300.07	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
IntplVarXY u16 u16Xu16Y Cnt	1	IntplVarXY u16 u16Xu16Y Cnt	1	~		



Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-476		
FinMtrVel_MtrRadpS_T_f32	-58.56		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	13		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	14		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-476	-476	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-1350		
FinMtrVel_MtrRadpS_T_f32	300		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-1350	-1350	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1350		
FinMtrVel_MtrRadpS_T_f32	-476		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	1350	1350	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	0		
FinMtrVel_MtrRadpS_T_f32	-654.98		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
Name	Actual Value Exp	pected Value	Result
MtrVelBlend()	-654.97998 -654	4.98	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY u16 u16Xu16Y Cnt	1	IntplVarXY u16 u16Xu16Y Cnt	1	~





Test Step 2.11 (Repeat Count = 1)		<b>✓</b>	
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	1100.19995		
FinMtrVel_MtrRadpS_T_f32	-765		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	1100.19995	1100.19995	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.12 (Repeat Count = 1)		<b>✓</b>	
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-654.65		
FinMtrVel_MtrRadpS_T_f32	675.87		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	13		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-654.650024	-654.65	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

Test Step 2.13 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-36		
FinMtrVel_MtrRadpS_T_f32	333		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-36	-36	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY u16 u16Xu16Y Cnt	1	IntplVarXY u16 u16Xu16Y Cnt	1	

Test Step 2.14 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	-58		
FinMtrVel_MtrRadpS_T_f32	987		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	-58	-58	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

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MtrVelBlend

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
CrsMtrVel_MtrRadpS_T_f32	345.96		
FinMtrVel_MtrRadpS_T_f32	-87.43		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	13		
Name	Actual Value	Expected Value	Result
MtrVelBlend()	345.959991	345.96	<b>✓</b>

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~

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MtrVel\_Per1

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Per1

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -l\$(PROJECTROOT)\NxtrLitb\include -l\$(PROJECTROOT)\StdDef\include -l\$(PROJECTROOT) \MtrVel_Digi\utp\contract -l\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -l\$(PROJECTROOT)\MtrVel_Digi\include -l\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text



Module 'Mtr/vel\_Digi'

Name of Tester:Sunita Kore
Code File(s) Under Test:Sa\_Mtr/vel.c
Code File(s) Version:5
Module Design Document:Motor/velocity\_MDD.doc
Module Design Document Version:0
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470\_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):1394
Total FLASH Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/E2/E0/214
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""Mtr/vel\_Per1"\*, the values of variable ""MechMtrPos2Timestamp\_USec\_u32"\* and
"Mtr/vel\_PrevMtrPosSampleTime2\_uS\_M\_u32"\* are taken slightly different from the actual range, to get value of ""DeltaTime2\_uS\_T\_f32"\* in the range of ""1800 - 2200"\* as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"\*, the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""CalcCoarseVel"\*, the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"\*, the path 'P5' i.e. "(SumDeltaSqT\_uSsq\_T\_f32 > FLT\_EPSILON) = FALSE\*\* has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3\_TimeBuffer\_uS\_M\_u16p0[4][8]"\*) and have been given value as '0' for all array elements.

Note5: ""CBD\_Sandbox\_dbg.map" is embedded for reference."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles: TS 1.1 4543 Cycles TS 1.2 4546 Cycles

Description VECTOR DESCRIPTION:

TS 1.1 "Shortest Execution Path:

TS 1.1 "Shortest Execution Path: if (MtrVel\_PosBuffer\_Rad\_T\_f32 > D\_PI\_ULS\_F32) = False; (MtrVel\_PosBuffer\_Rad\_T\_f32 <= D\_PI\_ULS\_F32) = False (MtrVel\_MtrRadpS\_T\_f32>=D\_MTRVELHILMT\_MTRRADPS\_F32)=False (MtrVel\_HwVel\_HwRadpS\_M\_f32>=D\_MAXHANDWHEELVEL\_HWRADPS\_F32)=False" TS 1.2 "Longest Execution Path: if (MtrVel\_PosBuffer\_Rad\_T\_f32 > D\_PI\_ULS\_F32) = False; (MtrVel\_PosBuffer\_Rad\_T\_f32 <= -D\_PI\_ULS\_F32) = False (MtrVel\_MtrRadpS\_T\_f32>=D\_MTRVELHILMT\_MTRRADPS\_F32)=True (MtrVel\_HwVel\_HwRadpS\_M\_f32>=D\_MAXHANDWHEELVEL\_HWRADPS\_F32)=Fa"

Input Value
0
2
7
4
1.12
1.13
1.14
1.15
1.16
1.17
1.18
1.19
1.2
1.21
1.22
1.23
1.24
1.25
1.26
1.27
1.28
1.29
1.3
1.31
1.32
1.33
1.34
1.35
1.35
1.37
1.37
1.38
1.39
1.4
1.41
1.42
166
65268
65330
65392
65454
65516
42
104
2042
2104
2166
1732
1794
1856
1918
1980

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800		
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestar	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u(	)p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	lwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-4.80000067	-4.8 ± 0.0625	<b>✓</b>
MtrVel_MtrVelMRF_MtrRadpS_M_f32	40.000076	40 ± 0.0625	✓
MtrVel_OldPosBuf_Cnt_M_u08	1	1	<b>✓</b>
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.00182160223	0 ± 0.0625	<b>✓</b>
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-4.80000067	-4.8 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-40.0000076	-40 ± 0.0625	✓
LIAMINIA DI AMALINIA DEL MADELAMIDA DE LA COMPANIA DEL COMPANIA DEL COMPANIA DE LA COMPANIA DE L	40,000070	40 - 0 0005	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•

40.0000076

-4.80000067

40.0000076

40 ± 0.0625

-4.8 ± 0.009

40 ± 0.0625

Test Step 1.2 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

 $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ 

tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value

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Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1] MtrVel3_PosBuffer_Rad_M_f32[1][2]	3 3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2] MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.72 3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.28
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52 4.6
MtrVel3_PosBuffer_Rad_M_f32[3][5] MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956 6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][6] MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400 1380
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7700
MtrVel3 TimeBuffer uS M u16p0[2][2]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6504 6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6566 6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-227.1569001
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08	0 3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.03
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	14
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1

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MtrVel\_Per1

Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	t_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	s_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	40.5	40.5 ± 0.0625	-
		40.0 ± 0.00±0	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32 MtrVel_OldPosBuf_Cnt_M_u08	1350 3		~
		1350 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	1350 ± 0.0625	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08	3 0	1350 ± 0.0625 3 0	
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3 0 0.00182160223	1350 ± 0.0625 3 0 0 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3 0 0.00182160223 897976	1350 ± 0.0625 3 0 0 ± 0.0625 897976	<b>V</b>
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	3 0 0.00182160223 897976 40.5	1350 ± 0.0625 3 0 0 ± 0.0625 897976 40.5 ± 0.009	<b>*</b>
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	3 0 0.00182160223 897976 40.5 1350	1350 ± 0.0625 3 0 0 ± 0.0625 897976 40.5 ± 0.009 1350 ± 0.0625	<b>*</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•

MtrVel\_Per1

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Test Case 2: Boundary Test

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MtrVel\_Per1



```
Specification
```

```
Performance Metrics
(With "None" Instrumentation and WithPS Environment)
      CPU Cycles:
  CPU Cycles:
TS 2.1 4543 Cycles
TS 2.2 4416 Cycles
TS 2.3 4456 Cycles
TS 2.4 4445 Cycles
TS 2.5 4428 Cycles
TS 2.6 4456 Cycles
TS 2.7 4367 Cycles
TS 2.6 4456 Cycles
TS 2.7 4367 Cycles
TS 2.8 4442 Cycles
TS 2.9 4522 Cycles
TS 2.10 4414 Cycles
TS 2.11 4446 Cycles
TS 2.11 4446 Cycles
TS 2.12 4382 Cycles
TS 2.13 4429 Cycles
TS 2.14 4483 Cycles
TS 2.15 4429 Cycles
TS 2.16 4386 Cycles
TS 2.17 4429 Cycles
TS 2.19 4353 Cycles
TS 2.19 4458 Cycles
TS 2.20 4458 Cycles
TS 2.21 4429 Cycles
TS 2.22 4458 Cycles
TS 2.23 4430 Cycles
TS 2.24 4436 Cycles
TS 2.25 4429 Cycles
TS 2.25 4429 Cycles
TS 2.26 4430 Cycles
TS 2.27 4429 Cycles
TS 2.28 4458 Cycles
TS 2.29 4458 Cycles
TS 2.30 4504 Cycles
TS 2.31 4428 Cycles
TS 2.33 4457 Cycles
TS 2.34 4477 Cycles
TS 2.35 4447 Cycles
TS 2.36 4487 Cycles
TS 2.36 4487 Cycles
TS 2.37 4410 Cycles
TS 2.38 4411 Cycles
TS 2.39 4458 Cycles
TS 2.39 4458 Cycles
  TS 2.38 4411 Cycles
TS 2.39 4458 Cycles
TS 2.40 4430 Cycles
TS 2.41 4429 Cycles
TS 2.42 4381 Cycles
TS 2.43 4430 Cycles
TS 2.45 4436 Cycles
TS 2.45 4436 Cycles
TS 2.46 4458 Cycles
TS 2.47 4523 Cycles
TS 2.48 4446 Cycles
VECTOR DESCRIPTION:
```

#### Description

```
TS2.1All Min
 TS2.2All Max
TS2.3AsstAssemblyPolarity_Cnt_s08==>Min
TS2.4AsstAssemblyPolarity_Cnt_s08==>Max
TS2.5AsstAssemblyPolarity_Cnt_s08==>Pos
 TS2.6MechMtrPos2Timestamp_USec_u32==>Min
TS2.7MechMtrPos2Timestamp_USec_u32==>Max
 TS2.8MechMtrPos2Timestamp_USec_u32==>Pos
TS2.9MechMtrPos2 Rev_u0p16==>Min
TS2.10MechMtrPos2 Rev_u0p16==>Max
TS2.11MechMtrPos2 Rev_u0p16==>Pos
TS2.12MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Min
TS2.13MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32==>Max
 TS2.14MtrVel PrevMtrPosMechMtrPos2 Rad M f32==>Pos
 TS2.15MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Min
TS2.16MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Max
TS2.17MtrVel_PrevMtrPosSampleTime2_uS_M_u32==>Pos
 TS2.18MtrVel_OsBufSelect_Cnt_M_u08==>Min
TS2.19MtrVel_OsBufSelect_Cnt_M_u08==>Max
TS2.20MtrVel_OsBufSelect_Cnt_M_u08==>Pos
TS2.20Mit/Veij_OSBUISelect_Crit_M_u08(==>P0S
TS2.21Mit/Veij_OSBUfPos_Cnt_M_u08[4]==>Min
TS2.22Mit/Veij_OSBufPos_Cnt_M_u08[4]==>Pos
TS2.23Mit/Veij_OldPosBuf_Cnt_M_u08[4]==>Pos
TS2.24Mit/Veij_OldPosBuf_Cnt_M_u08==>Min
 TS2.25MtrVel_OldPosBuf_Cnt_M_u08==>Max
TS2.26MtrVel_OldPosBuf_Cnt_M_u08==>Pos
TS2.27MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Min
 TS2.28MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Max
TS2.29MtrVel3_TimeBuffer_uS_M_u16p0[4][8]==>Pos
TS2.30MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Min
 TS2.31MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Max
TS2.32MtrVel3_PosBuffer_Rad_M_f32[4][8]==>Pos
TS2.33t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Min
 TS2.34L MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Max
TS2.35t_MtrVelBlendTblX_MtrRadpS_u12p4[2]==>Pos
TS2.36k_GearRatio_Uls_f32==>Min
 TS2.37k_GearRatio_Uls_f32==>Max
 TS2.38k_GearRatio_Uls_f32==>Pos
TS2.39MtrVel_HwVel_HwRadpS_M_f32==>Min
 TS2.40MtrVel_HwVel_HwRadpS_M_f32==>Max
TS2.40MtrVel_HwVel_HwRadpS_M_f32==>Max
TS2.41MtrVel_HwVel_HwRadpS_M_f32==>Pos
TS2.42MtrVel_HwVel_HwRadpS_M_f32==>Zero
TS2.43MtrVel_HwVel_HwRadpS_M_f32==>Neg
TS2.44MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Min
TS2.45MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Max
TS2.46MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Pos
TS2.47MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Zero
TS2.48MtrVel_MtrVelMRF_MtrRadpS_M_f32==>Neg
```

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Test Step 2.1 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0 0
MtrVel3_PosBuffer_Rad_M_f32[1][1] MtrVel3_PosBuffer_Rad_M_f32[1][2]	0
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3 PosBuffer Rad M f32[1][6]	0
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0
MtrVel3_PosBuffer_Rad_M_f32[3][4] MtrVel3_PosBuffer_Rad_M_f32[3][5]	0
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]  MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	700 710
MtrVel3_TimeBuffer_uS_M_u16p0[1][4] MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400
MtrVel3_TimeBuffer_uS_M_u16p0[3][7] MtrVel_HwVel_HwPadpS_M_f32	19300 -42
MtrVel_HwVel_HwRadpS_M_f32 MtrVel_MtrVelMRF_MtrRadpS_M_f32	-42 -1350
MtrVel_OldPosBuf_Cnt_M_u08	0
	•

MtrVel\_Per1

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Input Value MtrVel\_OsBufSelect\_Cnt\_M\_u08 MtrVel\_PrevMtrPosMechMtrPos2\_Rad\_M\_f32 MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32 0 Rte\_Inst\_Sa\_MtrVel tgt\_Rte\_Inst\_Sa\_MtrVel T\_MtrVelBlendTblY\_Uls\_u2p14[0] 0 T\_MtrVelBlendTblY\_Uls\_u2p14[1] 16384 k\_GearRatio\_Uls\_f32 0  $t\_MtrVelBlendTblX\_MtrRadpS\_u12p4[0]$ 0 t\_MtrVelBlendTblX\_MtrRadpS\_u12p4[1] 0  $tgt\_MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08.value$ -1 tgt\_MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32.value 1800 tgt\_MtrVel\_Per1\_MechMtrPos2\_Rev\_u0p16.value tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08 tgt\_MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08 tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32 tgt MtrVel Per1 HandwheelVel HwRadpS f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32 tgt\_MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MechMtrPos2\_Rev\_u0p16 tgt\_MtrVel\_Per1\_MechMtrPos2\_Rev\_u0p16  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32$ tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32 tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32$  $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32$ tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32 **Actual Value Expected Value** Name Result MtrVel\_HwVel\_HwRadpS\_M\_f32  $0 \pm 0.0625$ -0 MtrVel\_MtrVelMRF\_MtrRadpS\_M\_f32 0 ± 0.0625 0 MtrVel\_OldPosBuf\_Cnt\_M\_u08 1 MtrVel\_OsBufSelect\_Cnt\_M\_u08 2 MtrVel PrevMtrPosMechMtrPos2 Rad M f32  $0 \pm 0.0625$ 0 MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32 1800 1800 tgt MtrVel Per1 HandwheelVel HwRadpS f32.value 0 ± 0.009 -0  $tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32.value$ -0  $0 \pm 0.0625$ tgt MtrVel Per1 MotorVelMRF MtrRadpS f32.value 0 0 ± 0.0625  $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ -0  $0 \pm 0.009$ tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value 0 0 ± 0.0625

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>V</b>

Test Step 2.2 (Repeat Count = 1)	🗸
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	7
MtrVel3_OsBufPos_Cnt_M_u08[1]	7
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][4] MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433 6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000		
MtrVel3 TimeBuffer uS M u16p0[0][1]	11875		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150 2275		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6.283185307 4294965495		
Rte_Inst_Sa_MtrVel			
T MtrVelBlendTblY Uls u2p14[0]	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn	t_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	_	
tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	. –	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	1	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	· ·
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0 3	0 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08 MtrVel_OsBufSelect_Cnt_M_u08	0	3	
MtrVel_OsBurselect_Cnt_m_uu8  MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	
MtrVel PrevMtrPosSampleTime2 u.S. M. u32	4294967295	4294967295	

4294967295

0

4294967295

0 ± 0.009

MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32

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Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)	Invest Malice	
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
/trVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12	
/trVel3_PosBuffer_Rad_M_f32[0][1]	1.13	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15	
htrVel3_PosBuffer_Rad_M_f32[0][4]	1.16	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	1.17	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	1.18	
ftrVel3_PosBuffer_Rad_M_f32[0][7]	1.19	
/trVel3_PosBuffer_Rad_M_f32[1][0]	1.2	
/ltrVel3_PosBuffer_Rad_M_f32[1][1]	1.21	
/trVel3_PosBuffer_Rad_M_f32[1][2]	1.22	
/trVel3_PosBuffer_Rad_M_f32[1][3]	1.23	
/trVel3_PosBuffer_Rad_M_f32[1][4]	1.24	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25	
/trVel3_PosBuffer_Rad_M_f32[1][6]	1.26	
/trVel3_PosBuffer_Rad_M_f32[1][7]	1.27	
/trVel3_PosBuffer_Rad_M_f32[2][0]	1.28	
/trVel3_PosBuffer_Rad_M_f32[2][1]	1.29	
ItrVel3_PosBuffer_Rad_M_f32[2][2]	1.3	
1trVel3_PosBuffer_Rad_M_f32[2][3]	1.31	
/trVel3_PosBuffer_Rad_M_f32[2][4]	1.32	
MtrVel3 PosBuffer Rad M f32[2][5]	1.33	
/trVel3_PosBuffer_Rad_M_f32[2][6]	1.34	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35	
/trVel3_PosBuffer_Rad_M_f32[3][0]	1.35	
/trVel3_PosBuffer_Rad_M_f32[3][1]	1.37	
/trVel3_PosBuffer_Rad_M_f32[3][2]	1.37	
/trVel3_PosBuffer_Rad_M_f32[3][3]	1.38	
/trVel3_PosBuffer_Rad_M_f32[3][4]	1.39	
ttrVel3_PosBuffer_Rad_M_f32[3][5]	1.4	
htrVel3_PosBuffer_Rad_M_f32[3][6]	1.41	
	1.42	
htrVel3_PosBuffer_Rad_M_f32[3][7]	166	
htrVel3_TimeBuffer_uS_M_u16p0[0][0]		
MtVel3_TimeBuffer_uS_M_u16p0[0][1]	65268	
/trVel3_TimeBuffer_uS_M_u16p0[0][2]	65330	
/trVel3_TimeBuffer_uS_M_u16p0[0][3]	65392	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454	
/trVel3_TimeBuffer_uS_M_u16p0[0][5]	65516	
htrVel3_TimeBuffer_uS_M_u16p0[0][6]	42	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104	
ltrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166	
ftrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732	
ftrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794	
ltrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856	
ftrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918	
/trVel3_TimeBuffer_uS_M_u16p0[1][7]	1980	
/trVel3_TimeBuffer_uS_M_u16p0[2][0]	100	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200	

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MtrVel\_Per1



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Name	Input Value		
MtrVel3 TimeBuffer uS M u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800		
MtrVel_HwVel_HwRadpS_M_f32	-35.5619548		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-890.9661543		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity	/_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	amp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	ı0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRa		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_I	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	<b>✓</b>
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-555.555542	-555.555542 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	✓
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	555.555542	555.555542 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

-555.555542

-555.555542

42

-555.555542 ± 0.0625

-555.555542 ± 0.0625

42 ± 0.009

Test Step 2.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

 $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ 

tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value

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Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.19
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.21
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.23 2.24
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.25
MtrVel3_PosBuffer_Rad_M_f32[1][3] MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.26
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.27
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.31
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.33
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.34
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.38
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.41
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.43
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250 65352
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]  MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65414
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]  MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	880 110
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880
MtrVel_HwVel_HwRadpS_M_f32	10.93737113
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-573.3094931
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	293048
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.13
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	294848



MtrVel\_Per1

Name	Input Value			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolari	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times	tamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_	u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRa	adpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrR	adpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel	_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_	MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-762.023987	-762.0239258 ± 0.0625	✓	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~	
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>✓</b>	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	~	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	294848	294848	<b>✓</b>	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	<b>✓</b>	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	<b>✓</b>	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	~	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~	
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-762.023987	-762.0239258 ± 0.0625	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.22
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.26
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.34
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.36
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.38
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.46
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.48
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.54
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.56
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.58
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.64
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.66
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.68
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.74
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.78
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111

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Name	Input Value			
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221			
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331			
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441			
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551			
MtrVel3_TimeBuffer_uS_M_u16p0[0][5] MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	661 624			
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630			
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702			
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750			
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806			
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856			
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901			
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945			
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990			
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030			
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175			
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277			
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339			
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]  MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65401			
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65463 65525			
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51			
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113			
MtrVel3 TimeBuffer uS M u16p0[3][0]	301			
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363			
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425			
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527			
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53			
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115			
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177			
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239			
MtrVel_HwVel_HwRadpS_M_f32	-2.767454803			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	676.0312533			
MtrVel_OldPosBuf_Cnt_M_u08	2			
MtrVel_OsBufSelect_Cnt_M_u08	3			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6046			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.14			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7846			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn	_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_ tgt_MtrVel_Per1_SysCHandwheelVel_HwR			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRi			
Name	Actual Value	Expected Value		Result
MtrVel HwRadpS M f32	-42	-42 ± 0.0625		Result
MtrVel MtrVelMRF MtrRadpS M f32	-968.545654	-968.5455933 ± 0.0625		
MtrVel_OldPosBuf_Cnt_M_u08	3	3		
MtrVel OsBufSelect Cnt M u08	0	0		
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	1.2566179	1.256617887 ± 0.0625		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7846	7846		
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009		
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-968.545654	-968.545593 ± 0.0625		•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-968.545654	-968.5455933 ± 0.0625		-
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009		•
tot MtrVel Per1 SvsCMotorVelMRF MtrRadpS f32.value	-968.545654	-968.5455933 ± 0.0625		<b>₩</b>

-968.545654

 $tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value$ 

-968.5455933 ± 0.0625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.6 (Repeat Count = 1)	lumid Value	
Name	Input Value	
/trVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
/trVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
/trVel3_PosBuffer_Rad_M_f32[0][0]	3.2	
/trVel3_PosBuffer_Rad_M_f32[0][1]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	2.47	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	2.52	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	2.57	
htrVel3_PosBuffer_Rad_M_f32[1][0]	2.62	
ltrVel3_PosBuffer_Rad_M_f32[1][1]	2.67	
htrVel3_PosBuffer_Rad_M_f32[1][2]	2.72	
ftrVel3_PosBuffer_Rad_M_f32[1][3]	2.77	
/trVel3_PosBuffer_Rad_M_f32[1][4]	2.82	
/trVel3_PosBuffer_Rad_M_f32[1][5]	2.87	
/trVel3_PosBuffer_Rad_M_f32[1][6]	2.92	
/trVel3_PosBuffer_Rad_M_f32[1][7]	2.97	
/trVel3_PosBuffer_Rad_M_f32[2][0]	3.02	
/ltrVel3_PosBuffer_Rad_M_f32[2][1]	3.07	
/ltrVel3_PosBuffer_Rad_M_f32[2][2]	3.12	
/trVel3_PosBuffer_Rad_M_f32[2][3]	3.17	
/trVel3_PosBuffer_Rad_M_f32[2][4]	3.22	
/trVel3_PosBuffer_Rad_M_f32[2][5]	3.27	
/trVel3_PosBuffer_Rad_M_f32[2][6]	3.32	
/trVel3_PosBuffer_Rad_M_f32[2][7]	3.37	
/trVel3_PosBuffer_Rad_M_f32[3][0]	3.42	
/trVel3_PosBuffer_Rad_M_f32[3][1]	3.47	
/trVel3_PosBuffer_Rad_M_f32[3][2]	3.52	
/trVel3_PosBuffer_Rad_M_f32[3][3]	3.57	
/trVel3_PosBuffer_Rad_M_f32[3][4]	3.62	
/trVel3_PosBuffer_Rad_M_f32[3][5]	3.67	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72	
/trVel3_PosBuffer_Rad_M_f32[3][7]	3.77	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1926	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988	
/trVel3_TimeBuffer_uS_M_u16p0[0][2]	1554	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616	
/trVel3_TimeBuffer_uS_M_u16p0[0][4]	1678	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314	
htrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438	
/trVel3_TimeBuffer_uS_M_u16p0[1][4]	2004	
ItrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066	
ltrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128	
ItrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190	
htrVel3_TimeBuffer_uS_M_u16p0[2][0]	927	
/trVel3_TimeBuffer_uS_M_u16p0[2][1]	900	
/trVel3_TimeBuffer_uS_M_u16p0[2][2]	894	
/trVel3_TimeBuffer_uS_M_u16p0[2][3]	858	
/trVel3_TimeBuffer_uS_M_u16p0[2][4]	801	
/trVel3_TimeBuffer_uS_M_u16p0[2][5]	791	
/trVel3_TimeBuffer_uS_M_u16p0[2][6]	755	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881		
MtrVel3 TimeBuffer uS M u16p0[3][3]	835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606		
MtrVel_HwVel_HwRadpS_M_f32	-16.68913257		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-412.0075986		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.4		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.17		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	12		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1800		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPola	rity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hw	/RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Time	stamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	/_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mtrl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	967.551514	967.5514526 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1800	1800	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	967.551514	967.5514526 ± 0.0625	•
A A A A A A A A A A A A A A A A A A A	007.554544	007 5544500 : 0 0005	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

967.551514

967.551514

42

967.5514526 ± 0.0625

967.5514526 ± 0.0625

42 ± 0.009

Test Step 2.7 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
MtrVel3_OsBufPos_Cnt_M_u08[1]	7	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value

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Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][5] MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433 6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500 59375
MtrVel3_TimeBuffer_uS_M_u16p0[1][5] MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275
MtrVel3 TimeBuffer uS M u16p0[2][3]	2400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900
MtrVel_HwVel_HwRadpS_M_f32	42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350
MtrVel_OldPosBuf_Cnt_M_u08	2
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosSampleTime2_US_M_u32	6.283185307 4294965495
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	
Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0]	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[u] T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	1
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_mtrvei_Per1_AsstassembiyPolanty_Cnt_su8 tgt_mtrvei_Per1_HandwheelVel_HwRadpS_f32





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f	gt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_t	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRad	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	0	0 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.8 (Repeat Count = 1)	🗸
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.45
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	675
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	635
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	600
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	576
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	539
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	500

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	465		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7380		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7442		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7504		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7566		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7132		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7194		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7318		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8156		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8218		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8280		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8342		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8404		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8466		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8032		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8094		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]  MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1111		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1006 967		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	910		
	878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	793		
MtrVel3 TimeBuffer uS M u16p0[3][7]	739		
MtrVel_HwVel_HwRadpS_M_f32	-21.01813388		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	969.1185812		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3878		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.19		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	15		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	5678		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn	t_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1332.33911	1332.339233 ± 0.0625	-
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	-
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5678	5678	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	<b>*</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1332.33911	1332.339233 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

1332.33911

tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value

1332.339233 ± 0.0625





Test Step 2.9 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44 2.52
MtrVel3_PosBuffer_Rad_M_f32[0][3] MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.62
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.68
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.32 3.4
MtrVel3_PosBuffer_Rad_M_f32[1][6] MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.48
MtrVel3_PosBuffer_Rad_M_f32[1][7]  MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.64
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][1] MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.28 4.36
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.76
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6142
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6204
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6266
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]  MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5894 5956
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6018
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1520
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1480
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	1380   7777
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7770
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]  MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6194 6256
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442
MtrVel_HwVel_HwRadpS_M_f32	-15.40252668
	-227.1569001
MtrVel_MtrVelMRF_MtrRadpS_M_f32	





Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyl	Polarity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel	_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos21	Fimestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_	_Rev_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_	MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_	_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwhe	elVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVell	MRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	29.8089962	29.8089797 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	149.044983	149.0448914 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	29.8089962	29.8089797 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	29.8089962	29.8089797 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	<b>✓</b>
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.64		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91		
MtrVel3_PosBuffer_Rad_M_f32[3][6] MtrVel3_PosBuffer_Rad_M_f32[3][7]	5 5.09		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	30300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	30250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	30200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	30150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	30100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	30050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	30020		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	30000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	28876		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	28938		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	29000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	28566		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	28628		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	28690 28752		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	28/52 28814		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7] MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	25400		
MtrVel3 TimeBuffer uS M u16p0[2][1]	25300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	25200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	25160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	25100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	25060		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	25030		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	25000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	29452		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	29514		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	29576		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	29638		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	29700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	29266		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	29328 29390		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	-34.05995619		
MtrVel_HwVel_HwRadpS_M_f32 MtrVel_MtrVelMRF_MtrRadpS_M_f32	258.9155765		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	3.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6478		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.21		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	8378		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_I mestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	<b>✓</b>
1417/18 1418 1418 0 B 14480			

6.28308964

8378

MtrVel\_PrevMtrPosMechMtrPos2\_Rad\_M\_f32

MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32

6.283089433 ± 0.0625

8378

42 ± 0.009

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MtrVel\_Per1

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Total Char 2 44 (Parasat Count = 4)	
Test Step 2.11 (Repeat Count = 1)	<b>▼</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.62
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.82
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.12
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.52
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.62
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.82
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.02
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.22
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.42
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.72
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.92
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.02
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.12
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.22
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.32
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1110
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1172
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1234
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	862
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	924
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	986
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1048
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	4657
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	4609
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	4576
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	4500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	4480
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	4450
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	4401
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	4387
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2124
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2186

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Mtrvei_Peri			MACICAU
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2248		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2310		
MtrVel3 TimeBuffer uS M u16p0[2][4]	2372		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2434		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2062		
MtrVel3 TimeBuffer uS M u16p0[3][0]	7823		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	7800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	7760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	7700		
MtrVel3 TimeBuffer uS M u16p0[3][4]	7650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	7610		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	7570		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	7500		
MtrVel_HwVel_HwRadpS_M_f32	-41.71834391		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-838.1024514		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1653		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.22		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3553		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestam	np_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	wRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mt	rRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	-
MtrVel_MtrVelMRF_MtrRadpS_M_f32	285.048859	285.0487671 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	2	2	•
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3553	3553	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	-
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-285.048859	-285.0487671 ± 0.0625	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

285.048859

285.048859

-42

285.0487671 ± 0.0625

285.0487671 ± 0.0625

-42 ± 0.009

Test Step 2.12 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.34	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.45	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.56	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.67	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.78	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

 $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ 





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.89
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.33
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.88
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.99
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.21
	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][2]	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.43
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.54
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.65
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.98
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.09
MtrVel3 PosBuffer Rad M f32[3][2]	5.2
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.31
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.53
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.64
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.75
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	5678
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	5600
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	5570
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5500
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5460
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	5340
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	5300
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	13014
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	13076
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	13138
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	13200
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	12766
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	12828
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	12890
MtrVel3 TimeBuffer uS M u16p0[1][7]	12952
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8341
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8300
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8240
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8200
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	8150
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8100
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8050
MtrVel3 TimeBuffer uS M u16p0[2][7]	8000
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	14590
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	14652
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	14714
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	14776
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	14838
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	14900
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	14466
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	14528
MtrVel_HwRadpS_M_f32	39.33634806
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1200.540927
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	3
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2100
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k GearRatio Uls f32	0.23
k_GearRatio_Uls_f32 t_MtrVelRiendThIX_MtrRadpS_u12p4[0]	0.23
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3
t_MtrVelBlendTblX_MtrRadpS_u12p4[0] t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3 3
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Ci	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	5_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hwl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-661.377869	-661.3778076 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4000	4000	<b>~</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-661.377869	-661.377808 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-661.377869	-661.3778076 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-661.377869	-661.3778076 ± 0.0625	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.13 (Repeat Count = 1)		~
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.32	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.44	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.68	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.8	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.92	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.04	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.16	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.28	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.4	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.52	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.64	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.76	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.88	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.12	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.24	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.36	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.48	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.6	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.72	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.84	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.96	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.08	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	2069	





Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	2131		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	2193		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1759		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1821		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1883		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1945		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	2007		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3410		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	3370		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	3333		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	3290		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	3207		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	3175		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	3100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2450 2512		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]  MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2574		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2636		
	2698		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4] MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2760		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2822		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2388		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6890		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6810		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6700		
MtrVel3 TimeBuffer uS M u16p0[3][4]	6680		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6610		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6504		
MtrVel_HwVel_HwRadpS_M_f32	20.37118572		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-651.7195906		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5068		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTbiY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.24		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	6968		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt	t_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16	3	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	adpS_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-330.714172	-330.7141418 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	2	2	
MtrVel_OsBufSelect_Cnt_M_u08	3	3	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6968	6968	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-330.714172	-330.7141418 ± 0.0625	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-330.714172	-330.7141418 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	
	-330.714172	-330.7141418 ± 0.0625	





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 ChecknointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	-

Test Step 2.14 (Repeat Count = 1)		
lame	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
ItrVel3_OsBufPos_Cnt_M_u08[2]	3	
ltrVel3_OsBufPos_Cnt_M_u08[3]	4	
ltrVel3_PosBuffer_Rad_M_f32[0][0]	3.3	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	2.51	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	2.64	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	2.77	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.9	
trVel3_PosBuffer_Rad_M_f32[0][5]	3.03	
trVel3_PosBuffer_Rad_M_f32[0][6]	3.16	
trVel3_PosBuffer_Rad_M_f32[0][7]	3.29	
trVel3_PosBuffer_Rad_M_f32[1][0]	3.42	
trVel3_PosBuffer_Rad_M_f32[1][1]	3.55	
trVel3_PosBuffer_Rad_M_f32[1][2]	3.68	
trVel3_PosBuffer_Rad_M_f32[1][3]	3.81	
trVel3_PosBuffer_Rad_M_f32[1][4]	3.94	
trVel3_PosBuffer_Rad_M_f32[1][5]	4.07	
trVel3_PosBuffer_Rad_M_f32[1][6]	4.2	
trVel3_PosBuffer_Rad_M_f32[1][7]	4.33	
trVel3_PosBuffer_Rad_M_f32[2][0]	4.46	
trVel3_PosBuffer_Rad_M_f32[2][1]	4.59	
trVel3_PosBuffer_Rad_M_f32[2][2]	4.72	
trVel3_PosBuffer_Rad_M_f32[2][3]	4.85	
trVel3_PosBuffer_Rad_M_f32[2][4]	4.98	
trVel3_PosBuffer_Rad_M_f32[2][5]	5.11	
trVel3_PosBuffer_Rad_M_f32[2][6]	5.24	
trVel3_PosBuffer_Rad_M_f32[2][7]	5.37	
trVel3_PosBuffer_Rad_M_f32[3][0]	5.5	
trVel3_PosBuffer_Rad_M_f32[3][1]	5.63	
trVel3_PosBuffer_Rad_M_f32[3][2]	5.76	
trVel3_PosBuffer_Rad_M_f32[3][3]	5.89	
trVel3_PosBuffer_Rad_M_f32[3][4]	6.02	
trVel3_PosBuffer_Rad_M_f32[3][5]	3.3	
trVel3_PosBuffer_Rad_M_f32[3][6]	3.4	
trVel3_PosBuffer_Rad_M_f32[3][7]	3.5	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	9770	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	9832	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	9398	
trVel3 TimeBuffer uS M u16p0[0][3]	9460	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	9522	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	9584	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	9646	
trVel3_TimeBuffer_uS_M_u16p0[0][7]	9708	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	20450	
trVel3_TimeBuffer_uS_M_u16p0[1][1]	20401	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	20350	
trVel3 TimeBuffer uS M u16p0[1][3]	20306	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	20250	
trVel3_TimeBuffer_uS_M_u16p0[1][5]	20200	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	20156	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	20107	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	35035	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	35000	
	34070	
ltrVel3_TimeBuffer_uS_M_u16p0[2][2] ltrVel3_TimeBuffer_uS_M_u16p0[2][3]	34070	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900	
htrVel3_TimeBuffer_uS_M_u16p0[2][5] htrVel3_TimeBuffer_uS_M_u16p0[2][6]	33845 33000	

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MtrVel\_Per1

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	8928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	8990		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9052		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9114		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9176		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	8742		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	8804		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	8866		
MtrVel_HwVel_HwRadpS_M_f32	-5.642567992		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1302.259389		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	85754		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.25		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	87654		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	0	0	<b>✓</b>
MtrVel OsBufSelect Cnt M u08	1	1	<b>✓</b>

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel_OsBufSelect_Cnt_M_u08	1	1	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87654	87654	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.15 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.76	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.92	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.39	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.56	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.71	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.88	





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.03
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.36
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.52
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.68
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.84
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.16
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.48
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.64
Mtr/el3_PosBuffer_Rad_M_f32[2][5]	5.8 5.96
MtrVel3_PosBuffer_Rad_M_f32[2][6] MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.12
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.8
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.2
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.3
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004 2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]  MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3 TimeBuffer uS M u16p0[2][1]	900
MtrVel3 TimeBuffer uS M u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1001
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwVel_HwRadpS_M_f32	30.78114766
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-326.589513
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08  AttrVel_BroyMtrDecMachMtrDec2_Red_M_f22	1
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32  Pte_Inst_Sa_MtrVel	0 tgt_Rte_Inst_Sa_MtrVel
Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0]	Igi_Rie_irisi_Sa_Mili vei
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_UIs_f32	0.28
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	1900
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_	<u>f</u> 32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-256.191681	-256.1917114 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1900	1900	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-256.191681	-256.1917114 ± 0.0625	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

lame	Input Value	
/trVel3 OsBufPos Cnt M u08[0]	7	
/trVel3 OsBufPos Cnt M u08[1]	7	
htrVel3_OsBufPos_Cnt_M_u08[2]	7	
htrVel3_OsBufPos_Cnt_M_u08[3]	7	
ItrVel3 PosBuffer Rad M f32[0][0]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
ItrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
ItrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
ltrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
trVel3 PosBuffer Rad M f32[0][7]	6.283089433	
trVel3 PosBuffer Rad M f32[1][0]	6.283089433	
trVel3 PosBuffer Rad M f32[1][1]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433	
rVel3 PosBuffer Rad M f32[1][6]	6.283089433	
trVel3 PosBuffer Rad M f32[1][7]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433	
trVel3 PosBuffer Rad M f32[2][3]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433	
rVel3 PosBuffer Rad M f32[3][1]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433	
rVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433	
trVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433	
rVel3 TimeBuffer uS M u16p0[0][0]	12000	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	11875	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	11750	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	11625	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	11500	
trVel3 TimeBuffer uS M u16p0[0][5]	11375	

MtrVel\_Per1

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	59875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6025		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.283185307		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294965495		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0] T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	10		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4294967295		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tgt MtrVel Per1 AsstAssemblyPolarity Cn	t e08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt MtrVel Per1 MotorVelMRF MtrRadpS	_	
tgt Rte Inst Sa MtrVel.MtrVel Per1 SysCHandwheelVel HwRadpS f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	· <del>-</del>	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	0	0 ± 0.0625	- TOOUIL
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0	0 ± 0.0625	
MtrVel OldPosBuf Cnt M u08	3	3	
MtrVel OsBufSelect Cnt M u08	0	0	~
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	6.28308964	6.283089433 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4294967295	4294967295	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	0	0 ± 0.009	_
tgt MtrVel Per1 MotorVelCRF MtrRadpS f32.value	0	0 ± 0.0625	<b>~</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	0	0 ± 0.0625	~
tgt MtrVel Per1 SysCHandwheelVel HwRadpS f32.value	0	0 ± 0.009	•
tat MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	0	0 + 0.0625	

Test Step Call Trace	Test Step Call Trace			V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

0 ± 0.0625

0





Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.48 2.66
MtrVel3_PosBuffer_Rad_M_f32[0][1]  MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.02
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.38
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.55
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.74
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.91
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.1
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.27
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.46
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.7
MtrVel3_PosBuffer_Rad_M_f32[1][7] MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.8 1.9
MtrVel3_PosBuffer_Rad_M_f32[2][0] MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.9
MtrVel3_PosBuffer Rad M f32[2][2]	2.1
MtrVel3_r osbuffer_Rad_M_i32[2][3]	2.2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.7
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.8
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.2 3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6] MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.4
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]  MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64570 64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][4] MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
Mtr/cl3_TimeBuffer_uS_M_u16p0[3][0]	500
Mtr/el3_TimeBuffer_uS_M_u16p0[3][1] Mtr/el3_TimeBuffer_uS_M_u16p0[3][2]	520 540
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]  MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel_HwVel_HwRadpS_M_f32	18.08023292
MtrVel_MtrVelMRF_MtrRadpS_M_f32	654.1182904
MtrVel_OldPosBuf_Cnt_M_u08	2

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Name	Input Value			
MtrVel_OsBufSelect_Cnt_M_u08	3			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.2			
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	8078			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.3			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8			
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	9978			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPola	rity_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hw	vRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Time	estamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	v_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_Mtrl	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mtr	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	el_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRf	F_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-752.657959	-752.6581421 ± 0.0625	<b>✓</b>	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	<b>✓</b>	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	<b>✓</b>	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	-	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	9978	9978	<b>✓</b>	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	-	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	<b>✓</b>	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	<b>✓</b>	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	<b>✓</b>	
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-752.657959	-752.6581421 ± 0.0625	<b>✓</b>	

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	<b>✓</b>
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.18 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.88
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.07
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.26
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.45
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.63
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.83
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4.01
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.3
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.59
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.41
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.52
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.63
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.74
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.85
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.96
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.07
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.18
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.51		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.62		
	2.73		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.84		
MtrVel3_PosBuffer_Rad_M_f32[3][1]			
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.95		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.06		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.17		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.28		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.39		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]  MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]  MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]			
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3 TimeBuffer uS M u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel HwVel HwRadpS M f32	-10.02764416		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	570.9573146		
MtrVel_OldPosBuf_Cnt_M_u08	1		
	0		
MtrVel_OsBufSelect_Cnt_M_u08			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1467		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.31		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3367		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolari	ity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_		
	Actual Value		Boord
Name MtA/oL Hu/oL Hu/DodoS M f22		Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	999.058167	999.0581665 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	0	0	•
MtrVel_OsBufSelect_Cnt_M_u08	1	1	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3367	3367	•
tgt MtrVel Per1 HandwheelVel HwRadpS f32.value	42	42 ± 0.009	<b>■</b>

42

42 ± 0.009





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	✓
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	999.058167	999.0581665 ± 0.0625	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.19 (Repeat Count = 1)	<b>~</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.92
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3.12
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.32
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.52
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.71
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4.11
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4.32
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.51
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.72
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.44
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.56
MtrVel3 PosBuffer Rad M f32[1][7]	1.68
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.8
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.92
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.04
MtrVel3_r osbutier_read_in_roz[z][2]  MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.16
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.28
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.4
	2.52
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.64
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.76
MtrVel3_PosBuffer_Rad_M_f32[3][0]	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.88
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.12
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.24
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.36
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.48
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.6
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355

MtrVel\_Per1

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Input Value MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][2] 9390 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][3] 9425 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][4] 9460 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][5] 9495 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][6] 9530 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][7] 9285 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][0] 9625  $MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][1]$ 9660 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][2] 9695 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][3] 9730 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][4] 9765  $MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][5]$ 9800 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][6] 9835 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][7] 9870 MtrVel\_HwVel\_HwRadpS\_M\_f32 -9.132052481 MtrVel\_MtrVelMRF\_MtrRadpS\_M\_f32 -850.7045946 MtrVel\_OldPosBuf\_Cnt\_M\_u08 MtrVel\_OsBufSelect\_Cnt\_M\_u08 3 MtrVel\_PrevMtrPosMechMtrPos2\_Rad\_M\_f32 1.7  $MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32$ 1444 Rte\_Inst\_Sa\_MtrVel tgt\_Rte\_Inst\_Sa\_MtrVel T\_MtrVelBlendTblY\_Uls\_u2p14[0] 0 16384 T\_MtrVelBlendTblY\_Uls\_u2p14[1] k\_GearRatio\_Uls\_f32 0.32 t\_MtrVelBlendTblX\_MtrRadpS\_u12p4[0] t\_MtrVelBlendTblX\_MtrRadpS\_u12p4[1] 4 tgt\_MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08.value -1  $tgt\_MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32.value$ 3444 tgt MtrVel Per1 MechMtrPos2 Rev u0p16.value 52429  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08$ tgt\_MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08 tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32 tgt MtrVel Per1 HandwheelVel HwRadpS f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32 tgt\_MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32 tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2 Rev u0p16 tgt MtrVel Per1 MechMtrPos2 Rev u0p16 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32 tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32 tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32$ tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32$ tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32

Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3444	3444	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1350	1350 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.20 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16	





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28 1.29
MtrVel3_PosBuffer_Rad_M_f32[2][1] MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3 PosBuffer Rad M f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460 480
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480 64495
MtrVel3_TimeBuffer_uS_M_u16p0[1][0] MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]  MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	560
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	580 600
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640
MtrVel HwRadpS M f32	-21.84720886
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1194.596858
MtrVel_OldPosBuf_Cnt_M_u08	1
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	442545
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.33
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	444545





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_	_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_	<u>f</u> 32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_U	JSec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_t	32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRad	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1027.41431	1027.414185 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	444545	444545	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1027.41431	1027.414185 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3 OsBufPos Cnt M u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.14
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.15
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.16
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.17
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.19
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.21
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.23
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.24
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.25
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.26
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.27
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.28
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.29
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.31
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.33
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.34
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.35
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.38
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2.419
MtrVel3_PosBuffer_Rad_M_f32[3][5]	2.42
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.43
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.44
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	800





Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	205		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	290		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	545		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	630		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	715		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1005		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1090		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1175		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1260		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1345		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1430		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1515		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6030		
	5970		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[2][3] MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5940 5910		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]  MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5880		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5790		
MtrVel3 TimeBuffer uS M u16p0[3][1]	5760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	5730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	5700		
MtrVel3 TimeBuffer uS M u16p0[3][4]	5670		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	5640		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	5610		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5580		
MtrVel_HwVel_HwRadpS_M_f32	-22.67909569		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-685.3485959		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5888		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T MtrVelBlendTblY Uls u2p14[1]	16384		
k_GearRatio_Uls_f32	0.34		
t MtrVelBlendTblX MtrRadpS u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7888		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_C	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p	16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Resu
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	
MtrVel_OsBufSelect_Cnt_M_u08	2	2	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7888	7888	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	
igi_wave_i en_eyeenanawneerve_nwraape_ioz.valae			



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.22 (Repeat Count = 1)		
lame	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
ItrVel3_OsBufPos_Cnt_M_u08[1]	7	
ltrVel3_OsBufPos_Cnt_M_u08[2]	7	
ltrVel3_OsBufPos_Cnt_M_u08[3]	7	
trVel3_PosBuffer_Rad_M_f32[0][0]	2.16	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	2.18	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	2.2	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	2.22	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.24	
trVel3_PosBuffer_Rad_M_f32[0][5]	2.26	
trVel3_PosBuffer_Rad_M_f32[0][6]	2.28	
trVel3_PosBuffer_Rad_M_f32[0][7]	2.3	
ltrVel3_PosBuffer_Rad_M_f32[1][0]	2.32	
trVel3_PosBuffer_Rad_M_f32[1][1]	2.34	
trVel3_PosBuffer_Rad_M_f32[1][2]	2.36	
trVel3_PosBuffer_Rad_M_f32[1][3]	2.38	
trVel3_PosBuffer_Rad_M_f32[1][4]	2.4	
trVel3_PosBuffer_Rad_M_f32[1][5]	2.42	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	2.44	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	2.46	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	2.48	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	2.5	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	2.52	
trVel3_PosBuffer_Rad_M_f32[2][3]	2.54	
trVel3_PosBuffer_Rad_M_f32[2][4]	2.56	
trVel3_PosBuffer_Rad_M_f32[2][5]	2.58	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	2.6	
ItrVel3_PosBuffer_Rad_M_f32[2][7]	2.62	
ltrVel3_PosBuffer_Rad_M_f32[3][0]	2.64	
ItrVel3_PosBuffer_Rad_M_f32[3][1]	2.66	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	2.68	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	2.7	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	2.72	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	2.74	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	2.76	
trVel3_PosBuffer_Rad_M_f32[3][7]	2.78	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	1000	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	900	
trVel3 TimeBuffer uS M u16p0[0][2]	800	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	700	
trVel3 TimeBuffer uS M u16p0[0][4]	600	
trVel3 TimeBuffer uS M u16p0[0][5]	500	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	400	
trVel3 TimeBuffer uS M u16p0[0][7]	300	
trVel3 TimeBuffer uS M u16p0[1][0]	5250	
trVel3_TimeBuffer_uS_M_u16p0[1][1]	5270	
trVel3 TimeBuffer uS M u16p0[1][2]	5290	
trVel3_TimeBuffer_uS_M_u16p0[1][3]	5310	
trVel3_TimeBuffer_uS_M_u16p0[1][4]	5330	
trVel3_TimeBuffer_uS_M_u16p0[1][5]	5350	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	5370	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	5390	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	5700	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	5720	
htrVel3_TimeBuffer_uS_M_u16p0[2][2]	5740	
htrVel3_TimeBuffer_uS_M_u16p0[2][3]	5760	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	5780	
/trVel3_TimeBuffer_uS_M_u16p0[2][5]	5800	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5840		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	200		
MtrVel3 TimeBuffer uS M u16p0[3][1]	100		
MtrVel3 TimeBuffer uS M u16p0[3][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65436		
MtrVel3 TimeBuffer uS M u16p0[3][4]	65336		
MtrVel3 TimeBuffer uS M u16p0[3][5]	65236		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	65136		
MtrVel3 TimeBuffer uS M u16p0[3][7]	65036		
MtrVel HwVel HwRadpS M f32	-36.06386733		
MtrVel MtrVelMRF MtrRadpS M f32	-814.620298		
MtrVel OldPosBuf Cnt M u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	3		
MtrVel PrevMtrPosSampleTime2 uS M u32	52544		
Rte Inst Sa MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T MtrVelBlendTblY Uls u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k GearRatio Uls f32	0.35		
t MtrVelBlendTblX MtrRadpS u12p4[0]	11		
t MtrVelBlendTblX MtrRadpS u12p4[1]	11		
tgt MtrVel Per1 AsstAssemblyPolarity Cnt s08.value	-1		
tgt MtrVel Per1 MechMtrPos2Timestamp USec u32.value	54544		
tgt MtrVel Per1 MechMtrPos2 Rev u0p16.value	13107		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tgt MtrVel Per1 AsstAssemblyPolarity	Cnt s08	
tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32	tgt MtrVel Per1 HandwheelVel HwRad		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt MtrVel Per1 MechMtrPos2Timestar	· <del>-</del>	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt MtrVel Per1 MechMtrPos2 Rev u0		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt MtrVel Per1 MotorVelCRF MtrRad	•	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H		
tgt Rte Inst Sa MtrVel.MtrVel Per1 SysCMotorVelMRF MtrRadpS f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M		
Name	Actual Value	Expected Value	Result
		·	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	- V
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-871.69104	-871.69104 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	2	2	- V
MtrVel_OsBufSelect_Cnt_M_u08	3	3	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54544	54544	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

871.69104

-871.69104

-871.69104

42

871.69104 ± 0.0625

-871.69104 ± 0.0625

-871.69104 ± 0.0625

42 ± 0.009

Test Step 2.23 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.18	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.21	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.24	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.27	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.45	

tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.51
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.63
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.66
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.72 2.75
MtrVel3_PosBuffer_Rad_M_f32[2][3] MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.78
MtrVel3 PosBuffer Rad M f32[2][5]	2.81
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2.93
MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.96
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.99
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.05
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.11
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6010
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6020
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6030
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6040
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6050
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6060
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6070
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	700 770
MtrVel3_TimeBuffer_uS_M_u16p0[1][1] MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	840
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]  MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	350
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	420
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	490
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	560
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	630
MtrVel3 TimeBuffer uS M u16p0[2][0]	6080
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6090
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6100
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6110
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6120
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6130
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6140
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6150
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1190
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1260
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	840
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	980
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1050
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1120
MtrVel_HwVel_HwRadpS_M_f32	25.16924399
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1286.213058 1
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08 MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	43566
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.36
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	45566
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32



MtrVel\_Per1

Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCR	F_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMR	r_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandw	heelVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorV	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-20.7045307	-20.70454196 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-57.5125809	-57.5126152 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	✓
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	45566	45566	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-20.7045307	-20.70454196 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-57.5125809	-57.5126152 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-20.7045307	-20.70454196 ± 0.009	~
tgt MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	-57.5125809	-57.5126152 ± 0.0625	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	<b>~</b>
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	<b>~</b>
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

lawa.	Innut Value	
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	i i	
ItrVel3_OsBufPos_Cnt_M_u08[1]	1	
ItrVel3_OsBufPos_Cnt_M_u08[2]	2	
ItrVel3_OsBufPos_Cnt_M_u08[3]	3	
ItrVel3_PosBuffer_Rad_M_f32[0][0]	2.2	
ItrVel3_PosBuffer_Rad_M_f32[0][1]	2.24	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	2.28	
ItrVel3_PosBuffer_Rad_M_f32[0][3]	2.32	
ItrVel3_PosBuffer_Rad_M_f32[0][4]	2.36	
ItrVel3_PosBuffer_Rad_M_f32[0][5]	2.4	
ItrVel3_PosBuffer_Rad_M_f32[0][6]	2.44	
ItrVel3_PosBuffer_Rad_M_f32[0][7]	2.48	
ItrVel3_PosBuffer_Rad_M_f32[1][0]	2.52	
ItrVel3_PosBuffer_Rad_M_f32[1][1]	2.56	
htrVel3_PosBuffer_Rad_M_f32[1][2]	2.6	
ItrVel3_PosBuffer_Rad_M_f32[1][3]	2.64	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	2.68	
trVel3_PosBuffer_Rad_M_f32[1][5]	2.72	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	2.76	
ItrVel3_PosBuffer_Rad_M_f32[1][7]	2.8	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	2.84	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	2.88	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	2.92	
trVel3_PosBuffer_Rad_M_f32[2][3]	2.96	
ltrVel3_PosBuffer_Rad_M_f32[2][4]	3	
trVel3_PosBuffer_Rad_M_f32[2][5]	3.04	
trVel3_PosBuffer_Rad_M_f32[2][6]	3.08	
trVel3_PosBuffer_Rad_M_f32[2][7]	3.12	
trVel3_PosBuffer_Rad_M_f32[3][0]	3.16	
trVel3_PosBuffer_Rad_M_f32[3][1]	3.2	
trVel3_PosBuffer_Rad_M_f32[3][2]	3.24	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	3.28	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	3.32	
ltrVel3_PosBuffer_Rad_M_f32[3][5]	3.36	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	3.4	
ltrVel3_PosBuffer_Rad_M_f32[3][7]	3.44	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850	
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745	
ltrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760	
ftrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775	
htrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790	
ltrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805	

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MtrVel\_Per1

1000 V C1_1 C1 1			(0)
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40		
	50		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]  MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]			
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]  MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_HwVel_HwRadpS_M_f32	27.92102087		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-870.6542583		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	40358		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_UIs_f32	0.37		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	42358		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestar	np_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	ıp16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadg	pS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	pS_f32	
tgt Rte Inst Sa MtrVel.MtrVel Per1 SysCHandwheelVel HwRadpS f32	tgt_MtrVel_Per1_SysCHandwheelVel_H		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt MtrVel Per1 SysCMotorVelMRF Mi		
Name	Actual Value	Expected Value	Resul
MtrVel HwVel HwRadpS M f32	-42	-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1343.38196	-1343.38208 ± 0.0625	
MtrVel OldPosBuf Cnt M u08	2	2	
MtrVel OsBufSelect Cnt M u08	3	3	
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	2.51323581	2.513235773 ± 0.0625	
	42358	42358	
Mtr/Vel_PrevMtrPosSampleTime2_uS_M_u32			
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1343.38196 -1343.38106	-1343.38208 ± 0.0625	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1343.38196	-1343.38208 ± 0.0625	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~	
CalcCoarseVel	1	CalcCoarseVel	1	•	
RegressionFit	1	RegressionFit	1	•	
MtrVelBlend	1	MtrVelBlend	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•	

-1343.38196

-42

-42 ± 0.009

-1343.38208 ± 0.0625

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Test Step 2.25 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][0] MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][3] MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.77 2.82
MtrVel3_PosBuffer_Rad_M_f32[1][4]  MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.07
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][7] MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.37 3.42
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.77
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	8245
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8125
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]  MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	8005 7885
MtrVel3_TimeBuffer uS M u16p0[0][4]	7765
MtrVel3 TimeBuffer uS M u16p0[0][5]	7645
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	7525
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	7405
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7285
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7165
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7045
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	6925
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	6805
MtrVel3_TimeBuffer_uS_M_u16p0[1][5] MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	6685 6565
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6445
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3640
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3760
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3880
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4000
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	3160
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	3280
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	3400
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3520
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4020 4140
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4140
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4380
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4500
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3660
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3780
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3900
MtrVel_HwVel_HwRadpS_M_f32	-13.75014228
MtrVel_MtrVelMRF_MtrRadpS_M_f32 MtrVel_OldPosBuf_Cnt_M_u08	421.9869356 2





Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	32455		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.38		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	34455		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	32768		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyF	Polarity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_	_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2T	imestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_	Rev_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_	MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_	MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwhe	elVel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVell	MRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	320.796387	320.7963257 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.14159274	3.141592654 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	34455	34455	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-320.796387	-320.7963257 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	320.796387	320.7963257 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	320.796387	320.7963257 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	<b>✓</b>
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.26 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	4	
MtrVel3_OsBufPos_Cnt_M_u08[1]	5	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.6	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.72	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.78	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.9	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.08	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.14	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.2	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.26	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.32	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.44	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.5	

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.56		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.62		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.68		
MtrVel3 PosBuffer Rad M f32[3][1]	3.74		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.8		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.86		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.92		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.98		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04		
MtrVel3 PosBuffer Rad M f32[3][7]	4.1		
MtrVel3 TimeBuffer uS M u16p0[0][0]	340		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360		
	380		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3 TimeBuffer uS M u16p0[3][6]	620		
MtrVel3 TimeBuffer uS M u16p0[3][7]	640		
	-6.301655293		
MtrVel_HwVel_HwRadpS_M_f32	1105.133117		
MtrVel_MtrVelMRF_MtrRadpS_M_f32			
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.7		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2343		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_UIs_f32	0.39		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	3		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	3		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4343		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwF		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	· -	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	Result
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1034.97485	1034.974731 ± 0.0625	9
MtrVel OldPosBuf Cnt M u08	2	2	
MtrVel_OsBufSelect_Cnt_M_u08	3	3	
MtrVel_PrevMtrPosNamnleTime2_uS_M_u32	3.76994967 4343	3.769949534 ± 0.0625	

4343

42

MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32

4343

42 ± 0.009





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1034.97485	1034.97473 ± 0.0625	✓
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1034.97485	1034.974731 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	<b>✓</b>
tgt MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	1034.97485	1034.974731 ± 0.0625	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.27 (Repeat Count = 1)	I WI
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	3
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
/trVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	6
ftrVel3_PosBuffer_Rad_M_f32[0][0]	5.2
ftrVel3_PosBuffer_Rad_M_f32[0][1]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4
/trVel3_PosBuffer_Rad_M_f32[0][3]	2.47
/trVel3_PosBuffer_Rad_M_f32[0][4]	2.54
ftrVel3_PosBuffer_Rad_M_f32[0][5]	2.61
ltrVel3_PosBuffer_Rad_M_f32[0][6]	2.68
ltrVel3_PosBuffer_Rad_M_f32[0][7]	2.75
ltrVel3_PosBuffer_Rad_M_f32[1][0]	2.82
ltrVel3_PosBuffer_Rad_M_f32[1][1]	2.89
ftrVel3_PosBuffer_Rad_M_f32[1][2]	2.96
ltrVel3_PosBuffer_Rad_M_f32[1][3]	3.03
ftrVel3_PosBuffer_Rad_M_f32[1][4]	3.1
/trVel3_PosBuffer_Rad_M_f32[1][5]	3.17
1trVel3_PosBuffer_Rad_M_f32[1][6]	3.24
/trVel3_PosBuffer_Rad_M_f32[1][7]	3.31
/trVel3_PosBuffer_Rad_M_f32[2][0]	3.38
/trVel3_PosBuffer_Rad_M_f32[2][1]	3.45
ItrVel3_PosBuffer_Rad_M_f32[2][2]	3.52
ItrVel3_PosBuffer_Rad_M_f32[2][3]	3.59
/trVel3_PosBuffer_Rad_M_f32[2][4]	3.66
/trVel3 PosBuffer Rad M f32[2][5]	3.73
/trVel3_PosBuffer_Rad_M_f32[2][6]	3.8
/trVel3_PosBuffer_Rad_M_f32[2][7]	3.87
/trVel3_PosBuffer_Rad_M_f32[3][0]	3.94
//trVel3_PosBuffer_Rad_M_f32[3][1]	4.01
	4.08
htrVel3_PosBuffer_Rad_M_f32[3][2]	4.15
htrVel3_PosBuffer_Rad_M_f32[3][3]	4.15
ItrVel3_PosBuffer_Rad_M_f32[3][4]	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	4.29
ltrVel3_PosBuffer_Rad_M_f32[3][6]	4.36
ItrVel3_PosBuffer_Rad_M_f32[3][7]	4.43
htrVel3_TimeBuffer_uS_M_u16p0[0][0]	8470
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	8480
htrVel3_TimeBuffer_uS_M_u16p0[0][2]	8490
/trVel3_TimeBuffer_uS_M_u16p0[0][3]	8500
ftrVel3_TimeBuffer_uS_M_u16p0[0][4]	8430
ltrVel3_TimeBuffer_uS_M_u16p0[0][5]	8440
ltrVel3_TimeBuffer_uS_M_u16p0[0][6]	8450
htrVel3_TimeBuffer_uS_M_u16p0[0][7]	8460
ltrVel3_TimeBuffer_uS_M_u16p0[1][0]	8710
ltrVel3_TimeBuffer_uS_M_u16p0[1][1]	8720
ltrVel3_TimeBuffer_uS_M_u16p0[1][2]	8730
trVel3_TimeBuffer_uS_M_u16p0[1][3]	8740
ltrVel3_TimeBuffer_uS_M_u16p0[1][4]	8750
ltrVel3_TimeBuffer_uS_M_u16p0[1][5]	8680
ltrVel3_TimeBuffer_uS_M_u16p0[1][6]	8690
ntrVel3_TimeBuffer_uS_M_u16p0[1][7]	8700
htrVel3_TimeBuffer_uS_M_u16p0[2][0]	10
/trVel3_TimeBuffer_uS_M_u16p0[2][1]	20

2014-12-26, 17:43:42+0530





MtrVel_Per1	2014-12-	26, 17:43:42+0530		Razorcat
Name		Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]		30		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]		40		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]		50		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]		60		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]		70		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]		80		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]		90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]		100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]		110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]		120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]		130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]		140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]		150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]		160		
MtrVel HwVel HwRadpS M f32		42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32		-616.0772175		
MtrVel_OldPosBuf_Cnt_M_u08		0		
MtrVel_OsBufSelect_Cnt_M_u08		1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32		3.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32		5788		
Rte_Inst_Sa_MtrVel		tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]		0		
T_MtrVelBlendTblY_Uls_u2p14[1]		16384		
k_GearRatio_Uls_f32		0.4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]		4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]		4		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value		-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value		7788		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value		45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_	Cnt_s08	tgt_MtrVel_Per1_AsstAssen	nblyPolarity_Cnt_s08	
tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRad	pS f32	tgt MtrVel Per1 Handwhee	elVel HwRadpS f32	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestam	np USec u32	tgt MtrVel Per1 MechMtrPe	os2Timestamp USec u32	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2 Rev u0	p16	tgt MtrVel Per1 MechMtrPe	os2 Rev u0p16	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadp	S f32	tgt MtrVel Per1 MotorVelC	RF MtrRadpS f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadp	S_f32	tgt_MtrVel_Per1_MotorVelM	IRF_MtrRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_H		tgt_MtrVel_Per1_SysCHand		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_Mt		tgt_MtrVel_Per1_SysCMoto		
Name		Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32		-42	-42 ± 0.0625	<b>✓</b>
MtrVel_MtrVelMRF_MtrRadpS_M_f32		399.105316	399.1052551 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08		1	1	•
MtrVel_OsBufSelect_Cnt_M_u08		2	2	•
MtrVel PrevMtrPosMechMtrPos2 Rad M f32		4.39821053	4.39821054 ± 0.0625	•
MtrVel PrevMtrPosSampleTime2 uS M u32		7788	7788	•
tgt MtrVel Per1 HandwheelVel HwRadpS f32.value		-42	-42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value		-399.105316	-399.1052551 ± 0.0625	<b>✓</b>
tat Mtr\(a) Par1 Meter\(a)MPE MtrPade\$ f22 value		300 105316	300 1053551 + 0.0635	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~	
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>	
RegressionFit	1	RegressionFit	1	~	
MtrVelBlend	1	MtrVelBlend	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>	

399.105316

399.105316

-42

399.1052551 ± 0.0625

399.1052551 ± 0.0625

-42 ± 0.009

Test Step 2.28 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Name	
MtrVel3_PosBuffer_Rad_M_[32[0][6]         2.76           MtrVel3_PosBuffer_Rad_M_[32[0][7]         2.84           MtrVel3_PosBuffer_Rad_M_[32[1][1]         2.92           MtrVel3_PosBuffer_Rad_M_[32[1][2]         3.08           MtrVel3_PosBuffer_Rad_M_[32[1][2]         3.08           MtrVel3_PosBuffer_Rad_M_[32[1][3]         3.16           MtrVel3_PosBuffer_Rad_M_[32[1][4]         3.24           MtrVel3_PosBuffer_Rad_M_[32[1][5]         3.32           MtrVel3_PosBuffer_Rad_M_[32[1][6]         3.4           MtrVel3_PosBuffer_Rad_M_[32[1][7]         3.48           MtrVel3_PosBuffer_Rad_M_[32[1][7]         3.48           MtrVel3_PosBuffer_Rad_M_[32[2][0]         3.56           MtrVel3_PosBuffer_Rad_M_[32[2][1]         3.64           MtrVel3_PosBuffer_Rad_M_[32[2][3]         3.8           MtrVel3_PosBuffer_Rad_M_[32[2][3]         3.8           MtrVel3_PosBuffer_Rad_M_[32[2][6]         4.04           MtrVel3_PosBuffer_Rad_M_[32[2][7]         4.12           MtrVel3_PosBuffer_Rad_M_[32[3][1]         4.2           MtrVel3_PosBuffer_Rad_M_[32[3][3]         4.44           MtrVel3_PosBuffer_Rad_M_[32[3][3]         4.44           MtrVel3_PosBuffer_Rad_M_[32[3][6]         4.6           MtrVel3_PosBuffer_Rad_M_[32[3][6]         4.6           MtrVe	
Mitvel3 PosBuffer Rad M. [320][7]         2.84           Mitvel3 PosBuffer Rad M. [321][10]         2.92           Mitvel3 PosBuffer Rad M. [321][1]         3           Mitvel3 PosBuffer Rad M. [321][2]         3.08           Mitvel3 PosBuffer Rad M. [321][3]         3.16           Mitvel3 PosBuffer Rad M. [321][15]         3.24           Mitvel3 PosBuffer Rad M. [321][16]         3.4           Mitvel3 PosBuffer Rad M. [321][17]         3.48           Mitvel3 PosBuffer Rad M. [321][17]         3.48           Mitvel3 PosBuffer Rad M. [322][1]         3.56           Mitvel3 PosBuffer Rad M. [322][1]         3.64           Mitvel3 PosBuffer Rad M. [322][2]         3.72           Mitvel3 PosBuffer Rad M. [322][2]         3.72           Mitvel3 PosBuffer Rad M. [322][3]         3.8           Mitvel3 PosBuffer Rad M. [322][6]         4.04           Mitvel3 PosBuffer Rad M. [322][7]         4.12           Mitvel3 PosBuffer Rad M. [323][1]         4.2           Mitvel3 PosBuffer Rad M. [323][3]         4.4           Mitvel3 PosBuffer Rad M. [323][3]         4.2           Mitvel3 PosBuffer Rad M. [323][3]         4.4           Mitvel3 PosBuffer Rad M. [323][3]         4.6           Mitvel3 PosBuffer Rad M. [323][3]         4.6           Mitve	
Mitvela PosBuffer, Rad, M. [32[1][0] 3 Mitvela PosBuffer, Rad, M. [32[1][1] 3 Mitvela PosBuffer, Rad, M. [32[1][2] 3.08 Mitvela PosBuffer, Rad, M. [32[1][3] 3.16 Mitvela PosBuffer, Rad, M. [32[1][3] 3.16 Mitvela PosBuffer, Rad, M. [32[1][4] 3.24 Mitvela PosBuffer, Rad, M. [32[1][6] 3.32 Mitvela PosBuffer, Rad, M. [32[1][6] 3.4 Mitvela PosBuffer, Rad, M. [32[1][6] 3.4 Mitvela PosBuffer, Rad, M. [32[1][7] 3.48 Mitvela PosBuffer, Rad, M. [32[2][0] 3.56 Mitvela PosBuffer, Rad, M. [32[2][0] 3.56 Mitvela PosBuffer, Rad, M. [32[2][2] 3.72 Mitvela PosBuffer, Rad, M. [32[2][2] 3.72 Mitvela PosBuffer, Rad, M. [32[2][2] 3.88 Mitvela PosBuffer, Rad, M. [32[2][3] 3.8 Mitvela PosBuffer, Rad, M. [32[2][3] 3.8 Mitvela PosBuffer, Rad, M. [32[2][6] 3.98 Mitvela PosBuffer, Rad, M. [32[2][6] 4.04 Mitvela PosBuffer, Rad, M. [32[3][6] 4.04 Mitvela PosBuffer, Rad, M. [32[3][6] 4.04 Mitvela PosBuffer, Rad, M. [32[3][1] 4.28 Mitvela PosBuffer, Rad, M. [32[3][1] 4.28 Mitvela PosBuffer, Rad, M. [32[3][3] 4.38 Mitvela PosBuffer, Rad, M. [32[3][3] 4.38 Mitvela PosBuffer, Rad, M. [32[3][3] 4.44 Mitvela PosBuffer, Rad, M. [32[3][3] 4.44 Mitvela PosBuffer, Rad, M. [32[3][6] 4.68 Mitvela TimeBuffer u.S. M. u16p0[0][0] 4125 Mitvela TimeBuffer u.S. M. u16p0[0][0] 4126 Mitvela Time	
Mitvel3 PosBuffer Rad M [32[1][1]         3           Mitvel3 PosBuffer Rad M [32[1][2]         3.08           Mitvel3 PosBuffer Rad M [32[1][3]         3.16           Mitvel3 PosBuffer Rad M [32[1][4]         3.24           Mitvel3 PosBuffer Rad M [32[1][6]         3.32           Mitvel3 PosBuffer Rad M [32[1][7]         3.48           Mitvel3 PosBuffer Rad M [32[1][7]         3.48           Mitvel3 PosBuffer Rad M [32[2][1]         3.56           Mitvel3 PosBuffer Rad M [32[2][1]         3.64           Mitvel3 PosBuffer Rad M [32[2][2]         3.72           Mitvel3 PosBuffer Rad M [32[2][3]         3.8           Mitvel3 PosBuffer Rad M [32[2][3]         3.8           Mitvel3 PosBuffer Rad M [32[2][5]         3.96           Mitvel3 PosBuffer Rad M [32[2][6]         4.04           Mitvel3 PosBuffer Rad M [32[3][1]         4.12           Mitvel3 PosBuffer Rad M [32[3][1]         4.2           Mitvel3 PosBuffer Rad M [32[3][1]         4.2           Mitvel3 PosBuffer Rad M [32[3][3]         4.4           Mitvel3 PosBuffer Rad M [32[3][3]         4.4           Mitvel3 PosBuffer Rad M [32[3][4]         4.52           Mitvel3 PosBuffer Rad M [32[3][6]         4.6           Mitvel3 PosBuffer Rad M [32[3][6]         4.6           Mitvel3 Po	
MitrVel3 PosBuffer Rad M_132[1][2]         3.08           MitrVel3 PosBuffer Rad M_132[1][3]         3.16           MitrVel3 PosBuffer Rad M_132[1][4]         3.24           MitrVel3 PosBuffer Rad M_132[1][5]         3.32           MitrVel3 PosBuffer Rad M_132[1][7]         3.4           MitrVel3 PosBuffer Rad M_132[1][7]         3.48           MitrVel3 PosBuffer Rad M_132[1][7]         3.48           MitrVel3 PosBuffer Rad M_132[1][1]         3.64           MitrVel3 PosBuffer Rad M_132[2][1]         3.64           MitrVel3 PosBuffer Rad M_132[2][2]         3.72           MitrVel3 PosBuffer Rad M_132[2][3]         3.8           MitrVel3 PosBuffer Rad M_132[2][6]         3.96           MitrVel3 PosBuffer Rad M_152[2][6]         4.04           MitrVel3 PosBuffer Rad M_152[2][7]         4.12           MitrVel3 PosBuffer Rad M_152[3][0]         4.2           MitrVel3 PosBuffer Rad M_152[3][1]         4.28           MitrVel3 PosBuffer Rad M_152[3][3]         4.44           MitrVel3 PosBuffer Rad M_152[3][3]         4.44           MitrVel3 PosBuffer Rad M_152[3][6]         4.6           MitrVel3 PosBuffer Rad M_152[3][6]         4.6           MitrVel3 PosBuffer Rad M_152[3][7]         4.76           MitrVel3 PosBuffer Rad M_152[3][6]         4.8	
MttVet3 PosBuffer_Rad_M_[32[1][3]         3.16           MttVet3 PosBuffer_Rad_M_[32[1][4]         3.24           MttVet3 PosBuffer_Rad_M_[32[1][5]         3.32           MttVet3 PosBuffer_Rad_M_[32[1][7]         3.48           MttVet3 PosBuffer_Rad_M_[32[2][0]         3.56           MttVet3 PosBuffer_Rad_M_[32[2][1]         3.64           MttVet3 PosBuffer_Rad_M_[32[2][2]         3.72           MttVet3 PosBuffer_Rad_M_[32[2][3]         3.8           MttVet3 PosBuffer_Rad_M_[32[2][5]         3.88           MttVet3 PosBuffer_Rad_M_[32[2][5]         3.96           MttVet3 PosBuffer_Rad_M_[32[2][6]         4.04           MtrVet3 PosBuffer_Rad_M_[32[2][7]         4.12           MtrVet3 PosBuffer_Rad_M_[32[3][0]         4.2           MtrVet3 PosBuffer_Rad_M_[32[3][1]         4.28           MtrVet3 PosBuffer_Rad_M_[32[3][2]         4.36           MtrVet3 PosBuffer_Rad_M_[32[3][4]         4.52           MtrVet3 PosBuffer_Rad_M_[32[3][4]         4.52           MtrVet3 PosBuffer_Rad_M_[32[3][6]         4.6           MtrVet3 PosBuffer_Rad_M_[32[3][7]         4.76           MtrVet3 TimeBuffer_uS_M_u16p0[0][0]         4125           MtrVet3 TimeBuffer_uS_M_u16p0[0][1]         400           MtrVet3 TimeBuffer_uS_M_u16p0[0][1]         3625           <	
MttVel3_PosBuffer_Rad_M_[32[1][4]         3.24           MttVel3_PosBuffer_Rad_M_[32[1][5]         3.32           MttVel3_PosBuffer_Rad_M_[32[1][7]         3.4           MttVel3_PosBuffer_Rad_M_[32[1][7]         3.48           MttVel3_PosBuffer_Rad_M_[32[2][0]         3.56           MttVel3_PosBuffer_Rad_M_[32[2][1]         3.64           MttVel3_PosBuffer_Rad_M_[32[2][2]         3.72           MttVel3_PosBuffer_Rad_M_[32[2][4]         3.8           MttVel3_PosBuffer_Rad_M_[32[2][6]         3.8           MttVel3_PosBuffer_Rad_M_[32[2][6]         4.04           MttVel3_PosBuffer_Rad_M_[32[2][6]         4.04           MttVel3_PosBuffer_Rad_M_[32[2][7]         4.12           MttVel3_PosBuffer_Rad_M_[32[3][0]         4.2           MttVel3_PosBuffer_Rad_M_[32[3][1]         4.28           MttVel3_PosBuffer_Rad_M_[32[3][2]         4.36           MttVel3_PosBuffer_Rad_M_[32[3][4]         4.52           MttVel3_PosBuffer_Rad_M_[32[3][4]         4.52           MttVel3_PosBuffer_Rad_M_[32[3][6]         4.6           MttVel3_PosBuffer_Rad_M_[32[3][7]         4.76           MttVel3_TimeBuffer_us_M_u16p0[0][0]         4125           MttVel3_TimeBuffer_us_M_u16p0[0][1]         3875           MttVel3_TimeBuffer_us_M_u16p0[0][1]         3250 <t< td=""><td></td></t<>	
MttVel3_PosBuffer_Rad_M_[32[1][6]         3.4           MttVel3_PosBuffer_Rad_M_[32[1][7]         3.48           MttVel3_PosBuffer_Rad_M_[32[2][0]         3.56           MttVel3_PosBuffer_Rad_M_[32[2][1]         3.64           MttVel3_PosBuffer_Rad_M_[32[2][2]         3.72           MttVel3_PosBuffer_Rad_M_[32[2][3]         3.8           MttVel3_PosBuffer_Rad_M_[32[2][5]         3.96           MttVel3_PosBuffer_Rad_M_[32[2][5]         3.96           MttVel3_PosBuffer_Rad_M_[32[2][7]         4.12           MttVel3_PosBuffer_Rad_M_[32[2][7]         4.12           MttVel3_PosBuffer_Rad_M_[32[3][0]         4.2           MttVel3_PosBuffer_Rad_M_[32[3][1]         4.28           MttVel3_PosBuffer_Rad_M_[32[3][2]         4.36           MttVel3_PosBuffer_Rad_M_[32[3][3]         4.44           MttVel3_PosBuffer_Rad_M_[32[3][4]         4.52           MttVel3_PosBuffer_Rad_M_[32[3][5]         4.6           MttVel3_PosBuffer_Rad_M_[32[3][7]         4.76           MttVel3_TimeBuffer_uS_M_u16p0[0][0]         4125           MttVel3_TimeBuffer_uS_M_u16p0[0][1]         4000           MttVel3_TimeBuffer_uS_M_u16p0[0][1]         3875           MttVel3_TimeBuffer_uS_M_u16p0[0][1]         3625           MttVel3_TimeBuffer_uS_M_u16p0[1][1]         3000	
MtrVel3 PosBuffer_Rad_M_f32[1][7]       3.48         MtrVel3_PosBuffer_Rad_M_f32[2][0]       3.56         MtrVel3_PosBuffer_Rad_M_f32[2][1]       3.64         MtrVel3_PosBuffer_Rad_M_f32[2][2]       3.72         MtrVel3_PosBuffer_Rad_M_f32[2][3]       3.8         MtrVel3_PosBuffer_Rad_M_f32[2][5]       3.96         MtrVel3_PosBuffer_Rad_M_f32[2][6]       4.04         MtrVel3_PosBuffer_Rad_M_f32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_f32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875	
MtrVel3_PosBuffer_Rad_M_[52[2][0]         3.56           MtrVel3_PosBuffer_Rad_M_[52[2][1]         3.64           MtrVel3_PosBuffer_Rad_M_[52[2][2]         3.72           MtrVel3_PosBuffer_Rad_M_[52[2][3]         3.8           MtrVel3_PosBuffer_Rad_M_[52[2][4]         3.88           MtrVel3_PosBuffer_Rad_M_[52[2][5]         3.96           MtrVel3_PosBuffer_Rad_M_[52[2][6]         4.04           MtrVel3_PosBuffer_Rad_M_[52[2][7]         4.12           MtrVel3_PosBuffer_Rad_M_[52[3][0]         4.2           MtrVel3_PosBuffer_Rad_M_[52[3][1]         4.28           MtrVel3_PosBuffer_Rad_M_[52[3][2]         4.36           MtrVel3_PosBuffer_Rad_M_[52[3][3]         4.44           MtrVel3_PosBuffer_Rad_M_[52[3][3]         4.44           MtrVel3_PosBuffer_Rad_M_[52[3][6]         4.6           MtrVel3_PosBuffer_Rad_M_[52[3][7]         4.76           MtrVel3_PosBuffer_Rad_M_[52[3][7]         4.76           MtrVel3_TimeBuffer_uS_M_u16p0[0][0]         4125           MtrVel3_TimeBuffer_uS_M_u16p0[0][1]         3625           MtrVel3_TimeBuffer_uS_M_u16p0[0][2]         3875           MtrVel3_TimeBuffer_uS_M_u16p0[0][6]         3375           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         3250           MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         3125	
MtrVel3_PosBuffer_Rad_M_f32[2][1]         3.64           MtrVel3_PosBuffer_Rad_M_f32[2][2]         3.72           MtrVel3_PosBuffer_Rad_M_f32[2][3]         3.8           MtrVel3_PosBuffer_Rad_M_f32[2][4]         3.88           MtrVel3_PosBuffer_Rad_M_f32[2][5]         3.96           MtrVel3_PosBuffer_Rad_M_f32[2][6]         4.04           MtrVel3_PosBuffer_Rad_M_f32[2][7]         4.12           MtrVel3_PosBuffer_Rad_M_f32[3][0]         4.2           MtrVel3_PosBuffer_Rad_M_f32[3][1]         4.28           MtrVel3_PosBuffer_Rad_M_f32[3][2]         4.36           MtrVel3_PosBuffer_Rad_M_f32[3][3]         4.44           MtrVel3_PosBuffer_Rad_M_f32[3][4]         4.52           MtrVel3_PosBuffer_Rad_M_f32[3][5]         4.6           MtrVel3_PosBuffer_Rad_M_f32[3][6]         4.68           MtrVel3_PosBuffer_Rad_M_f32[3][7]         4.76           MtrVel3_TimeBuffer_uS_M_u16p0[0][0]         4125           MtrVel3_TimeBuffer_uS_M_u16p0[0][1]         4000           MtrVel3_TimeBuffer_uS_M_u16p0[0][1]         4000           MtrVel3_TimeBuffer_uS_M_u16p0[0][6]         3875           MtrVel3_TimeBuffer_uS_M_u16p0[0][6]         3375           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         3250           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         3000	
MtrVel3_PosBuffer_Rad_M_[32[2][3]       3.72         MtrVel3_PosBuffer_Rad_M_[32[2][4]       3.88         MtrVel3_PosBuffer_Rad_M_[32[2][5]       3.96         MtrVel3_PosBuffer_Rad_M_[32[2][6]       4.04         MtrVel3_PosBuffer_Rad_M_[32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_[32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_[32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_[32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_[32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_[32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_[32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_[32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_[32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375	
MtrVel3_PosBuffer_Rad_M_f32[2][3]       3.8         MtrVel3_PosBuffer_Rad_M_f32[2][4]       3.88         MtrVel3_PosBuffer_Rad_M_f32[2][5]       3.96         MtrVel3_PosBuffer_Rad_M_f32[2][6]       4.04         MtrVel3_PosBuffer_Rad_M_f32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_f32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][4]       4.52         MtrVel3_PosBuffer_Rad_M_f32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375	
MtrVel3_PosBuffer_Rad_M_[32[2][4]       3.88         MtrVel3_PosBuffer_Rad_M_[32[2][6]       3.96         MtrVel3_PosBuffer_Rad_M_[32[2][6]       4.04         MtrVel3_PosBuffer_Rad_M_[32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_[32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_[32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_[32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_[32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_[32[3][4]       4.52         MtrVel3_PosBuffer_Rad_M_[32[3][5]       4.6         MtrVel3_PosBuffer_Rad_M_[32[3][6]       4.68         MtrVel3_PosBuffer_Rad_M_[32[3][7]       4.76         MtrVel3_TimeBuffer_us_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_us_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_us_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_us_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_us_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_us_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_us_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_us_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_us_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_us_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_us_M_u16p0[1][7]       2250	
MtrVel3_PosBuffer_Rad_M_f32[2][5]       3.96         MtrVel3_PosBuffer_Rad_M_f32[2][6]       4.04         MtrVel3_PosBuffer_Rad_M_f32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_f32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][4]       4.52         MtrVel3_PosBuffer_Rad_M_f32[3][6]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375 <td></td>	
MtrVel3_PosBuffer_Rad_M_f32[2][6]       4.04         MtrVel3_PosBuffer_Rad_M_f32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_f32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][4]       4.52         MtrVel3_PosBuffer_Rad_M_f32[3][5]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][4]       3625         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750	
MtrVel3_PosBuffer_Rad_M_f32[2][7]       4.12         MtrVel3_PosBuffer_Rad_M_f32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][4]       4.52         MtrVel3_PosBuffer_Rad_M_f32[3][5]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][4]       3625         MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       77	
MtrVel3_PosBuffer_Rad_M_f32[3][0]       4.2         MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][4]       4.52         MtrVel3_PosBuffer_Rad_M_f32[3][5]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_PosBuffer_US_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_US_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_US_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_US_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_US_M_u16p0[0][5]       3625         MtrVel3_TimeBuffer_US_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_US_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_US_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_US_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_US_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_US_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_US_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_US_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_US_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_US_M_u16p0[2][1]       775	
MtrVel3_PosBuffer_Rad_M_f32[3][1]       4.28         MtrVel3_PosBuffer_Rad_M_f32[3][2]       4.36         MtrVel3_PosBuffer_Rad_M_f32[3][3]       4.44         MtrVel3_PosBuffer_Rad_M_f32[3][5]       4.6         MtrVel3_PosBuffer_Rad_M_f32[3][6]       4.68         MtrVel3_PosBuffer_Rad_M_f32[3][7]       4.76         MtrVel3_TimeBuffer_uS_M_u16p0[0][0]       4125         MtrVel3_TimeBuffer_uS_M_u16p0[0][1]       4000         MtrVel3_TimeBuffer_uS_M_u16p0[0][2]       3875         MtrVel3_TimeBuffer_uS_M_u16p0[0][3]       3750         MtrVel3_TimeBuffer_uS_M_u16p0[0][4]       3625         MtrVel3_TimeBuffer_uS_M_u16p0[0][5]       3500         MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       <	
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MtrVel3_TimeBuffer_uS_M_u16p0[0][6]       3375         MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]       3250         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       3125         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       3000         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       2875         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       2750         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       2625         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       2500         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       2375         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       2250         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       7625         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       7750         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       7875         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       8000         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       7125         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]         7125           MtrVel3_TimeBuffer_uS_M_u16p0[2][5]         7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] 7250	
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] 7500	
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] 11500	
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] 11625	
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 11750	
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] 11875	
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] 12000	
MtrVel3_TimeBuffer_uS_M_u16p0[3][5] 11125	
MtrVel3_TimeBuffer_uS_M_u16p0[3][6] 11250	
MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 11375	
MtrVel_HwVel_HwRadpS_M_f32 17.41422999	
MtrVel_MtrVelMRF_MtrRadpS_M_f32 -657.4433444	
MtrVel_OldPosBuf_Cnt_M_u08 2	
MtrVel_OsBufSelect_Cnt_M_u08 3  MtrVel_DrawMtrDesMeehMtrDes2_Red_M_f22	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 3.5	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32 54544  Pto_Inct_Sa_MtrVel tot Pto_Inct_Sa_Mtr	
Rte_Inst_Sa_MtrVel         tgt_Rte_Inst_Sa_Mt           T MtrVelBlendTblY Uls u2p14[0]         0	
T_MtrVelBlendTblY_Uls_u2p14[0] 0  T_MtrVelBlendTblY_Uls_u2p14[1] 16384	'el
k_GearRatio_Uls_f32 0.41	'el
t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 6	'el
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 6	'el
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	'el
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 56544	'el





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn	t_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_	<u>f</u> 32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	763.283752	763.2836914 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	56544	56544	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	763.283752	763.2836914 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.29 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.64
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1150

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	950		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1800 1450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3] MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1550		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	4328		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	4278		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	4228		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4178		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4128		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4078		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4028		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3978		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3828		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3778		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3728		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3678		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3628		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3578		
MtrVel_HwVel_HwRadpS_M_f32	-0.304720819		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	732.5839302		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	63534		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.42		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value			
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65534 58982		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tgt MtrVel Per1 AsstAssemblyPolarity Cnt	608	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt MtrVel Per1 HandwheelVel HwRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt MtrVel Per1 MechMtrPos2 Rev u0p16	<del>-</del>	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt MtrVel Per1 MotorVelCRF MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt MtrVel Per1 MotorVelMRF MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	· -	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	827.414246	827.4141846 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	
MtrVel OsBufSelect Cnt M u08	2	2	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	65534	65534	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	827.414246	827.4141846 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	
tot MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	827.414246	827.4141846 ± 0.0625	

827.414246

 $tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value$ 

827.4141846 ± 0.0625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
/trVel3_OsBufPos_Cnt_M_u08[3]	4
/trVel3_PosBuffer_Rad_M_f32[0][0]	0
/ltrVel3_PosBuffer_Rad_M_f32[0][1]	0
/trVel3_PosBuffer_Rad_M_f32[0][2]	0
/trVel3_PosBuffer_Rad_M_f32[0][3]	0
ftrVel3_PosBuffer_Rad_M_f32[0][4]	0
ltrVel3_PosBuffer_Rad_M_f32[0][5]	0
ltrVel3_PosBuffer_Rad_M_f32[0][6]	0
ftrVel3_PosBuffer_Rad_M_f32[0][7]	0
ftrVel3_PosBuffer_Rad_M_f32[1][0]	0
ftrVel3_PosBuffer_Rad_M_f32[1][1]	0
ltrVel3_PosBuffer_Rad_M_f32[1][2]	0
ltrVel3_PosBuffer_Rad_M_f32[1][3]	0
ltrVel3_PosBuffer_Rad_M_f32[1][4]	0
ltrVel3_PosBuffer_Rad_M_f32[1][5]	0
ltrVel3_PosBuffer_Rad_M_f32[1][6]	0
ltrVel3_PosBuffer_Rad_M_f32[1][7]	0
htrVel3_PosBuffer_Rad_M_f32[2][0]	0
ltrVel3_PosBuffer_Rad_M_f32[2][1]	0
ftrVel3_PosBuffer_Rad_M_f32[2][2]	0
ltrVel3_PosBuffer_Rad_M_f32[2][3]	0
ltrVel3_PosBuffer_Rad_M_f32[2][4]	0
ltrVel3_PosBuffer_Rad_M_f32[2][5]	0
ftrVel3_PosBuffer_Rad_M_f32[2][6]	0
htrVel3_PosBuffer_Rad_M_f32[2][7]	0
ftrVel3_PosBuffer_Rad_M_f32[3][0]	0
ftrVel3_PosBuffer_Rad_M_f32[3][1]	0
ItrVel3_PosBuffer_Rad_M_f32[3][2]	0
ItrVel3_PosBuffer_Rad_M_f32[3][3]	0
ItrVel3_PosBuffer_Rad_M_f32[3][4]	0
ItrVel3_PosBuffer_Rad_M_f32[3][5]	0
ItrVel3_PosBuffer_Rad_M_f32[3][6]	0
ItrVel3_PosBuffer_Rad_M_f32[3][7]	0
htrVel3_TimeBuffer_uS_M_u16p0[0][0]	6481
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	6543
ltrVel3_TimeBuffer_uS_M_u16p0[0][2]	6109
ItrVel3 TimeBuffer uS M u16p0[0][3]	6171
ltrVel3_TimeBuffer_uS_M_u16p0[0][4]	6233
ItrVel3 TimeBuffer uS M u16p0[0][5]	6295
ltrVel3_TimeBuffer_uS_M_u16p0[0][6]	6357
ItrVel3 TimeBuffer uS M u16p0[0][7]	6419
ItrVel3 TimeBuffer uS M u16p0[1][0]	19800
ltrVel3_TimeBuffer_uS_M_u16p0[1][1]	19755
trVel3 TimeBuffer uS M u16p0[1][2]	19706
trVel3_TimeBuffer_uS_M_u16p0[1][3]	19676
trVel3_TimeBuffer_uS_M_u16p0[1][4]	19603
trVel3_TimeBuffer_uS_M_u16p0[1][5]	19578
trVel3_TimeBuffer_uS_M_u16p0[1][6]	19504
trVel3_TimeBuffer_uS_M_u16p0[1][7]	19460
trVel3_TimeBuffer_uS_M_u16p0[2][0]	5500
trVel3_TimeBuffer_uS_M_u16p0[2][1]	5400
htrVel3_TimeBuffer_uS_M_u16p0[2][2]	5300
	5200
htrVel3_TimeBuffer_uS_M_u16p0[2][3]	
/trVel3_TimeBuffer_uS_M_u16p0[2][4]	5100
MtrVel3_TimeBuffer_uS_M_u16p0[2][5] MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5050 5000

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Name	Input Value		
MtrVel3 TimeBuffer uS M u16p0[2][7]	4960		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	7138		
MtrVel3 TimeBuffer uS M u16p0[3][1]	7200		
MtrVel3 TimeBuffer uS M u16p0[3][2]	7262		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	7324		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	7386		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6952		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	7014		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	7076		
MtrVel_HwVel_HwRadpS_M_f32	28.83693385		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	698.9029244		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	443476		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.43		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	11		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	11		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	445676		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity	_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timesta	mp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u	0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRac	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_h	HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel HwVel HwRadpS M f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	868.882874	868.8828125 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	<b>✓</b>
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	445676	445676	✓
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	<b>✓</b>
· · · · · · · · · · · · · · · · · · ·		1	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	<b>✓</b>
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

-868.882874

868.882874

868.882874

-42

-868.8828125 ± 0.0625

868.8828125 ± 0.0625

868.8828125 ± 0.0625

-42 ± 0.009

Test Step 2.31 (Repeat Count = 1)		~
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	5	
MtrVel3_OsBufPos_Cnt_M_u08[1]	6	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	

tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][4] MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433 6.283089433
MtrVel3 PosBuffer Rad M f32[2][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250 20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][5] MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwVel_HwRadpS_M_f32	-13.1049369
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-298.8190119
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08  AttrVel_DrovMtrDecMachMtrDec2_Red_M_f22	1.4
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1.4 63355
	tgt_Rte_Inst_Sa_MtrVel
Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_UIs_f32	0.44
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	65555
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107
	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolanty_Cnt_su8 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-28.6764164	-28.6764221 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-65.1736755	-65.1736908 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel_OsBufSelect_Cnt_M_u08	1	1	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	<b>✓</b>
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	65555	65555	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-28.6764164	-28.6764221 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-28.6764164	-28.6764221 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-65.1736755	-65.1736908 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Name	Input Value	
	1	
/trVel3_OsBufPos_Cnt_M_u08[0]		
MtrVel3_OsBufPos_Cnt_M_u08[1]	3 4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
/trVel3_OsBufPos_Cnt_M_u08[3]		
htrVel3_PosBuffer_Rad_M_f32[0][0]	1.12 1.13	
htrVel3_PosBuffer_Rad_M_f32[0][1]	1.13	
ItrVel3_PosBuffer_Rad_M_f32[0][2]		
htrVel3_PosBuffer_Rad_M_f32[0][3]	1.15	
htrVel3_PosBuffer_Rad_M_f32[0][4]	1.16	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17	
/trVel3_PosBuffer_Rad_M_f32[0][6]	1.18	
htrVel3_PosBuffer_Rad_M_f32[0][7]	1.19	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21	
htrVel3_PosBuffer_Rad_M_f32[1][2]	1.22	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23	
ItrVel3_PosBuffer_Rad_M_f32[1][4]	1.24	
ItrVel3_PosBuffer_Rad_M_f32[1][5]	1.25	
htrVel3_PosBuffer_Rad_M_f32[1][6]	1.26	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28	
ItrVel3_PosBuffer_Rad_M_f32[2][1]	1.29	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	1.3	
ItrVel3_PosBuffer_Rad_M_f32[2][3]	1.31	
htrVel3_PosBuffer_Rad_M_f32[2][4]	1.32	
ltrVel3_PosBuffer_Rad_M_f32[2][5]	1.33	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	1.34	
ItrVel3_PosBuffer_Rad_M_f32[2][7]	1.35	
htrVel3_PosBuffer_Rad_M_f32[3][0]	1.35	
ltrVel3_PosBuffer_Rad_M_f32[3][1]	1.37	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	1.37	
ItrVel3_PosBuffer_Rad_M_f32[3][3]	1.38	
htrVel3_PosBuffer_Rad_M_f32[3][4]	1.39	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41	
ftrVel3_PosBuffer_Rad_M_f32[3][7]	1.42	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9645	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9602	
/trVel3_TimeBuffer_uS_M_u16p0[0][2]	9560	
/trVel3_TimeBuffer_uS_M_u16p0[0][3]	9501	
/trVel3_TimeBuffer_uS_M_u16p0[0][4]	9470	
htrVel3_TimeBuffer_uS_M_u16p0[0][5]	9403	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9366		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9304		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7760		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7822		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7884		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7946		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7512		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7574		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7636		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	7698		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6662		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6724		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6786		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6848		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6910		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6476		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6538		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4012		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3946		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3901		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3866		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3752		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3702		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3650		
MtrVel_HwVel_HwRadpS_M_f32	-29.04110444		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-930.6708422		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	86466		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384 0.45		
k_GearRatio_Uls_f32	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] tgt MtrVel Per1 AsstAssemblyPolarity Cnt s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	88666		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08		608	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt_MtrVel_Per1_MandwheelVel_HwRadpS_r32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt MtrVel Per1 MotorVelCRF MtrRadpS 1		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt MtrVel Per1 MotorVelMRF MtrRadpS		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa		
Name	Actual Value	Expected Value	Result
MtrVel_HwRadpS_M_f32	42	42 ± 0.0625	Result
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-325.011383	-325.0114746 ± 0.0625	
MtrVel OldPosBuf Cnt M u08	1	1	
MtrVel_OsBufSelect_Cnt_M_u08	2	2	_
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	
MtrVel PrevMtrPosSampleTime2 uS M u32	88666	88666	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	325.011383	325.0114746 ± 0.0625	-
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-325.011383	-325.0114746 ± 0.0625	
tgt MtrVel Per1 SysCHandwheelVel HwRadpS f32.value	42	42 ± 0.009	-
tot. Mtr.\(c). Por1. SycCMotor\(c)IMPE. Mtr.Podp.S. f22 yellyo	225 044292	325 0114746 ± 0.0625	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

-325.011383

tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value

-325.0114746 ± 0.0625





Test Step 2.33 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	3
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.18
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.21
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.24 2.27
MtrVel3_PosBuffer_Rad_M_f32[0][3] MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.33
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.39
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.45
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.51
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.54
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.57 2.6
MtrVel3_PosBuffer_Rad_M_f32[1][6] MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.63
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.66
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.69
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.75
MtrVel3_PosBuffer_Rad_M_f32[2][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2.81
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][7]	2.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2.9
MtrVel3_PosBuffer_Rad_M_f32[3][1]  MtrVel3_PosBuffer_Rad_M_f32[3][2]	2.93 2.96
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2.99
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.05
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.11
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	11148
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11210
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	10776
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	10838 10900
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]  MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	10962
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	11024
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11086
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10100
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	10070
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10010
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	9950
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	9904
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	9860
MtrVel3_TimeBuffer_uS_M_u16p0[1][6] MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	9800 9770
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9173
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9235
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9297
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9359
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9421
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	8987
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9049
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9111
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6415
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6325 6225
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6175
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6110
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6060
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6000
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	5960
MtrVel_HwVel_HwRadpS_M_f32	10.2496587
MtrVel_MtrVelMRF_MtrRadpS_M_f32	964.6684363
MtrVel_OldPosBuf_Cnt_M_u08	2

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MtrVel\_Per1

Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.7		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3366		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.49		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	0		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	0		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	5566		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	45875		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolar	ity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hwl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times	stamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrR	tadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	I_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	771.913879	771.9138794 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	0	0	<b>✓</b>
MtrVel_OsBufSelect_Cnt_M_u08	1	1	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.39821053	4.39821054 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5566	5566	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	771.913879	771.9138794 ± 0.0625	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.34 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	4
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.24
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.28
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.4
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.44
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.48
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.52
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.56
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.64
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.68
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.76
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.8
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2.84
MtrVel3_PosBuffer_Rad_M_f32[2][1]	2.88
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2.96
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.04

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Name .	Invest Mad		
Name MtrVel3_PosBuffer_Rad_M_f32[2][6]	Input Value 3.08		
MtrVel3 PosBuffer Rad M f32[2][7]	3.12		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.16		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.24		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.28		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.32		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.36		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.44		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65352 65414		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2] MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65476		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	64		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	126		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	188		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1626		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1688		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1316		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1378		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1440		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1502		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1564		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	220 330		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	550		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	660		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880		
MtrVel_HwVel_HwRadpS_M_f32	-12.63669056		
MtrVel_MtrVelMRF_MtrRadpS_M_f32 MtrVel_OldPosBuf_Cnt_M_u08	457.1572797 0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	1		
MtrVel PrevMtrPosSampleTime2 uS M u32	54343		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	16		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	56543		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429	larity Oak 200	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPo		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_HandwheelVel_H tgt_MtrVel_Per1_MechMtrPos2Tin		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Innestamp_USec_us2 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Re		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_Mi		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_M		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheel		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32			
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1025.73547	-1025.735352 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	1	1	-
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	5.02656746	5 026567421 + 0 0625	

5.02656746

56543

42

MtrVel\_PrevMtrPosMechMtrPos2\_Rad\_M\_f32

MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32

5.026567421 ± 0.0625

56543

42 ± 0.009

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MtrVel\_Per1

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1025.73547	1025.735352 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1025.73547	-1025.735352 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1025.73547	-1025.735352 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3 OsBufPos Cnt M u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.27
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.32
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.37
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.47
MtrVel3_r osbuffer Rad M f32[0][6]	2.52
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.57
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.62
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.67
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.72 2.77
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.17
MtrVel3_PosBuffer_Rad_M_f32[1][4]	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2.87
MtrVel3_PosBuffer_Rad_M_f32[1][6]	2.92
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2.97
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.02
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.07
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.17
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.22
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.27
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.47
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3.52
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.57
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.67
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.72
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.77
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	111
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	221
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	441
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551
الالالاواع TimeBuller_uS_M_u16p0[0][4] MtrVel3 TimeBuffer uS M u16p0[0][5]	661
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624
	630
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	856
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	901
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	945
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277

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MtrVel Per1

MtrVel_Per1			MADUCAL
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239		
MtrVel_HwVel_HwRadpS_M_f32	39.80989385		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-93.04319823		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6897678		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T MtrVelBlendTblY Uls u2p14[1]	16384		
k GearRatio Uls f32	0.51		
t MtrVelBlendTblX MtrRadpS u12p4[0]	11		
t MtrVelBlendTblX MtrRadpS u12p4[1]	11		
tgt MtrVel Per1 AsstAssemblyPolarity Cnt s08.value	1		
tgt MtrVel Per1 MechMtrPos2Timestamp USec u32.value	6899878		
tgt MtrVel Per1 MechMtrPos2 Rev u0p16.value	58982		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tgt MtrVel Per1 AsstAssemblyPo	larity Cnt s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_H	,	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Tin		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_R		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_M		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_M		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheel		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMi		
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	•
MtrVel MtrVelMRF MtrRadpS M f32	297.649353	297.649292 ± 0.0625	•
MtrVel OldPosBuf Cnt M u08	3	3	•
MtrVel OsBufSelect Cnt M u08	0	0	•
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	5.65482855	5.654828427 ± 0.0625	•
MtrVel PrevMtrPosSampleTime2 uS M u32	6899878	6899878	•
tgt MtrVel Per1 HandwheelVel HwRadpS f32.value	42	42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	297.649353	297.649292 ± 0.0625	•
tgt MtrVel Per1 MotorVelMRF MtrRadpS f32.value	297.649353	297.649292 ± 0.0625	
tgt_wave_rer_wotorvenwrd_wartaupo_152.value	10	10 : 0 000	

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

42

297.649353

42 ± 0.009

297.649292 ± 0.0625

Test Step 2.36 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	5	
MtrVel3_OsBufPos_Cnt_M_u08[1]	6	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.42	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.48	

 $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ 





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.54
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.66
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.72
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.78
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.84
MtrVel3_PosBuffer_Rad_M_f32[1][3]	2.9
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.96
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.02
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.14
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.26
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.38
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.44
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.68
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.74
	3.8
MtrVel3_PosBuffer_Rad_M_f32[3][2] MtrVel3_PosBuffer_Rad_M_f32[3][3]	3.86
	3.92
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3.98
MtrVel3_PosBuffer_Rad_M_f32[3][5] MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[3][7]  MtrVel3_TimeRuffer_us_N_u16e0[0][0]	3383
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3631
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455
MtrVel_HwVel_HwRadpS_M_f32	-10.45496923
MtrVel_MtrVelMRF_MtrRadpS_M_f32	179.1975787
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08	0
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	5577
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	77777





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_C	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p2	16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-0	0 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1323.42847	1323.428223 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel_OsBufSelect_Cnt_M_u08	1	1	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	<b>✓</b>
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7777	7777	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1323.42847	-1323.428223 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	1323.42847	1323.428223 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-0	0 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	1323.42847	1323.428223 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.37 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3 OsBufPos Cnt M u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5.2	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.33	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.4	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.47	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.61	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2.89	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2.96	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.1	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.17	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.24	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.38	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.45	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.52	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.59	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.66	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.73	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3.8	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3.87	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	3.94	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.01	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4.08	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.15	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.22	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.29	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.36	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.43	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9645	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9602		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9560		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9403		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9366		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9304		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	7760		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7822		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7884		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	7946		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	7512		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	7574 7636		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	7698		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6662		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0] MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6724		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6786		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6848		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6910		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6476		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6538		
MtrVel3 TimeBuffer uS M u16p0[2][7]	6600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4012		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3946		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3901		
MtrVel3 TimeBuffer uS M u16p0[3][3]	3866		
MtrVel3 TimeBuffer uS M u16p0[3][4]	3805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3752		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3702		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3650		
MtrVel_HwVel_HwRadpS_M_f32	-16.19755042		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-722.7552819		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2133		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	4333		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107	000	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt tgt_MtrVel_Per1_HandwheelVel_HwRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_mandwheervel_mwRadpS_		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2 Rev u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16	_	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt_MtrVel_Per1_MecrimitPos2_Rev_uop10		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt MtrVel Per1 SysCHandwheelVel HwRa		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	. –	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	- Nooull
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-792.446411	-792.4464111 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	
MtrVel OsBufSelect Cnt M u08	2	2	•
MtrVel PrevMtrPosMechMtrPos2 Rad M f32	1.2566179	1.256617887 ± 0.0625	-
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	4333	4333	-
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	-
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-792.446411	-792.4464111 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-792.446411	-792.4464111 ± 0.0625	<b>✓</b>

-42

-792.446411

-42 ± 0.009

-792.4464111 ± 0.0625

 $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ 



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Гest Step 2.38 (Repeat Count = 1)		
Name Same	Input Value	
/trVel3_OsBufPos_Cnt_M_u08[0]	2	
ItrVel3_OsBufPos_Cnt_M_u08[1]	4	
ltrVel3_OsBufPos_Cnt_M_u08[2]	6	
trVel3_OsBufPos_Cnt_M_u08[3]	1	
trVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.6	
trVel3_PosBuffer_Rad_M_f32[0][5]	2.68	
trVel3_PosBuffer_Rad_M_f32[0][6]	2.76	
trVel3_PosBuffer_Rad_M_f32[0][7]	2.84	
trVel3_PosBuffer_Rad_M_f32[1][0]	2.92	
trVel3_PosBuffer_Rad_M_f32[1][1]	3	
trVel3_PosBuffer_Rad_M_f32[1][2]	3.08	
trVel3_PosBuffer_Rad_M_f32[1][3]	3.16	
trVel3_PosBuffer_Rad_M_f32[1][4]	3.24	
trVel3_PosBuffer_Rad_M_f32[1][5]	3.32	
trVel3_PosBuffer_Rad_M_f32[1][6]	3.4	
trVel3_PosBuffer_Rad_M_f32[1][7]	3.48	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	3.56	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	3.64	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	3.72	
trVel3_PosBuffer_Rad_M_f32[2][3]	3.8	
trVel3_PosBuffer_Rad_M_f32[2][4]	3.88	
trVel3_PosBuffer_Rad_M_f32[2][5]	3.96	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	4.04	
ltrVel3_PosBuffer_Rad_M_f32[2][7]	4.12	
ltrVel3_PosBuffer_Rad_M_f32[3][0]	4.2	
ItrVel3_PosBuffer_Rad_M_f32[3][1]	4.28	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	4.36	
ItrVel3_PosBuffer_Rad_M_f32[3][3]	4.44	
trVel3_PosBuffer_Rad_M_f32[3][4]	4.52	
trVel3_PosBuffer_Rad_M_f32[3][5]	4.6	
trVel3_PosBuffer_Rad_M_f32[3][6]	4.68	
trVel3_PosBuffer_Rad_M_f32[3][7]	4.76	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	6897	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	6800	
trVel3 TimeBuffer uS M u16p0[0][2]	6776	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	6700	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	6650	
trVel3 TimeBuffer uS M u16p0[0][5]	6603	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	6575	
trVel3 TimeBuffer uS M u16p0[0][7]	6508	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	2695	
trVel3_TimeBuffer_uS_M_u16p0[1][1]	2757	
trVel3 TimeBuffer uS M u16p0[1][2]	2819	
trVel3_TimeBuffer_uS_M_u16p0[1][3]	2881	
trVel3 TimeBuffer uS M u16p0[1][4]	2943	
trVel3_TimeBuffer_uS_M_u16p0[1][5]	2509	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	2571	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	2633	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	3825	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	3887	
trVel3_TimeBuffer_uS_M_u16p0[2][2]	3949	
htrVel3_TimeBuffer_uS_M_u16p0[2][3]	4011	
htrVel3_TimeBuffer_uS_M_u16p0[2][4]	4073	
htrVel3_TimeBuffer_uS_M_u16p0[2][5] htrVel3_TimeBuffer_uS_M_u16p0[2][6]	4135 4197	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3763		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	5085		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	5012		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4950		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4908		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4860		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	4805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	4775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	4700		
MtrVel_HwVel_HwRadpS_M_f32	-23.42079562		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1306.267611		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	75688		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.64		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	77888		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolar	ity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hw	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Times	stamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrR	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	I_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-33.4618645	-33.46188599 ± 0.0625	•
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-52.2841644	-52.28419876 ± 0.0625	•
MtrVel_OldPosBuf_Cnt_M_u08	2	2	•
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	77888	77888	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-33.4618645	-33.46188599 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-52.2841644	-52.28419876 ± 0.0625	•
	E0 0044044	50.00440070 . 0.0005	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

-52.2841644

-33.4618645

-52.2841644

-52.28419876 ± 0.0625 -33.46188599 ± 0.009

-52.28419876 ± 0.0625

Test Step 2.39 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.39	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.48	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.57	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.66	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.75	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2.84	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2.93	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.11	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.38
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3.74
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3.83
MtrVel3_PosBuffer_Rad_M_f32[2][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.01
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.19
MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[3][1]	4.55 4.64
MtrVel3_PosBuffer_Rad_M_f32[3][2] MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.73
MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.82
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.91
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.09
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268
MtrVel3_TimeBuffer uS M u16p0[0][2]	65330
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392
MtrVel3 TimeBuffer uS M u16p0[0][4]	65454
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300 400
MtrVel3_TimeBuffer_uS_M_u16p0[3][3] MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800
MtrVel_HwRadpS_M_f32	-42
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-660.4303926
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	53233
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.67
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	55433
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	<u>f</u> 32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	dpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-812.165649	-812.1655273 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.51323581	2.513235773 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	55433	55433	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	812.165649	812.1655273 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-812.165649	-812.1655273 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-812.165649	-812.1655273 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

lame	Input Value	
/trVel3 OsBufPos Cnt M u08[0]	0	
/trVel3 OsBufPos Cnt M u08[1]	2	
ItrVel3_OsBufPos_Cnt_M_u08[2]	4	
htrVel3_OsBufPos_Cnt_M_u08[3]	7	
ItrVel3 PosBuffer Rad M f32[0][0]	2.32	
ItrVel3_PosBuffer_Rad_M_f32[0][1]	2.42	
ItrVel3_PosBuffer_Rad_M_f32[0][2]	2.52	
trVel3_PosBuffer_Rad_M_f32[0][3]	2.62	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.72	
trVel3_PosBuffer_Rad_M_f32[0][5]	2.82	
trVel3_PosBuffer_Rad_M_f32[0][6]	2.92	
trVel3_PosBuffer_Rad_M_f32[0][7]	3.02	
trVel3_PosBuffer_Rad_M_f32[1][0]	3.12	
trVel3 PosBuffer Rad M f32[1][1]	3.22	
trVel3_PosBuffer_Rad_M_f32[1][2]	3.32	
trVel3_PosBuffer_Rad_M_f32[1][3]	3.42	
trVel3_PosBuffer_Rad_M_f32[1][4]	3.52	
trVel3_PosBuffer_Rad_M_f32[1][5]	3.62	
rVel3 PosBuffer Rad M f32[1][6]	3.72	
trVel3 PosBuffer Rad M f32[1][7]	3.82	
trVel3_PosBuffer_Rad_M_f32[2][0]	3.92	
trVel3_PosBuffer_Rad_M_f32[2][1]	4.02	
trVel3_PosBuffer_Rad_M_f32[2][2]	4.12	
trVel3_PosBuffer_Rad_M_f32[2][3]	4.22	
trVel3_PosBuffer_Rad_M_f32[2][4]	4.32	
rVel3_PosBuffer_Rad_M_f32[2][5]	4.42	
rVel3_PosBuffer_Rad_M_f32[2][6]	4.52	
rVel3_PosBuffer_Rad_M_f32[2][7]	4.62	
rVel3_PosBuffer_Rad_M_f32[3][0]	4.02	
trVel3_PosBuffer_Rad_M_f32[3][1]	4.72	
trVel3_PosBuffer_Rad_M_f32[3][2]	4.92	
	5.02	
rVel3_PosBuffer_Rad_M_f32[3][3]	5.12	
trVel3_PosBuffer_Rad_M_f32[3][4]	5.12	
trVel3_PosBuffer_Rad_M_f32[3][5]	5.32	
rVel3_PosBuffer_Rad_M_f32[3][6]	5.32	
rVel3_PosBuffer_Rad_M_f32[3][7]	250	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	65352	
trVel3_TimeBuffer_uS_M_u16p0[0][1]		
trVel3_TimeBuffer_uS_M_u16p0[0][2]	65414	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	65476	
trVel3_TimeBuffer_uS_M_u16p0[0][4] trVel3 TimeBuffer uS M u16p0[0][5]	64	

MtrVel Per1

2014-12-26, 17:43:42+0530



Input Value Name MtrVel3\_TimeBuffer\_uS\_M\_u16p0[0][6] 126 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[0][7] 188 MtrVel3 TimeBuffer\_uS\_M\_u16p0[1][0] 1626 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[1][1] 1688 MtrVel3 TimeBuffer uS M u16p0[1][2] 1750 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[1][3] 1316 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[1][4] 1378 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[1][5] 1440 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[1][6] 1502 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[1][7] 1564 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][0] 110 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][1] 220 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][2] 330  $MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][3]$ 440 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][4] 550 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][5] 660 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][6] 770 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[2][7] 880 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][0] 110 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][1] 220 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][2] 330 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][3] 440 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][4] 550 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][5] 660 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][6] 770 MtrVel3\_TimeBuffer\_uS\_M\_u16p0[3][7] 880 MtrVel HwVel HwRadpS M f32 42 MtrVel\_MtrVelMRF\_MtrRadpS\_M\_f32 1222.594758 MtrVel OldPosBuf Cnt M u08 0 MtrVel\_OsBufSelect\_Cnt\_M\_u08 1 MtrVel PrevMtrPosMechMtrPos2 Rad M f32 2.6 MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32 32378 Rte Inst Sa MtrVel tgt Rte Inst Sa MtrVel T\_MtrVelBlendTblY\_Uls\_u2p14[0] 0 T\_MtrVelBlendTblY\_Uls\_u2p14[1] 16384 k GearRatio Uls f32 0.7 t\_MtrVelBlendTblX\_MtrRadpS\_u12p4[0] 6 6 t\_MtrVelBlendTblX\_MtrRadpS\_u12p4[1] tgt\_MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08.value tgt\_MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32.value 34578 tgt\_MtrVel\_Per1\_MechMtrPos2\_Rev\_u0p16.value 32768 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08 tgt MtrVel\_Per1\_AsstAssemblyPolarity\_Cnt\_s08  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_HandwheelVel\_HwRadpS\_f32$ tgt\_MtrVel\_Per1\_HandwheelVel\_HwRadpS\_f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32 tgt\_MtrVel\_Per1\_MechMtrPos2Timestamp\_USec\_u32  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MechMtrPos2\_Rev\_u0p16$ tgt\_MtrVel\_Per1\_MechMtrPos2\_Rev\_u0p16 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32 tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32$ tgt MtrVel Per1 MotorVelMRF MtrRadpS f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32 tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32 tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32 **Actual Value** Name Expected Value Result MtrVel\_HwVel\_HwRadpS\_M\_f32 42 42 ± 0.0625 MtrVel MtrVelMRF\_MtrRadpS\_M\_f32 246.178558 246.1784821 ± 0.0625 MtrVel\_OldPosBuf\_Cnt\_M\_u08 1 MtrVel\_OsBufSelect\_Cnt\_M\_u08 2 2 MtrVel\_PrevMtrPosMechMtrPos2\_Rad\_M\_f32 3.14159274 3.141592654 ± 0.0625 MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32 34578 34578 tgt\_MtrVel\_Per1\_HandwheelVel\_HwRadpS\_f32.value 42  $42 \pm 0.009$ tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32.value 246.178558 246.178482 ± 0.0625 tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value 246.178558 246.1784821 ± 0.0625

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~	
CalcCoarseVel	1	CalcCoarseVel	1	•	
RegressionFit	1	RegressionFit	1	•	
MtrVelBlend	1	MtrVelBlend	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•	

246.178558

42 ± 0.009

246.1784821 ± 0.0625

42

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Test Step 2.41 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	0 2
MtrVel3_OsBufPos_Cnt_M_u08[3] MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.34
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.45
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.56
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.67
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.78
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.89
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.11
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.22
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.33 3.44
MtrVel3_PosBuffer_Rad_M_f32[1][2] MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.55
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.66
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3.88
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3.99
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.1
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.21
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.32
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.43
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.54 4.65
MtrVel3_PosBuffer_Rad_M_f32[2][5] MtrVel3_PosBuffer_Rad_M_f32[2][6]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4.98
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.09
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.2
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.31
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.53
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.64
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.75
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]  MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	111 221
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	331
MtrVel3 TimeBuffer uS M u16p0[0][3]	441
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	551
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	661
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	624
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	630
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	702
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	750
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	806
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]  MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	856 901
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	945
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	990
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	175
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	65277
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525
MtrVel3_TimeBuffer_uS_M_u16p0[2][6] MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	51 113
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177
Mtr/(a)2 Time Buffer u.S. M. u.16e0[2][7]	239
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	
MtrVel_HwVel_HwRadpS_M_f32	0
	0 -1201.403606 2

MtrVel\_Per1



Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87234		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.73		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	8		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	89434		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPo	arity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_H	wRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Tim	nestamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Re	ev_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_Mi	rRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_M	trRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheel	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMF	RF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	577.249817	577.2498169 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	89434	89434	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-577.249817	-577.2498169 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	577.249817	577.2498169 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	577.249817	577.2498169 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	<b>✓</b>
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.42 (Repeat Count = 1)	v v
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.36
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.48
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.84
MtrVel3_PosBuffer_Rad_M_f32[0][5]	2.96
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.08
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.32
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.44
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.56
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.04
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.16
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.4
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.76
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4.88

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Minded Position Food (1920) Mi				
Minoral, Prosidence, Patal, M. 1979 7    517	Name	Input Value		
Minkey Pauliner, 1961, 1967, 1971 Minkey Pauliner, 1961, 1967, 196	MtrVel3 PosBuffer Rad M f32[2][6]	-		
Michael Desiduer, Rost J. 1903(19)  Michael Desiduer, Rost J. 1903		5.12		
Minded Position Food (1920) Mi		5.24		
Minches   Peabler   Fast   M. (2019)   5.0	MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.36		
Mirvis   Possibler   Res   M. (2018)	MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.48		
Mary   Designation   Design	MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.6		
Mary New   Destature   Res   M.   DESTITE	MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.72		
Microsoft   Micr	MtrVel3_PosBuffer_Rad_M_f32[3][5]	5.84		
Marchel Treadure   M.   1807 07    608	MtrVel3_PosBuffer_Rad_M_f32[3][6]	5.96		
Minyel   TimeBuffer   S. M.   USPORTION		6.08		
Minyel   TimeBuffer   15   M.   15   10   10   10   10   10   10   10	MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	999		
Mirved_TimeBuffer_US_M_10fp(0)[0]	MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	888		
Mirved   Dimebuffer   S. M.	MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	777		
Mirvisor   Immebition   M. M.   M.   M.   M.   M.   M.   M.	MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	666		
Milved_TimeBuffer_BM_M156000[7]   323   325	MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	555		
Minked   Principating   15 M. jurispolity    222   225   2	MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	444		
MeVel	MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	333		
Mirvis	MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	222		
Mirved   TimeBuffer   15 M   1140(1)   25	MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	925		
Miny	MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	825		
Mirves   TimeBuffer   1.5 M   1.1600 15    425	MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	725		
Minyes   TimeBuffer   Lis M   Lis   Dol   150	MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	625		
Mirves   TimeBuffer   Lis M   Lis   DOUBT   Color	MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	525		
Micros   TimeBuffer   U.S. M.   1450 2 19   225   226   22	MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	425		
Mirves J. TimeBuffer U.S. M. u1600[21]  Mirves	MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	325		
Mirves   TimeBuffer u.S. M. u16p02  12    234   168   169	MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	225		
Mirvel3_TimeBuffer_uS_M_u16p02[13]   356   3	MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	666		
Mirvel3_TimeBuffer_uS_M_urlsp0g2 3	MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	232		
Mirvels, TimeBuffer, u.S. M., u16902[14]  Mirvels, TimeBuffer, u.S. M., u16902[15]  Mirvels, TimeBuffer, u.S. M., u16902[17]  Mirvels, TimeBuffer, u.S. M., u16903[17]  Mirvels, TimeBuffer, u.S. M., u16903[17]  Mirvels, TimeBuffer, u.S. M., u16903[18]  Mirvels, TimeBuffer, u.S. M., u16903[19]  Mirvels, Mirvels, u.S. M., u16903[17]  Mirvels, u.S. M., u16903[17]  Mirvels, u.S. Mirvels, u.S. M., u16903[17]  Mirvels, u.S. Mirvels, u.S. u16903[17]  Mirvels, u.S. Mirvels, u.S. u16903[17]  Mirvels, u.S. u16903[17]  Mirvels, u.	MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	294		
Mirvels_TimeBuffer_uS_M_u16pd[2][6]   542   54	MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	356		
MtVels_TimeBuffer_uS_M_u16p0[2][6] 542  MtVels_TimeBuffer_uS_M_u16p0[2][7] 604  MtVels_TimeBuffer_uS_M_u16p0[3][7] 1042  MtVels_TimeBuffer_uS_M_u16p0[3][7] 1106  MtVels_TimeBuffer_uS_M_u16p0[3][7] 1106  MtVels_TimeBuffer_uS_M_u16p0[3][8] 732  MtVels_TimeBuffer_uS_M_u16p0[3][8] 732  MtVels_TimeBuffer_uS_M_u16p0[3][8] 734  MtVels_TimeBuffer_uS_M_u16p0[3][8] 856  MtVels_TimeBuffer_uS_M_u16p0[3][9] 818  MtVel_Diverbett_uS_M_u16p0[3][9] 818  MtVel_Diverbett_uS_M_u16p0[3]	MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	418		
MirVelS_TimeBuffer_uS_M_u16p0[3][7]	MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	480		
MirVel3_TimeBuffer_uS_M_u16p0[3][0]	MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	542		
MirVel3_TimeBuffer_uS_M_u16p0[3][1]	MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	604		
MITVel3_TimeBuffer_uS_M_u16p0[3][2]	MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1042		
MirVel3_TimeBuffer_uS_M_u16p0[3][3]	MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1104		
MirVel3_TimeBuffer_uS_M_u16p0[3][4]	MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1166		
MirVel3_TimeBuffer_US_M_u16p0[3][5]   856     918	MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	732		
MirVel3_TimeBuffer_uS_M_u16p0[3][6]   918	MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	794		
MittVel3_TimeBuffer_uS_M_u16p0[3][7]         980           MitVel_HVwel_HvRadpS_M_f32         23.7894063           MitVel_HVWelMRR_MtrRadpS_M_f32         953.297722           MitVel_OldPosBul_Cnt_M_u08         2           MitVel_OldPosBul_Cnt_M_u08         3           MitVel_PowerMitPosSempleTime2_uS_M_u32         1           MitVel_PrevMtrPosSempleTime2_uS_M_u32         2378           Rte_Inst_Sa_MtrVel         T_MtrVelBlendTbly_Uis_u2p14[0]         0           T_MtrVelBlendTbly_Uis_u2p14[0]         0	MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	856		
MitrVel_HwVel_HwRadpS_M_f32	MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	918		
MitVe_HwVe_HwRadpS_M_f32 955.297722  MitVe_MitVeMRF_MitRadpS_M_f32 955.297722  MitVe_OldPoSeufSelect_Cnt_M_u08 3  MitVe_OldPoSesDuf_Cnt_M_u08 3  MitVe_PrevMitPosSendhitrPos2_Rad_M_f32 1  MitVe_PrevMitPosSendhitrPos2_Rad_M_f32 1  MitVe_PrevMitPosSendhitrPos2_Rad_M_f32 1  MitVel_PrevMitPosSendhitrPos2_Rad_M_f32 1  MitVel_PrevMitPosSendhitrPos2_Timestamp_USec_u32_value 1  MitVel_PrevMitPosSendhitrPos2_Rad_Up016_value 1  MitVel_PrevMitPosSendhitrPos2_Rad_Up016_value 1  MitVel_PrevMitPosSendhitrPos2_Rad_Up016_value 1  MitVel_PrevMitPosSendhitrPo		980		
MitrVel_MitrVelMRF_MitrRadpS_M_f32         953.297722           MitrVel_OldPosBuf_Cnt_M_u08         2           MitrVel_DsbufSelect_Cnt_M_u08         3           MitrVel_PrewMitrPosSampleTime2_uS_M_u32         1           MitrVel_PrewMitrPosSampleTime2_uS_M_u32         2378           Rte_Inst_Sa_MitrVel         Us_2074[0]           T_MitrVelBlendTblY_UIs_u2p14[0]         0           T_MitrVelBlendTblY_UIS_u2p14[1]         16384           K_GearRatio_UIs_f32         0.76           UftrVelBlendTblX_MitrRadpS_u12p4[0]         10           UftrVelBlendTblX_MitrRadpS_u12p4[1]         10           UftrVelPer1_AsstAssemblyPolarity_Cnt_s08.value         478           Uft_MitrVel_Per1_MechMitrPos2Timestamp_USec_u32.value         45875           Uft_Rie_Inst_Sa_MitrVel.MitrVel_Per1_AsstAssemblyPolarity_Cnt_s08         Uft_MitrVel_Per1_AsstAssemblyPolarity_Cnt_s08         Uft_MitrVel_Per1_HandwheelVel_HwRadpS_f32         Uft_MitrVel_Per1_MechMitrPos2_Rev_u0p16         Uft_MitrVel_Per1_MendwhrevS_f2         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevS_f32         Uft_MitrVel_Per1_MendwhrevB_f32         Uft_MitrVel_Per1_Mend		23.7894063		
MtrVel_PrewMtrPosRechMtrPos2_Rad_M_[32]         1           MtrVel_PrewMtrPosNechMtrPos2_Rad_M_[32]         1           MtrVel_PrewMtrPosNechMtrPos2_Rad_M_[32]         2378           Rte_Inst_Sa_MtrVel         tgt_Rte_Inst_Sa_MtrVel           T_MtrVelBlendTblY_Uls_u2p14[0]         0           T_MtrVelBlendTblY_Uls_u2p14[1]         16384           K_GearRatio_Uls_f32         0.76           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         10           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         10           tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value         4578           tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value         45875           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08         tgt_MtrVel_Per1_HandwheelVel_HarkadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HarkadpS_f32         tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32         tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16         tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p18           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32         tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHan		953.297722		
MtrVel_PrewMtrPosRechMtrPos2_Rad_M_[32]         1           MtrVel_PrewMtrPosNechMtrPos2_Rad_M_[32]         1           MtrVel_PrewMtrPosNechMtrPos2_Rad_M_[32]         2378           Rte_Inst_Sa_MtrVel         tgt_Rte_Inst_Sa_MtrVel           T_MtrVelBlendTblY_Uls_u2p14[0]         0           T_MtrVelBlendTblY_Uls_u2p14[1]         16384           K_GearRatio_Uls_f32         0.76           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         10           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         10           tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value         4578           tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value         45875           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08         tgt_MtrVel_Per1_HandwheelVel_HarkadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HarkadpS_f32         tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32         tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16         tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p18           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32         tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHan		2		
MitrVel_PrevMitrPosMechMtrPos2_Rad_M_f32				
Rte_Inst_Sa_MirVel		1		
Rte_Inst_Sa_MirVel				
T_MtrVelBlendTblY_Uls_u2p14[0] 0  T_MtrVelBlendTblY_Uls_u2p14[1] 16384  k_GearRatio_Uls_f32 0.76  t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 10  t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 10  tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 11  tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 15  tgt_MtrVel_Per1_MechMtrPos2_Trimestamp_USec_u32.value 15  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Trimestamp_USec_u32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32 15  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32 15  tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32 15  tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32 15  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32 15				
T_MtrVeiBlendTblY_Uls_u2p14[1] 16384 k_GearRatio_Uls_f32 0.76 t_MtrVeiBlendTblX_MtrRadpS_u12p4[0] 10 t_MtrVeiBlendTblX_MtrRadpS_u12p4[1] 10 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 1-1 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 4578 tgt_MtrVel_Per1_MechMtrPos2Rev_u0p16.value 45875 tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_SysChandwheelVel_HwRadpS_f32 tgt_MtrVel_Per1_SysChandwheelVe				
k_GearRatio_Uls_f32				
t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 10  t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 10  tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 11  tgt_MtrVel_Per1_MechMtrPos2_Timestamp_USec_u32.value 4578  tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16 value 45875  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Timestamp_USec_u32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_MtrVel_MtrRadpS_M_f32  MtrVel_MtrVel_MtrVel_MtrRadpS_M_f32  d2  d2  d2  d2  d2  d2  d2  d3  d3				
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 10  tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value -1  tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value 4578  tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value 45875  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_MtrVelMrRadpS_M_f32  Name  Actual Value  Expected Value  Resul  MtrVel_MtrVelMtrRadpS_M_f32   MtrVel_MtrVelMtrRadpS_M_f32   MtrVel_OldPosBuf_Cnt_M_u08  0  0  0  0  0  0  0  0  0  0  0  0				
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value		10		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value       4578         tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value       45875         tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08       tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08         tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32       tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32       tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16       tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32         tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32       tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32         Name       Actual Value       Expected Value       Resul         MtrVel_HwVel_HwRadpS_M_f32       42       42 ± 0.0625       42         MtrVel_MtrRadpS_M_f32       -1311.352279       -1311.352173 ± 0.0625       43         MtrVel_OldPosBuf_Cnt_M_u08       0       0       0				
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value         45875           tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08           tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32         tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16         tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32         tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32           tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32           tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32           Name         Actual Value         Expected Value         Resul           MtrVel_HwVel_HwRadpS_M_f32         42         42 ± 0.0625         42           MtrVel_MtrRadpS_M_f32         -1311.352173 ± 0.0625         43           MtrVel_OldPosBuf_Cnt_M_u08         0         0         0				
tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2_Rev_u0p16  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRer_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRer_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRer_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRer_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRer_MtrRadpS_f32  tgt_MtrVel_				
tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_HandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2_Rev_u0p16  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRef_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRef_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRef_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRef_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRef_MtrRadpS_f32  tgt_MtrVel_Per1_MotorVelRef_Mtr			nt s08	
tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32         tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32           tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MctorVelCRF_MtrRadpS_f32         tgt_MtrVel_Per1_MctorVelCRF_MtrRadpS_f32           tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MctorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_MctorVelMRF_MtrRadpS_f32           tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_MctorVelMRF_MtrRadpS_f32           tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32           tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32           tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32           Name         Actual Value         Expected Value         Resul           MtrVel_HwVel_HwRadpS_M_f32         42         42 ± 0.0625         42           MtrVel_MtrRadpS_M_f32         -1311.352173 ± 0.0625         3           MtrVel_OldPosBuf_Cnt_M_u08         3         3         3           MtrVel_OsBufSelect_Cnt_M_u08         0         0         0			_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16       tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32       tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32         Name       Actual Value       Expected Value       Resul         MtrVel_HwVel_HwRadpS_M_f32       42       42 ± 0.0625       42         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -1311.35229       -1311.352173 ± 0.0625       43         MtrVel_OldPosBuf_Cnt_M_u08       3       3       3         MtrVel_OsBufSelect_Cnt_M_u08       0       0       0			_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32       tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32         Name       Actual Value       Expected Value       Resul         MtrVel_HwVel_HwRadpS_M_f32       42       42 ± 0.0625       42         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -1311.35229       -1311.352173 ± 0.0625       43         MtrVel_OldPosBuf_Cnt_M_u08       3       3       3         MtrVel_OsBufSelect_Cnt_M_u08       0       0       0				
tgt_Rte_Inst_Sa_MtrVel_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32       tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32       tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32       Result Value       Expected Value       Result Value       Nation Value       Value Value Value       Value Value Value       Value Value Value Value       Value Va				
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32         tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32           Name         Actual Value         Expected Value         Result           MtrVel_HwVel_HwRadpS_M_f32         42         42 ± 0.0625         42           MtrVel_MtrVelMRF_MtrRadpS_M_f32         -1311.35229         -1311.352173 ± 0.0625         43           MtrVel_OldPosBuf_Cnt_M_u08         3         3         44           MtrVel_OsBufSelect_Cnt_M_u08         0         0         0			_	
tgt_Rte_Inst_Sa_MtrVel_MtrRadpS_f32         tgt_MtrVel_Per1_sysCMotorVelMRF_MtrRadpS_f32           Name         Actual Value         Expected Value         Resul           MtrVel_HwVel_HwRadpS_M_f32         42         42 ± 0.0625         42           MtrVel_MtrVelMRF_MtrRadpS_M_f32         -1311.35229         -1311.352173 ± 0.0625         43           MtrVel_OldPosBuf_Cnt_M_u08         3         3         44           MtrVel_OsBufSelect_Cnt_M_u08         0         0         0				
Name         Actual Value         Expected Value         Result           MtrVel_HwVel_HwRadpS_M_f32         42         42 ± 0.0625         42           MtrVel_MtrVelMRF_MtrRadpS_M_f32         -1311.35229         -1311.352173 ± 0.0625         43           MtrVel_OldPosBuf_Cnt_M_u08         3         3         3           MtrVel_OsBufSelect_Cnt_M_u08         0         0         0			. –	
MtrVel_HwVel_HwRadpS_M_f32       42       42 ± 0.0625         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -1311.35229       -1311.352173 ± 0.0625         MtrVel_OldPosBuf_Cnt_M_u08       3       3         MtrVel_OsBufSelect_Cnt_M_u08       0       0				Pocult
MtrVel_MtrVelMRF_MtrRadpS_M_f32         -1311.35229         -1311.352173 ± 0.0625           MtrVel_OldPosBuf_Cnt_M_u08         3         3           MtrVel_OsBufSelect_Cnt_M_u08         0         0				Result
MtrVel_OldPosBuf_Cnt_M_u08         3         3           MtrVel_OsBufSelect_Cnt_M_u08         0         0				
MtrVel_OsBufSelect_Cnt_M_u08 0				
				-
BUILTO PLOABILIDO BUILDO STANDO DE LA GOOGNO	Mtr/el_OsButSelect_Cnt_M_u08  Mtr/el_PreyMtrPosMechMtrPos2_Rad_M_f32	4 39821053	4 39821054 + 0 0625	

0 4.39821053

4578

42

MtrVel\_PrevMtrPosMechMtrPos2\_Rad\_M\_f32

MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32

4.39821054 ± 0.0625

4578

42 ± 0.009

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MtrVel\_Per1

Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1311.35229	1311.352173 ± 0.0625	<b>✓</b>
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1311.35229	-1311.352173 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1311.35229	-1311.352173 ± 0.0625	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	<b>~</b>
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	5
MtrVel3_OsBufPos_Cnt_M_u08[1]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3 PosBuffer Rad M f32[0][0]	3.3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77
MtrVel3 PosBuffer Rad M f32[0][4]	2.9
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03
MtrVel3 PosBuffer Rad M f32[0][6]	3.16
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55
	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][2] MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2
	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][7]	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.59
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.37
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	843
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	793
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	743
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	693
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	543
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	493
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	443
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	393
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	447
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	407
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	367
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	327
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	287
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	247
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	207
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	167
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2796
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2362

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MtrVel_Per1			MACICAL
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2424		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2486		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2548		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2610		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2672		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2734		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3672		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3734		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3796		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3362		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3424		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3486		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3548		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3610		
MtrVel HwVel HwRadpS M f32	-40.36163938		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1131.737475		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	52133		
Rte Inst Sa MtrVel	tgt Rte Inst Sa MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T MtrVelBlendTblY Uls u2p14[1]	16384		
k GearRatio Uls f32	0.79		
t MtrVelBlendTblX MtrRadpS u12p4[0]	12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	12		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	54333		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	52429		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tgt MtrVel Per1 AsstAssemblyPolarity	Cnt s08	
tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32	tgt MtrVel Per1 HandwheelVel HwRad	_	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt MtrVel Per1 MechMtrPos2Timestam	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt MtrVel Per1 MotorVelCRF MtrRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hu		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtr	· –	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	9.54013348	9.540119181 ± 0.0625	rtoourt
MtrVel MtrVelMRF MtrRadpS M f32	12.0761175	12.07610035 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel OsBufSelect Cnt M u08	0	0	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.02656746	5.026567421 ± 0.0625	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	54333	54333	
tgt MtrVel Per1 HandwheelVel HwRadpS f32.value	9.54013348	9.540119181 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	12.0761175	9.540 119 18 1 ± 0.009 12.07610035 ± 0.0625	
tgt_iviti vol_i oi i_iviotoi veioixi _ivitii\aupo_ioz.vaiue	12.0701173	12.07010033 ± 0.0023	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 ChecknointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	_

12.0761175

9.54013348

12.0761175

12.07610035 ± 0.0625

9.540119181 ± 0.009

12.07610035 ± 0.0625

Test Step 2.44 (Repeat Count = 1)		🗸
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	3	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.54	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.68	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.82	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.96	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





	l
Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.1 3.24
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.38
MtrVel3_PosBuffer_Rad_M_f32[0][7] MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.52
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.66
MtrVel3 PosBuffer Rad M f32[1][2]	3.8
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.08
MtrVel3 PosBuffer Rad M f32[1][5]	4.22
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.36
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.5
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.64
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.78
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.92
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.06
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.2
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.34
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.48
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.62
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.75
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.8
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.04
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.17
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.7 3.8
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1926
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1988
MtrVel3_TimeBuffer_uS_M_u16p0[0][1] MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1554
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1616
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1678
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1740
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1802
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1864
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2252
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2314
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2376
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2438
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2004
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2066
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2128
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2190
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	927
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	900
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	894
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	858
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	801
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	791
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	755
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	700 1001
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	910
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	881
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	835
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	749
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	698
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	666
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	606
MtrVel_HwRadpS_M_f32	29.00406408
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	1
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	676689
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0]	0
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_Uls_f32	0.82
t_MtrVelBlendTbiX_MtrRadpS_u12p4[0]	13
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	14
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	678889





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	58982		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hwf	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	752.194763	752.1947632 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>✓</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	5.65482855	5.654828427 ± 0.0625	<b>✓</b>
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	678889	678889	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-752.194763	-752.1947632 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	752.194763	752.1947632 ± 0.0625	•
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	752.194763	752.1947632 ± 0.0625	~

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.45 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	2
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.42
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.57
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.72
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.87
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.02
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.31
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.47
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.62
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.77
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.92
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.22
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.37
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.52
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.67
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.82
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.97
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.12
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.27
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.42
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.57
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.7
MtrVel3_PosBuffer_Rad_M_f32[2][7]	5.87
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.01
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.16
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.31
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.9
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.1
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	835

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MtrVel\_Per1

Mtrvei_Per1			MACILAG
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	765		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	695		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	660		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	590		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10345		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]  MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	10301		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	10290		
MtrVel3 TimeBuffer uS M u16p0[1][4]	10175		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	10154		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	10100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	10060		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	712		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	774		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	836		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	898		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	960		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	1022		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	1084		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	650		
Mtr/el3_TimeBuffer_uS_M_u16p0[3][0]	2922 2984		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]  MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2550		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2612		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2674		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2736		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2798		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2860		
MtrVel_HwVel_HwRadpS_M_f32	41.65612358		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	2255		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	16384		
T_MtrVelBlendTblY_Uls_u2p14[1]	0.85		
k_GearRatio_Uls_f32 t MtrVelBlendTblX MtrRadpS u12p4[0]	15		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	16		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt MtrVel Per1 MechMtrPos2Timestamp USec u32.value	4455		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	26214		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cn	t_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	. –	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	1	1
Name	Actual Value	Expected Value	Resul
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-221.256454	-221.2564697 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08  MtrVel_PrevMtrPosMechMtrPos2 Rad M f32	2.51323581	0 2.513235773 ± 0.0625	
MtrVel PrevMtrPosNampleTime2 uS M u32	4455	2.513235773 ± 0.0625	
tgt MtrVel Per1 HandwheelVel HwRadpS f32.value	-42	-42 ± 0.009	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-221.256454	-221.25647 ± 0.0625	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-221.256454	-221.2564697 ± 0.0625	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt MtrVel Per1 SvsCMotorVelMRF MtrRadpS f32.value	-221.256454	-221,2564697 ± 0.0625	

-221.256454

 $tgt\_MtrVel\_Per1\_SysCMotorVelMRF\_MtrRadpS\_f32.value$ 

-221.2564697 ± 0.0625



Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.46 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
/trVel3_OsBufPos_Cnt_M_u08[1]	4	
ItrVel3_OsBufPos_Cnt_M_u08[2]	6	
ItrVel3_OsBufPos_Cnt_M_u08[3]	1	
trVel3_PosBuffer_Rad_M_f32[0][0]	2.44	
ltrVel3_PosBuffer_Rad_M_f32[0][1]	2.6	
ltrVel3_PosBuffer_Rad_M_f32[0][2]	2.76	
ltrVel3_PosBuffer_Rad_M_f32[0][3]	2.92	
ltrVel3_PosBuffer_Rad_M_f32[0][4]	3.08	
trVel3_PosBuffer_Rad_M_f32[0][5]	3.24	
ltrVel3_PosBuffer_Rad_M_f32[0][6]	3.39	
ltrVel3_PosBuffer_Rad_M_f32[0][7]	3.56	
ltrVel3_PosBuffer_Rad_M_f32[1][0]	3.71	
trVel3_PosBuffer_Rad_M_f32[1][1]	3.88	
trVel3_PosBuffer_Rad_M_f32[1][2]	4.03	
trVel3_PosBuffer_Rad_M_f32[1][3]	4.2	
trVel3_PosBuffer_Rad_M_f32[1][4]	4.36	
trVel3_PosBuffer_Rad_M_f32[1][5]	4.52	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	4.68	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	4.84	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	5	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	5.16	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	5.32	
trVel3_PosBuffer_Rad_M_f32[2][3]	5.48	
trVel3_PosBuffer_Rad_M_f32[2][4]	5.64	
trVel3_PosBuffer_Rad_M_f32[2][5]	5.8	
ItrVel3_PosBuffer_Rad_M_f32[2][6]	5.96	
ItrVel3_PosBuffer_Rad_M_f32[2][7]	6.12	
ItrVel3_PosBuffer_Rad_M_f32[3][0]	2.4	
1trVel3_PosBuffer_Rad_M_f32[3][1]	2.5	
trVel3_PosBuffer_Rad_M_f32[3][2]	2.6	
ItrVel3_PosBuffer_Rad_M_f32[3][3]	2.7	
ItrVel3_PosBuffer_Rad_M_f32[3][4]	2.8	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	4.2	
ItrVel3_PosBuffer_Rad_M_f32[3][6]	4.3	
trVel3_PosBuffer_Rad_M_f32[3][7]	4.4	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	6142	
trVel3_TimeBuffer_uS_M_u16p0[0][1]	6204	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	6266	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	5832	
trVel3_TimeBuffer_uS_M_u16p0[0][4]	5894	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	5956	
trVel3_TimeBuffer_uS_M_u16p0[0][6]	6018	
trVel3 TimeBuffer uS M u16p0[0][7]	6080	
trVel3_TimeBuffer_uS_M_u16p0[1][0]	1520	
trVel3_TimeBuffer_uS_M_u16p0[1][1]	1500	
trVel3_TimeBuffer_uS_M_u16p0[1][2]	1480	
	1460	
trVel3_TimeBuffer_uS_M_u16p0[1][3]	1440	
trVel3_TimeBuffer_uS_M_u16p0[1][4]		
trVel3_TimeBuffer_uS_M_u16p0[1][5]	1420	
trVel3_TimeBuffer_uS_M_u16p0[1][6]	1400	
trVel3_TimeBuffer_uS_M_u16p0[1][7]	1380	
trVel3_TimeBuffer_uS_M_u16p0[2][0]	7777	
trVel3_TimeBuffer_uS_M_u16p0[2][1]	7700	
ttrVel3_TimeBuffer_uS_M_u16p0[2][2]	7610	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	7590	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500	
htrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7378		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6504		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6566		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6132		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6194		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6256		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6442		
MtrVel_HwVel_HwRadpS_M_f32	-40.36163938		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	443466		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.88		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	5		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	5		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	445666		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	39322		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPola	rity_Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_Hw	/RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Time	estamp_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev	/_u0p16	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_Mtrl	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_Mtr	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVe	el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRf	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	804.522583	804.522522 ± 0.0625	<b>✓</b>
MtrVel_OldPosBuf_Cnt_M_u08	3	3	<b>✓</b>
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.76994967	3.769949534 ± 0.0625	<b>✓</b>
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	445666	445666	<b>✓</b>
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	<b>✓</b>
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	804.522583	804.522522 ± 0.0625	<b>✓</b>
A A A A A A A A A A A A A A A A A A A	004 500500	004 500500 : 0 0005	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>
RegressionFit	1	RegressionFit	1	-
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	~

804.522583

804.522583

42

804.522522 ± 0.0625

804.522522 ± 0.0625

42 ± 0.009

Test Step 2.47 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.46	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.63	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.8	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.97	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.14	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.31	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.47	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.65	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.81	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.99	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	4.15
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4.33
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4.5
MtrVel3_PosBuffer_Rad_M_f32[1][5] MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.67 4.84
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5.01
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5.18
MtrVel3_PosBuffer_Rad_M_f32[2][1]	5.35
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.52
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5.69
MtrVel3_PosBuffer_Rad_M_f32[2][4]	5.86
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.03
MtrVel3_PosBuffer_Rad_M_f32[2][6] MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.2 1.3
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.6
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.7
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.8
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.5
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4.6
MtrVel3_PosBuffer_Rad_M_f32[3][7]	4.7
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]  MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	2069 2131
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	2193
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1759
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1821
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1883
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1945
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	2007
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3410 3370
MtrVel3_TimeBuffer_uS_M_u16p0[1][2] MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	3333
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	3290
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	3207
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	3175
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	3100
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2450
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2512
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2574
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]  MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2636 2698
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2760
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2822
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	2388
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	6890
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6810
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6770
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6700
MtrVel3_TimeBuffer_uS_M_u16p0[3][4] MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6680 6610
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]  MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6610 6540
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6504
MtrVel HwVel HwRadpS M f32	13.81390291
MtrVel_MtrVelMRF_MtrRadpS_M_f32	469.5
MtrVel_OldPosBuf_Cnt_M_u08	0
MtrVel_OsBufSelect_Cnt_M_u08	2
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	4.3
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	754558
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[0] T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_UIs_f32	0.91
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	7
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	7
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	756758
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	19661
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08 tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
IIII KIE IOST SA MITVELMITVEL PETT HANDWNEELVEL HWRADS 132	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32



Name	Input Value			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_t	32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwRa	tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	dpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	~	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1097.73877	-1097.73877 ± 0.0625	•	
MtrVel_OldPosBuf_Cnt_M_u08	2	2	•	
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.88497484	1.884974767 ± 0.0625	~	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	756758	756758	•	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	~	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	1097.73877	1097.73877 ± 0.0625	•	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1097.73877	-1097.73877 ± 0.0625	•	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	•	
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	-1097.73877	-1097.73877 ± 0.0625	•	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~	
CalcCoarseVel	1	CalcCoarseVel	1	~	
RegressionFit	1	RegressionFit	1	~	
MtrVelBlend	1	MtrVelBlend	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>	

lama	Innut Value	
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	5	
ItrVel3_OsBufPos_Cnt_M_u08[1]	6	
ItrVel3_OsBufPos_Cnt_M_u08[2]	7	
ItrVel3_OsBufPos_Cnt_M_u08[3]	4	
ItrVel3_PosBuffer_Rad_M_f32[0][0]	2.48	
ItrVel3_PosBuffer_Rad_M_f32[0][1]	2.66	
ItrVel3_PosBuffer_Rad_M_f32[0][2]	2.84	
ItrVel3_PosBuffer_Rad_M_f32[0][3]	3.02	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3.2	
ItrVel3_PosBuffer_Rad_M_f32[0][5]	3.38	
ItrVel3_PosBuffer_Rad_M_f32[0][6]	3.55	
ItrVel3_PosBuffer_Rad_M_f32[0][7]	3.74	
ItrVel3_PosBuffer_Rad_M_f32[1][0]	3.91	
ItrVel3_PosBuffer_Rad_M_f32[1][1]	4.1	
htrVel3_PosBuffer_Rad_M_f32[1][2]	4.27	
ItrVel3_PosBuffer_Rad_M_f32[1][3]	4.46	
ltrVel3_PosBuffer_Rad_M_f32[1][4]	1.5	
trVel3_PosBuffer_Rad_M_f32[1][5]	1.6	
ItrVel3_PosBuffer_Rad_M_f32[1][6]	1.7	
ItrVel3_PosBuffer_Rad_M_f32[1][7]	1.8	
ItrVel3_PosBuffer_Rad_M_f32[2][0]	1.9	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	2	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	2.1	
trVel3_PosBuffer_Rad_M_f32[2][3]	2.2	
ltrVel3_PosBuffer_Rad_M_f32[2][4]	2.3	
trVel3_PosBuffer_Rad_M_f32[2][5]	2.4	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	2.5	
ltrVel3_PosBuffer_Rad_M_f32[2][7]	2.6	
ltrVel3_PosBuffer_Rad_M_f32[3][0]	2.7	
ltrVel3_PosBuffer_Rad_M_f32[3][1]	2.8	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	2.9	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	3	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	3.1	
ltrVel3_PosBuffer_Rad_M_f32[3][5]	3.2	
ltrVel3_PosBuffer_Rad_M_f32[3][6]	3.3	
ltrVel3_PosBuffer_Rad_M_f32[3][7]	3.4	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	3383	
ltrVel3_TimeBuffer_uS_M_u16p0[0][1]	3445	
ltrVel3_TimeBuffer_uS_M_u16p0[0][2]	3507	
htrVel3_TimeBuffer_uS_M_u16p0[0][3]	3569	
/trVel3_TimeBuffer_uS_M_u16p0[0][4]	3631	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3693	

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MtrVel\_Per1

10tt V C1_1 C1 1			1000
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3259		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3321		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	20200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	35000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	34008		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000		
	32906		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	2517		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	2579		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	2641		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	2703		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	2765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	2331		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	2393		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	2455		
MtrVel_HwVel_HwRadpS_M_f32	39.10801578		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-421.600006		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	0		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	2.6		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	88583		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_UIs_f32	0.94		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	8		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	9		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	90783		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	13107		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_0	Cnt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadp	oS f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestam		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2 Rev u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadp		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtr		
Name	Actual Value	Expected Value	Resul
	-42	·	Resul
MtrVel_HwVel_HwRadpS_M_f32		-42 ± 0.0625	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-610.628174	-610.6282349 ± 0.0625	
MtrVel_OldPosBuf_Cnt_M_u08	0	0	•
MtrVel_OsBufSelect_Cnt_M_u08	1	1	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1.2566179	1.256617887 ± 0.0625	•
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	90783	90783	•
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	•
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-610.628174	-610.6282349 ± 0.0625	•
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-610.628174	-610.6282349 ± 0.0625	•
tot MtrVel Per1 SysCHandwheelVel HwRadnS f32 value	-42	-42 + 0 009	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~	
CalcCoarseVel	1	CalcCoarseVel	1	<b>✓</b>	
RegressionFit	1	RegressionFit	1	<b>✓</b>	
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•	

-610.628174

-42

-42 ± 0.009

-610.6282349 ± 0.0625

tgt\_MtrVel\_Per1\_SysCHandwheelVel\_HwRadpS\_f32.value



#### Test Case 3: Path Test

Specification

Performance Metrics
(With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 3.1 4449 Cycles
TS 3.2 4557 Cycles
TS 3.3 4542 Cycles
TS 3.3 4542 Cycles
TS 3.4 4407 Cycles
TS 3.5 4483 Cycles

VECTOR DESCRIPTION: Description

 $\begin{array}{lll} TS3.1 & \text{if (MtrVel\_PosBuffer\_Rad\_T\_f32} > D\_Pl\_ULS\_F32) = True \\ TS3.2 & \text{"if (MtrVel\_PosBuffer\_Rad\_T\_f32} > D\_Pl\_ULS\_F32) = False; \\ (MtrVel\_PosBuffer\_Rad\_T\_f32 <= -D\_Pl\_ULS\_F32) = True" \\ TS3.3 & \text{"if (MtrVel\_PosBuffer\_Rad\_T\_f32} > D\_Pl\_ULS\_F32) = False; \\ (MtrVel\_PosBuffer\_Rad\_T\_f32 <= -D\_Pl\_ULS\_F32) = False" \\ TS3.4 & (MtrVel\_MtrRadpS\_T\_f32 <= -1350) = True \\ TS3.5 & (MtrVel\_MtrRadpS\_T\_f32 <= -1350) = True \\ \end{array}$ 

lame	Input Value	
ltrVel3_OsBufPos_Cnt_M_u08[0]	2	
ItrVel3_OsBufPos_Cnt_M_u08[1]	4	
ItrVel3_OsBufPos_Cnt_M_u08[2]	0	
ltrVel3_OsBufPos_Cnt_M_u08[3]	2	
ItrVel3_PosBuffer_Rad_M_f32[0][0]	2.16	
ttrVel3_PosBuffer_Rad_M_f32[0][1]	2.18	
ttrVel3_PosBuffer_Rad_M_f32[0][2]	2.2	
ttrVel3_PosBuffer_Rad_M_f32[0][3]	2.22	
	2.24	
ItrVel3_PosBuffer_Rad_M_f32[0][4]	2.24	
htrVel3_PosBuffer_Rad_M_f32[0][5]		
htrVel3_PosBuffer_Rad_M_f32[0][6]	2.28 2.3	
ItrVel3_PosBuffer_Rad_M_f32[0][7]		
ItrVel3_PosBuffer_Rad_M_f32[1][0]	2.32	
ItrVel3_PosBuffer_Rad_M_f32[1][1]	2.34	
ItrVel3_PosBuffer_Rad_M_f32[1][2]	2.36	
ItrVel3_PosBuffer_Rad_M_f32[1][3]	2.38	
ItrVel3_PosBuffer_Rad_M_f32[1][4]	2.4	
ItrVel3_PosBuffer_Rad_M_f32[1][5]	2.42	
ItrVel3_PosBuffer_Rad_M_f32[1][6]	2.44	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	2.46	
ltrVel3_PosBuffer_Rad_M_f32[2][0]	2.48	
ltrVel3_PosBuffer_Rad_M_f32[2][1]	2.5	
ltrVel3_PosBuffer_Rad_M_f32[2][2]	2.52	
ltrVel3_PosBuffer_Rad_M_f32[2][3]	2.54	
ltrVel3_PosBuffer_Rad_M_f32[2][4]	2.56	
ltrVel3_PosBuffer_Rad_M_f32[2][5]	2.58	
ltrVel3_PosBuffer_Rad_M_f32[2][6]	2.6	
ltrVel3_PosBuffer_Rad_M_f32[2][7]	2.62	
ltrVel3_PosBuffer_Rad_M_f32[3][0]	2.64	
ltrVel3_PosBuffer_Rad_M_f32[3][1]	2.66	
ltrVel3_PosBuffer_Rad_M_f32[3][2]	2.68	
ltrVel3_PosBuffer_Rad_M_f32[3][3]	2.7	
ltrVel3_PosBuffer_Rad_M_f32[3][4]	2.72	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	2.74	
ItrVel3_PosBuffer_Rad_M_f32[3][6]	2.76	
ItrVel3_PosBuffer_Rad_M_f32[3][7]	2.78	
ltrVel3_TimeBuffer_uS_M_u16p0[0][0]	111	
ItrVel3_TimeBuffer_uS_M_u16p0[0][1]	221	
ItrVel3_TimeBuffer_uS_M_u16p0[0][2]	331	
ltrVel3_TimeBuffer_uS_M_u16p0[0][3]	441	
ttrVel3_TimeBuffer_uS_M_u16p0[0][4]	551	
trVel3_TimeBuffer_uS_M_u16p0[0][5]	661	
htrVel3_TimeBuffer_uS_M_u16p0[0][6]	624	
ttrVel3_TimeBuffer_uS_M_u16p0[0][7]	630	
htrVel3_TimeBuffer_uS_M_u16p0[1][0]	702	
htrVel3_TimeBuffer_uS_M_u16p0[1][1]	750	
ttrVel3_TimeBuffer_uS_M_u16p0[1][2]	806	
ttrVel3_TimeBuffer_uS_M_u16p0[1][3]	856	
	901	
htrVel3_TimeBuffer_uS_M_u16p0[1][4]	945	
htrVel3_TimeBuffer_uS_M_u16p0[1][5]	990	
htrVel3_TimeBuffer_uS_M_u16p0[1][6]		
ItrVel3_TimeBuffer_uS_M_u16p0[1][7]	1030	
ltrVel3_TimeBuffer_uS_M_u16p0[2][0]	175	

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MtrVel Per1

MtrVel_Per1			MACICAL
Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	65339		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	65401		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	65463		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	65525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	51		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	113		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	301		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	363		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	425		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	65527		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	53		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	115		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	177		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	239		
MtrVel HwVel HwRadpS M f32	-2.767454803		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	676.0312533		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	6046		
Rte Inst Sa MtrVel	tgt Rte Inst Sa MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T MtrVelBlendTblY Uls u2p14[1]	16384		
k GearRatio Uls f32	0.14		
t MtrVelBlendTblX MtrRadpS u12p4[0]	6		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	7846		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535		
tgt Rte Inst Sa MtrVel.MtrVel Per1 AsstAssemblyPolarity Cnt s08	tgt MtrVel Per1 AsstAssemblyPolarity C	Cnt s08	
tgt Rte Inst Sa MtrVel.MtrVel Per1 HandwheelVel HwRadpS f32	tgt MtrVel Per1 HandwheelVel HwRadp	_	
tgt Rte Inst Sa MtrVel.MtrVel Per1 MechMtrPos2Timestamp USec u32	tgt MtrVel Per1 MechMtrPos2Timestam	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p		
tgt Rte Inst Sa MtrVel.MtrVel Per1 MotorVelCRF MtrRadpS f32	tgt MtrVel Per1 MotorVelCRF MtrRadps		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadp	_	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_Hw		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_Mtrl	. –	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	rooun
MtrVel MtrVelMRF MtrRadpS M f32	640.000244	640 ± 0.0625	<u> </u>
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel OsBufSelect Cnt M u08	0	0	
	6.28308964	6.283089433 ± 0.0625	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32 MtrVel_PrevMtrPosSampleTime2_uS_M_u32	7846	7846	· ·
	42	42 ± 0.009	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	640.000244	42 ± 0.009 640 ± 0.0625	
tgt_iviti vei_r ei i_ivi0t0i vei0Kr_ivitiKaupo_ioz.value	040.000244	040 I 0.0025	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
Rte Call MtrVel Per1 CP1 ChecknointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	_

640.000244

640.000244

42

640 ± 0.0625

640 ± 0.0625

42 ± 0.009

Test Step 3.2 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	2	
MtrVel3_OsBufPos_Cnt_M_u08[1]	4	
MtrVel3_OsBufPos_Cnt_M_u08[2]	6	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2.28	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.36	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.44	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.52	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.6	

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

 $tgt\_MtrVel\_Per1\_SysCH and wheelVel\_HwRadpS\_f32.value$ 

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MtrVel\_Per1



Name         Import Value Statistics, Stat. M., CLICIUG         288           Neveral, producture, Stat. M., CLICIUG         276           Neveral, producture, Stat. M., CLICIUG         284           Neveral, producture, Stat. M., CLICIUG         284           Neveral, producture, Stat. M., CLICIUG         280           Neveral, producture, Stat. M., CLICIUG         280           Neveral, producture, Stat. M., CLICIUG         284           Neveral, producture, Stat. M., CLICIUG         324           Neveral, producture, Stat. M., CLICIUG         344           Neveral, producture, Stat. M., CLICIUG         348           Neveral, producture, Stat. M., CLICIUG         348           Neveral, producture, Stat. M., CLICIUG         349           Neveral, producture, Stat. M., CLICIUG         349           Neveral, producture, Stat. M., CLICIUG         340           Neveral, producture, Stat. M., CLICIUG         440           Neveral, producture, Stat. M., CLICIUG         440           Neveral, producture, Stat. M., CLICIUG         440		
Marvell Postular Feel   1900 01   276   Marvell Postular   1900 07   244   2	Name	Input Value
Mercia Pesidane Red M. (2010)  Mirce Pesidane Red M. (2010)  2 92  Mirce Pesidane Red M. (2010)  3 1  Mirce Pesidane Red M. (2010)  3 2  Mirce Pesidane Red M. (2010)  3 2  Mirce Pesidane Red M. (2010)  3 3  Mirce Pesidane Red M. (2010)  3 4  Mirce Pesidane Red M. (2010)  3 4  Mirce Pesidane Red M. (2010)  3 5  Mirce Pesidane Red M. (2010)  3 6  Mirce Pesidane Red M. (2010)  3 72  Mirce Pesidane Red M. (2010)  3 72  Mirce Pesidane Red M. (2010)  3 8  Mirce Pesidane Red M. (2010)  4 10  Mirce Pesidane Red M. (2010)  4 10  Mirce Pesidane Red M. (2010)  4 10  Mirce Pesidane Red M. (2010)  4 2  Mirce Pesidane Red M. (2010)  4 3  Mirce Pesidane Red M. (2010)  4 3  Mirce Pesidane Red M. (2010)  4 4  Mirce Pesidane Red M. (2010)  4 5  Mirce Pesidane Red M. (2010)  4 6  Mirce Pesidane Red M. (2010)  4 7  Mirce Pesidane Red M. (2010)  4 8  Mirce Pesidane Red M. (2010)  4 9  Mirce Pesidane	MtrVel3 PosBuffer Rad M f32[0][5]	•
Noverly Position   Feet   1907  0    264		
Montage   Marked		
Minystap Pauliture (Park W 1921 12)   3.00		
Mirviella Publisher (1944 M. 1921)  Mirviella Publisher (1944 M. 1922)  Mirviella Publisher (1944 M. 1		
Ministrate   Passalanter   Passa M. 1921 79    3.24		
Minvide_Postular_Food_M_120_119   3.24	MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.08
Minyest   Pestature   Pestat	MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.16
Mirvell_Positific_Post_M_POSITION   Mirvell_Positific_Post_M_POSITION	MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.24
Mirvell_Positific_Post_M_POSITION   Mirvell_Positific_Post_M_POSITION	MtrVel3 PosBuffer Rad M f32[1][5]	3.32
Minchest_Pesither_Read_M_02[17]   3-68   Minchest_Pesither_Read_M_02[17]   3-79   Minchest_Pesither_Read_M_02[17]   3-79   Minchest_Pesither_Read_M_02[17]   3-79   Minchest_Pesither_Read_M_02[17]   3-89   Minchest_Pesither_Read_M_02[17]   3-80   Minchest_Pesither_Read_M_02[17]   3-80   Minchest_Pesither_Read_M_02[17]   3-80   Minchest_Pesither_Read_M_02[17]   4-12   Minchest_Pesither_Read_M_02[17]   4-13   Minchest_Pesither_Read_M_02[17]   4-16   Minchest_Pesither_Read_M_02[17]   4-16   Minchest_Pesither_Read_M_02[17]   4-17   Minchest_Pesither_Read_M_02[17]   4-18   Minchest_Pesither_Read_M_02[17]   4		
Microla Possibilities and M. 1920/19   Microla Possibilit		
Mervica   Production   Paul M. 1927 21   3.94		
Mervice   Production   Rad   M. (20202)   5.8		
Mervice   Desputing Rad   M. (20219)   3.8		
Mrviss   Postubur   Rad   M. 1922 19   5.88	MtrVel3_PosBuffer_Rad_M_f32[2][2]	
Milwids   Designifier   Rast   M. 1922[25]   404	MtrVel3_PosBuffer_Rad_M_f32[2][3]	3.8
MeVes   Positisher Ray   M. 1923  7   4.12	MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.88
Minyell Posturiner Rad   M. 1923 17    4.12   4.24   4.2	MtrVel3_PosBuffer_Rad_M_f32[2][5]	3.96
Minyell Posturiner Rad   M. 1923 17    4.12   4.24   4.2		4.04
Mrv49.5   Pesither Rad   M. 125 19		
Mirves   Posburker Ray   M. 1203 12		
Marvas   Possberger Read   M. 2023 23		
Marves   Posebure   Rad   M. 1203 13    Marves   Timebure   Lis   M. 1050 10    Marv		
MVV4S   PosBuffer Rad M. (323) 4    452		
Micros   Posbuffer, Rad   M. 120 19    46	MtrVel3_PosBuffer_Rad_M_f32[3][3]	4.44
MrVMSJ, Posburfer, Pad. M. (323) 37         4.88           MrVMSJ, Posburfer, Lad. M. (323) 37         4.76           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 1         5142           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 2         6206           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 3         5832           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 3         5832           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 5         5956           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 5         6018           MrVMSJ, TimeBuffer, LuS. M., (156)(01) 1         1500           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 1         1500           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 2         1480           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 3         1460           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 3         1460           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 4         1440           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 5         1420           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 6         1400           MrVMSJ, TimeBuffer, LuS. M., (156)(11) 7         1380           MrVMSJ, TimeBuffer, LuS. M., (156)(12) 1         7770           MrVMSJ, TimeBuffer, LuS. M., (156)(12) 1         7700           MrVMSJ, TimeBuffer, LuS. M., (156)(12) 2         7610           MrVMSJ, TimeBuffer, LuS. M., (156)(13) 3	MtrVel3_PosBuffer_Rad_M_f32[3][4]	4.52
MrV48]	MtrVel3_PosBuffer_Rad_M_f32[3][5]	4.6
MerVelS_TimeBuffer_US_M_U16p0[0]0   6142		4.68
MIVVIS J. TIMEBURFIC U.S. M. U1500[01]         6142           MIVVIS J. TIMEBURFIC U.S. M. U1500[012]         6264           MIVVIS J. TIMEBURFIC U.S. M. U1500[012]         6266           MIVVIS J. TIMEBURFIC U.S. M. U1500[013]         5852           MIVVIS J. TIMEBURFIC U.S. M. U1500[015]         606           MIVVIS J. TIMEBURFIC U.S. M. U1500[017]         6080           MIVVIS J. TIMEBURFIC U.S. M. U1500[017]         1520           MIVVIS J. TIMEBURFIC U.S. M. U1500[017]         1500           MIVVIS J. TIMEBURFIC U.S. M. U1500[017]         1400           MIVVIS J. TIMEBURFIC U.S. M. U1500[113]         1400           MIVVIS J. TIMEBURFIC U.S. M. U1500[121]         7777           MIVVIS J. TIMEBURFIC U.S. M. U1500[121]         770           MIVVIS J. TIMEBURFIC U.S. M. U1500[121]         7500           MIVVIS J. TIMEBURFIC U.S. M. U1500[121]         7500           MIVVIS J. TIMEBURFIC U.S. M. U1500[121]         7500           MIVVIS J. TIMEBURFIC U.S. M. U1500[121]         7600		
Mirvels_TimeBuffer_us_M_urlsp00[11]         6204           Mirvels_TimeBuffer_us_M_urlsp00[12]         6268           Mirvels_TimeBuffer_us_M_urlsp00[13]         5852           Mirvels_TimeBuffer_us_M_urlsp00[16]         5984           Mirvels_TimeBuffer_us_M_urlsp00[16]         5968           Mirvels_TimeBuffer_us_M_urlsp00[17]         6868           Mirvels_TimeBuffer_us_M_urlsp00[17]         6869           Mirvels_TimeBuffer_us_M_urlsp00[17]         6860           Mirvels_TimeBuffer_us_M_urlsp00[17]         1500           Mirvels_TimeBuffer_us_M_urlsp00[17]         1480           Mirvels_TimeBuffer_us_M_urlsp00[18]         1460           Mirvels_TimeBuffer_us_M_urlsp00[18]         1460           Mirvels_TimeBuffer_us_M_urlsp00[18]         1420           Mirvels_TimeBuffer_us_M_urlsp00[18]         1420           Mirvels_TimeBuffer_us_M_urlsp00[17]         1380           Mirvels_TimeBuffer_us_M_urlsp00[17]         1380           Mirvels_TimeBuffer_us_M_urlsp00[21]         777           Mirvels_TimeBuffer_us_M_urlsp00[21]         770           Mirvels_TimeBuffer_us_M_urlsp00[21]         770           Mirvels_TimeBuffer_us_M_urlsp00[21]         736           Mirvels_TimeBuffer_us_M_urlsp00[21]         7378           Mirvels_TimeBuffer_us_M_urlsp00[21]		
MrVeS_TimeBuffer_US_M_u16p0[0][3]         6266           MrVeS_TimeBuffer_US_M_u16p0[0][3]         5832           MrVeS_TimeBuffer_US_M_u16p0[0][5]         5894           MrVeS_TimeBuffer_US_M_u16p0[0][6]         6018           MrVeS_TimeBuffer_US_M_u16p0[0][7]         6080           MrVeS_TimeBuffer_US_M_u16p0[1][7]         1500           MrVeS_TimeBuffer_US_M_u16p0[1][7]         1480           MrVeS_TimeBuffer_US_M_u16p0[1][7]         1480           MrVeS_TimeBuffer_US_M_u16p0[1][7]         1440           MrVeS_TimeBuffer_US_M_u16p0[1][8]         1400           MrVeS_TimeBuffer_US_M_u16p0[1][9]         1420           MrVeS_TimeBuffer_US_M_u16p0[1][9]         1420           MrVeS_TimeBuffer_US_M_u16p0[1][9]         1380           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7777           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7777           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7700           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7300           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7435           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7435           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7435           MrVeS_TimeBuffer_US_M_u16p0[2][9]         7400           MrVeS_TimeBuffer_US_M_u16p0[2][9]         666		
MRYV83_TimeBuffer_uS_M_u16p0[0] 3   5832		
MrVaS J. TimeBuffer u.S. M. u16p00[16]         5894           MrVaS J. TimeBuffer u.S. M. u16p00[16]         6018           MrVaS J. TimeBuffer u.S. M. u16p00[17]         6080           MrVaS J. TimeBuffer u.S. M. u16p00[17]         1500           MrVaS J. TimeBuffer u.S. M. u16p00[11]         1500           MrVaS J. TimeBuffer u.S. M. u16p01[13]         1480           MrVaS J. TimeBuffer u.S. M. u16p01[13]         1480           MrVaS J. TimeBuffer u.S. M. u16p01[16]         1420           MrVaS J. TimeBuffer u.S. M. u16p01[16]         1400           MrVaS J. TimeBuffer u.S. M. u16p01[17]         1380           MrVaS J. TimeBuffer u.S. M. u16p01[17]         1380           MrVaS J. TimeBuffer u.S. M. u16p01[17]         777           MrVaS J. TimeBuffer u.S. M. u16p01[17]         770           MrVaS J. TimeBuffer u.S. M. u16p01[17]         770           MrVaS J. TimeBuffer u.S. M. u16p01[17]         780           MrVaS J. TimeBuffer u.S. M. u16p01[17]         730           MrVaS J. TimeBuffer u.S. M. u16p01[17]         736           MrVaS J. TimeBuffer u.S. M. u16p01[18]         736           MrVaS J. TimeBuffer u.S. M. u16p01[18]         736           MrVaS J. TimeBuffer u.S. M. u16p01[18]         6504           MrVaS J. TimeBuffer u.S. M. u16p01[18]         6504		
MMV483, TimeBuffer, u.S. M_ u16p0[0][5]   5056   5018	MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	5832
MrVel3, TmeBuffer, u.S. M. u16p0[0][7]         608           MrVel3, TmeBuffer, u.S. M. u16p0[0][7]         1520           MrVel3, TimeBuffer, u.S. M. u16p0[1][9]         1520           MrVel3, TimeBuffer, u.S. M. u16p0[1][9]         1500           MrVel3, TimeBuffer, u.S. M. u16p0[1][3]         1480           MrVel3, TimeBuffer, u.S. M. u16p0[1][3]         1460           MrVel3, TimeBuffer, u.S. M. u16p0[1][4]         1440           MrVel3, TimeBuffer, u.S. M. u16p0[1][6]         1420           MrVel3, TimeBuffer, u.S. M. u16p0[1][7]         1380           MrVel3, TimeBuffer, u.S. M. u16p0[2][9]         7777           MrVel3, TimeBuffer, u.S. M. u16p0[2][9]         7777           MrVel3, TimeBuffer, u.S. M. u16p0[2][4]         7500           MrVel3, TimeBuffer, u.S. M. u16p0[2][4]         7500           MrVel3, TimeBuffer, u.S. M. u16p0[2][4]         7500           MrVel3, TimeBuffer, u.S. M. u16p0[2][6]         7400           MrVel3, TimeBuffer, u.S. M. u16p0[2][7]         7378           MrVel3, TimeBuffer, u.S. M. u16p0[2][8]         6564           MrVel3, TimeBuffer, u.S. M. u16p0[3][3]         6594           MrVel3, TimeBuffer, u.S. M. u16p0[3][3]         6194           MrVel3, TimeBuffer, u.S. M. u16p0[3][3]         6194           MrVel3, TimeBuffer, u.S. M. u16p0[3][3]         6194	MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	5894
MMVR3_TimeBuffer_u.S. M_u16p0[1]0]         6080           MMVR3_TimeBuffer_u.S. M_u16p0[1]10]         1520           MMVR3_TimeBuffer_u.S. M_u16p0[1]2]         1480           MMVR4_TimeBuffer_u.S. M_u16p0[1]3]         1480           MMVR4_TimeBuffer_u.S. M_u16p0[1]4]         1440           MMVR4_TimeBuffer_u.S. M_u16p0[1]5]         1420           MMVR4_TimeBuffer_u.S. M_u16p0[1]6]         1400           MMVR4_TimeBuffer_u.S. M_u16p0[1]7]         1380           MMVR4_TimeBuffer_u.S. M_u16p0[2]10]         7777           MMVR4_TimeBuffer_u.S. M_u16p0[2]11         7770           MMVR4_TimeBuffer_u.S. M_u16p0[2]2]         7610           MMVR4_TimeBuffer_u.S. M_u16p0[2]3         7590           MMVR4_TimeBuffer_u.S. M_u16p0[2]4         7500           MMVR4_TimeBuffer_u.S. M_u16p0[2]5         7435           MMVR4_TimeBuffer_u.S. M_u16p0[2]6         7400           MMVR4_TimeBuffer_u.S. M_u16p0[2]7         7378           MMVR4_TimeBuffer_u.S. M_u16p0[3]1         6564           MMVR4_TimeBuffer_u.S. M_u16p0[3]1         6564           MMVR4_TimeBuffer_u.S. M_u16p0[3]2         6132           MMVR4_TimeBuffer_u.S. M_u16p0[3]3         6134           MMVR4_TimeBuffer_u.S. M_u16p0[3]3         6134           MMVR4_TimeBuffer_u.S. M_u16p0[3]3         6318 <tr< td=""><td>MtrVel3_TimeBuffer_uS_M_u16p0[0][5]</td><td>5956</td></tr<>	MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	5956
MrVe3_TmeBuffer_uS_M_16p0[1]0         6880           MrVe3_TmeBuffer_uS_M_16p0[1]10         1520           MrVe3_TimeBuffer_uS_M_16p0[1]21         1800           MrVe3_TimeBuffer_uS_M_16p0[1]21         1480           MrVe3_TimeBuffer_uS_M_16p0[1]31         1460           MrVe3_TimeBuffer_uS_M_16p0[1]41         1440           MrVe3_TimeBuffer_uS_M_16p0[1]51         1420           MrVe3_TimeBuffer_uS_M_16p0[1]71         1380           MrVe3_TimeBuffer_uS_M_16p0[1]71         1380           MrVe3_TimeBuffer_uS_M_16p0[2]10         7777           MrVe3_TimeBuffer_uS_M_16p0[2]11         7700           MrVe3_TimeBuffer_uS_M_16p0[2]21         7610           MrVe3_TimeBuffer_uS_M_16p0[2]213         7590           MrVe3_TimeBuffer_uS_M_16p0[2]213         7590           MrVe3_TimeBuffer_uS_M_16p0[2]215         7435           MrVe3_TimeBuffer_uS_M_16p0[2]21         730           MrVe3_TimeBuffer_uS_M_16p0[2]21         738           MrVe3_TimeBuffer_uS_M_16p0[2]21         738           MrVe3_TimeBuffer_uS_M_16p0[2]21         6564           MrVe3_TimeBuffer_uS_M_16p0[3]21         6564           MrVe3_TimeBuffer_uS_M_16p0[3]21         6526           MrVe3_TimeBuffer_uS_M_16p0[3]21         632           MrVe3_TimeBuffer_uS_M_16p0[3]21 <t< td=""><td></td><td>6018</td></t<>		6018
MrtVel3_TimeBuffer_uS_M_u16p0[1]01         1520           MrtVel3_TimeBuffer_uS_M_u16p0[1]21         1480           MrtVel3_TimeBuffer_uS_M_u16p0[1]31         1480           MrtVel3_TimeBuffer_uS_M_u16p0[1]31         1480           MrtVel3_TimeBuffer_uS_M_u16p0[1]31         1440           MrtVel3_TimeBuffer_uS_M_u16p0[1]31         1420           MrtVel3_TimeBuffer_uS_M_u16p0[1]31         1400           MrtVel3_TimeBuffer_uS_M_u16p0[1]31         7777           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7770           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7500           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7500           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7500           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7500           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7400           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         7400           MrtVel3_TimeBuffer_uS_M_u16p0[2]31         6504           MrtVel3_TimeBuffer_uS_M_u16p0[3]31         6504           MrtVel3_TimeBuffer_uS_M_u16p0[3]31         6504           MrtVel3_TimeBuffer_uS_M_u16p0[3]31         6504           MrtVel3_TimeBuffer_uS_M_u16p0[3]31         6304           MrtVel3_TimeBuffer_uS_M_u16p0[3]31         6306           MrtVel3_TimeBuffer_uS_M_u16p0[3]31         6308 <td></td> <td></td>		
MitVela_TimeBuffer_uS_M_u16p0[1][1]         1500           MitVela_TimeBuffer_uS_M_u16p0[1][2]         1480           MitVela_TimeBuffer_uS_M_u16p0[1][3]         1460           MitVela_TimeBuffer_uS_M_u16p0[1][4]         1440           MitVela_TimeBuffer_uS_M_u16p0[1][5]         1420           MitVela_TimeBuffer_uS_M_u16p0[1][7]         1380           MitVela_TimeBuffer_uS_M_u16p0[1][7]         1380           MitVela_TimeBuffer_uS_M_u16p0[2][7]         7777           MitVela_TimeBuffer_uS_M_u16p0[2][7]         7700           MitVela_TimeBuffer_uS_M_u16p0[2][8]         7560           MitVela_TimeBuffer_uS_M_u16p0[2][8]         7560           MitVela_TimeBuffer_uS_M_u16p0[2][8]         7435           MitVela_TimeBuffer_uS_M_u16p0[2][8]         7435           MitVela_TimeBuffer_uS_M_u16p0[2][8]         7436           MitVela_TimeBuffer_uS_M_u16p0[2][9]         7400           MitVela_TimeBuffer_uS_M_u16p0[2][1]         6504           MitVela_TimeBuffer_uS_M_u16p0[3][1]         6566           MitVela_TimeBuffer_uS_M_u16p0[3][1]         6566           MitVela_TimeBuffer_uS_M_u16p0[3][3]         6194           MitVela_TimeBuffer_uS_M_u16p0[3][4]         6256           MitVela_TimeBuffer_uS_M_u16p0[3][5]         6318           MitVel_TimeBuffer_uS_M_u16p0[3][6]         <		
Mitvela_TimeBuffer_us_M_u16p0[1][2]         1480           Mitvela_TimeBuffer_us_M_u16p0[1][3]         1460           Mitvela_TimeBuffer_us_M_u16p0[1][5]         1420           Mitvela_TimeBuffer_us_M_u16p0[1][5]         1420           Mitvela_TimeBuffer_us_M_u16p0[1][7]         1380           Mitvela_TimeBuffer_us_M_u16p0[2][0]         7777           Mitvela_TimeBuffer_us_M_u16p0[2][1]         7700           Mitvela_TimeBuffer_us_M_u16p0[2][3]         7890           Mitvela_TimeBuffer_us_M_u16p0[2][3]         7890           Mitvela_TimeBuffer_us_M_u16p0[2][6]         7435           Mitvela_TimeBuffer_us_M_u16p0[2][6]         7400           Mitvela_TimeBuffer_us_M_u16p0[2][6]         7400           Mitvela_TimeBuffer_us_M_u16p0[2][7]         7378           Mitvela_TimeBuffer_us_M_u16p0[2][7]         7378           Mitvela_TimeBuffer_us_M_u16p0[2][7]         6504           Mitvela_TimeBuffer_us_M_u16p0[3][8]         6504           Mitvela_TimeBuffer_us_M_u16p0[3][8]         6194           Mitvela_TimeBuffer_us_M_u16p0[3][8]         6194           Mitvela_TimeBuffer_us_M_u16p0[3][8]         6194           Mitvela_TimeBuffer_us_M_u16p0[3][8]         6318           Mitvela_TimeBuffer_us_M_u16p0[3][8]         6318           Mitvel_Muffer_us_M_u16p0[3][8]         6		
MitVel3_TimeBuffer_uS_M_u16p0[1][3]         1460           MitVel3_TimeBuffer_uS_M_u16p0[1][6]         1420           MitVel3_TimeBuffer_uS_M_u16p0[1][6]         1420           MitVel3_TimeBuffer_uS_M_u16p0[1][7]         1380           MitVel3_TimeBuffer_uS_M_u16p0[2][0]         7777           MitVel3_TimeBuffer_uS_M_u16p0[2][1]         7700           MitVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MitVel3_TimeBuffer_uS_M_u16p0[2][3]         7590           MitVel3_TimeBuffer_uS_M_u16p0[2][6]         7800           MitVel3_TimeBuffer_uS_M_u16p0[2][6]         7400           MitVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MitVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MitVel3_TimeBuffer_uS_M_u16p0[3][7]         6566           MitVel3_TimeBuffer_uS_M_u16p0[3][8]         6194           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6194           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6380           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6380           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6380           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6442           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6380           MitVel3_TimeBuffer_uS_M_u16p0[3][9]         6380           MitVel4_TimeBuffer_uS_M_u16p0[3][9]		
MttVel3_TimeBuffer_u.S_M_u16p0[1][4]         1440           MttVel3_TimeBuffer_u.S_M_u16p0[1][5]         1420           MttVel3_TimeBuffer_u.S_M_u16p0[1][7]         1380           MttVel3_TimeBuffer_u.S_M_u16p0[2][0]         7777           MttVel3_TimeBuffer_u.S_M_u16p0[2][1]         7700           MttVel3_TimeBuffer_u.S_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_u.S_M_u16p0[2][3]         7590           MttVel3_TimeBuffer_u.S_M_u16p0[2][6]         7600           MttVel3_TimeBuffer_u.S_M_u16p0[2][6]         7400           MttVel3_TimeBuffer_u.S_M_u16p0[2][6]         7400           MttVel3_TimeBuffer_u.S_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_u.S_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_u.S_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_u.S_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_u.S_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_u.S_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_u.S_M_u16p0[3][7]         6442           MttVel3_TimeBuffer_u.S_M_u16p0[3][7]         6442           MttVel_Delfer_u.S_M_u16p0[3][7]         6442           MttVel_Delfer_u.S_M_u16p0[3][7]         6442           MttVel_Delfer_u.S_M_u16p0[3][7]         6442           MttVel_Delfer_u.S_M_u16p0[3][7]	MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	
Mitryel3_TimeBuffer_uS_M_u16p0[1][5]         1420           Mitryel3_TimeBuffer_uS_M_u16p0[1][6]         1400           Mitryel3_TimeBuffer_uS_M_u16p0[2][0]         7777           Mitryel3_TimeBuffer_uS_M_u16p0[2][1]         7700           Mitryel3_TimeBuffer_uS_M_u16p0[2][3]         7500           Mitryel3_TimeBuffer_uS_M_u16p0[2][3]         7590           Mitryel3_TimeBuffer_uS_M_u16p0[2][6]         7500           Mitryel3_TimeBuffer_uS_M_u16p0[2][6]         7400           Mitryel3_TimeBuffer_uS_M_u16p0[2][7]         7378           Mitryel3_TimeBuffer_uS_M_u16p0[3][1]         6504           Mitryel3_TimeBuffer_uS_M_u16p0[3][1]         6566           Mitryel3_TimeBuffer_uS_M_u16p0[3][2]         6132           Mitryel3_TimeBuffer_uS_M_u16p0[3][3]         6194           Mitryel3_TimeBuffer_uS_M_u16p0[3][6]         6318           Mitryel3_TimeBuffer_uS_M_u16p0[3][6]         6318           Mitryel3_TimeBuffer_uS_M_u16p0[3][7]         6442           Mitryel3_TimeBuffer_uS_M_u16p0[3][7]         6442           Mitryel4_HwRelp_Mrakaps_M_132         15.40252688           Mitryel_OthosBuf_Cnt_M_08         0           Mitryel_OthosBuf_Cnt_M_08         0           Mitryel_Depositif_Cnt_M_08         0           Mitryel_Depositif_Descript_M_08         6	MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1460
MttVel3_TimeBuffer_uS_M_u16p0[1][6]         1400           MttVel3_TimeBuffer_uS_M_u16p0[1][7]         1380           MttVel3_TimeBuffer_uS_M_u16p0[2][1]         7777           MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][3]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MttVel3_TimeBuffer_uS_M_u16p0[2][6]         7435           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MttVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MttVel3_TimeBuffer_uS_M_u16p0[3][5]         642           MttVel_HwRadpS_M_[32]         15.40252668           MttVel_HwRadpS_M_[32]         15.40252668           MttVel_UnbRadpS_M_[32]         6           MttVel_UnbRadpS_M_[32]         6           MttVel_UnbRadpS_M_[32]         6           MttVel_UnbRadpS_M_	MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1440
MttVel3_TimeBuffer_uS_M_u16p0[1][7]         1380           MttVel3_TimeBuffer_uS_M_u16p0[2][0]         7777           MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][3]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MttVel3_TimeBuffer_uS_M_u16p0[2][6]         7405           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MttVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel_UNdRF_MtrRadpS_M_f32         15.40252668           MttVel_UNdRF_MtrRadpS_M_f32         227.1569001           MttVel_OldPosBuf_Cnt_M_u08         3           MttVel_Dosbetch_Cnt_M_u08         3           MttVel_Dosbetch_Cnt_M_u08         6	MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1420
MttVel3_TimeBuffer_uS_M_u16p0[1][7]         1380           MttVel3_TimeBuffer_uS_M_u16p0[2][0]         7777           MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][3]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MttVel3_TimeBuffer_uS_M_u16p0[2][6]         7435           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6192           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel_Hwel_HwradpS_M_f32         15 40252668           MttVel_UNdMFp_MradpS_M_f32         227.1569001           MttVel_OdPosBuf_Cnt_M_u08         0           MttVel_DosbechMtPos2_Rad_M_f32         6           MttVel_PrevMtrPosMerchMtPos2_Rad_M_f32         6           MttVel_PrevMtrPosMerchMtPos2_Rad_M_f32         6	MtrVel3 TimeBuffer uS M u16p0[1][6]	1400
MttVel3_TimeBuffer_uS_M_u16p0[2][0]         7777           MttVel3_TimeBuffer_uS_M_u16p0[2][1]         7700           MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][3]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MttVel3_TimeBuffer_uS_M_u16p0[2][5]         7435           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6504           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6318           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel_MtVel_		1380
MttVel3_TimeBuffer_uS_M_u16p0[2][1]         7700           MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][5]         7435           MttVel3_TimeBuffer_uS_M_u16p0[2][6]         7400           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MttVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel_HwVel_HwRadpS_M_132         15.40252668           MttVel_OldPosBuf_Cnt_M_u08         0           MttVel_OsBufSelect_Cnt_M_u08         0           MttVel_OsBufSelect_Cnt_M_u08         0           MttVel_PrevMtrPosMechMtrPos2_Rad_M_132         6           MttVel_PrevMtrPosMechMtrPos2_Rad_M_132         6           MttVel_PrevMtrPosMechMtrPos2_Us_M_14[1]         10384      <		
MttVel3_TimeBuffer_uS_M_u16p0[2][2]         7610           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][5]         7435           MttVel3_TimeBuffer_uS_M_u16p0[2][6]         7400           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6564           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel_MtVelMRF_MtrRadpS_M_132         -227.1569001           MttVel_OldPosBuf_Cnt_M_u08         0           MttVel_OldPosBuf_Cnt_M_u08         0           MttVel_PrevMtrPosSampleTime2_uS_M_u132         6           MttVel_PrevMtrPosSampleTime2_uS_M_u132         6           Mtvel_PrevMtrPosSampleTime2_uS_M_u132         15(Rt_Re_Inst_Sa_MtrVel           T_MtvelBlendTbiv_Uis_u2p14[1]         16384           k_GearRatio_Uis_132         0		
MttVel3_TimeBuffer_uS_M_u16p0[2][3]         7590           MttVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MttVel3_TimeBuffer_uS_M_u16p0[2][5]         7435           MttVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MttVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MttVel3_TimeBuffer_uS_M_u16p0[3][1]         6132           MttVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MttVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MttVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MttVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MttVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MttVel_HwRadpS_M_[32         -15.40252668           MttVel_HwRedpS_M_[32         -227.1569001           MttVel_OldPosBuf_Cnt_M_u08         0           MttVel_OldPosBuf_Cnt_M_u08         3           MttVel_PrewMtrPosSempleTime2_us_M_u32         896076           **Le_Inst_Sa_MttVel         1           **Le_Inst_Sa_MttVel         0           **LuftVelBlendTblY_Us_u2p14[0]         0           **LuftVelBlendTblY_Us_u2p14[1]         16384           **CearRatio_Us_f32         0.2           **LuftVelBlendTblX_MtrRadpS_u12p4[0]         5 </td <td></td> <td></td>		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]         7500           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         7435           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         7400           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         7378           MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwRadpS_M_[32         -15.40252668           MtrVel_HwRadpS_M_[32         -227.1569001           MtrVel_MtrVelMRF_utrRadpS_M_[32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         3           MtrVel_PrevMtrPosMechMtrPos2_Rad_M_[32         6           MtrVel_PrevMtrPosMechMtrPos2_Rad_M_[32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         886076           Rte_inst_Sa_MtrVel         tgt_Rtinst_Sa_MtrVel           _InttVelBlendTblY_UIs_u2p14[0]         0           _LMtrVelBlendTblY_UIs_u2p14[1]         16384           _K_GearRatio_UIs_f32         0.2           _L		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         7435           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         7400           MtrVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwVel_HwRadpS_M_f32         -15.40252668           MtrVel_HwRadpS_M_f32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         0           MtrVel_OsBufSelect_Cnt_M_u08         3           MtrVel_PrevMtrPosNechMtrPos2_Rad_M_f32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         89076           Rte_Inst_Sa_MtrVel         tjt_Rie_Inst_Sa_MtrVel           T_MtrVelBiendTblY_UIs_u2p14[0]         0           T_MtrVelBiendTblY_UIs_u2p14[1]         16384           k_GearRatio_UIs_f32         0.2           t_MtrVelBiendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBiend		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         7400           MtrVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][5]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwradpS_M_f32         -15 A025668           MtrVel_HwrBF_MtrRadpS_M_f32         -227.1569001           MtrVel_OSBufSelect_Cnt_M_u08         0           MtrVel_OSBufSelect_Cnt_M_u08         3           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         896076           Kte_Inst_Sa_MtrVel         0           T_MtrVelBlendTbiY_UIs_u2p14[0]         0           T_MtrVelBlendTbiY_UIs_u2p14[1]         16384           k_GearRatio_UIs_f32         0.2           t_MtrVelBlendTbiX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTbiX_MtrRadpS_u12p4[1]         5           t_MtrVelBlendTbiX_MtrRadpS_u12p4[1]	MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7500
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]         7378           MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_MtrRadpS_M_f32         -15.40252668           MtrVel_MtrVelMtrP_MtrRadpS_M_f32         -227.1569001           MtrVel_ObgufSelect_Ort_M_u08         0           MtrVel_OsBufSelect_Ort_M_u08         3           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         86076           Rte_inst_sa_MtrVel         tgt_Rte_inst_Sa_MtrVel           T_MtrVelBlendTblY_Uls_u2p14[0]         0           T_MtrVelBlendTblY_Uls_u2p14[1]         16384           k_GearRatio_Uls_f32         0.2           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5	MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7435
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwRadpS_M_f32         -15 40252668           MtrVel_HwrRadpS_M_f32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         0           MtrVel_OsBufSelect_Cnt_M_u08         3           MtrVel_PrevMtrPosMerMtrPos2_Rad_M_f32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         896076           Rte_Inst_Sa_MtrVel         tgt_Rte_Inst_Sa_MtrVel           T_MtrVelBlendTblY_UIs_u2p14[0]         0           T_MtrVelBlendTblY_UIs_u2p14[1]         16384           k_GearRatio_UIs_f32         0.2           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5	MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7400
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         6504           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwRadpS_M_f32         -15.40252668           MtrVel_Hwrel_MtrRadpS_M_f32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         0           MtrVel_OldPosBuf_Cnt_M_u08         3           MtrVel_PrevMtrPosMercMtrPos2_Rad_M_f32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         896076           Rte_inst_Sa_MtrVel         tgt_Rte_Inst_Sa_MtrVel           T_MtrVelBlendTblY_UIs_u2p14[0]         0           T_MtrVelBlendTblY_UIs_u2p14[1]         16384           K_GearRatio_UIs_f32         0.2           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5           tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_sole.value         1		7378
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         6566           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         6132           MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwRadpS_M_f32         -15.40252668           MtrVel_HwRadpS_M_f32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         0           MtrVel_OsBufSelect_Cnt_M_u08         0           MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         896076           Rte_inst_Sa_MtrVel         tgt_Rte_Inst_Sa_MtrVel           T_MtrVelBlendTbty_Uls_u2p14[0]         0           T_MtrVelBlendTbty_Uls_u2p14[1]         16384           k_GearRatio_Uls_f32         0.2           t_MtrVelBlendTbtx_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTbtx_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTbtx_MtrRadpS_u12p4[1]         5           t_MtrVelBlendTbtx_AssasemblyPolarity_Cnt_s08.value         1		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       6132         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       6194         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       6256         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       6318         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       6380         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       6442         MtrVel_HwVel_HwRdpS_M_f32       -15.40252668         MtrVel_HtvVelMRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]         6194           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         6256           MtrVel3_TimeBuffer_uS_M_u16p0[3][5]         6318           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         6380           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         6442           MtrVel_HwRadpS_M_f32         -15.40252668           MtrVel_HwRedpS_M_f32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         0           MtrVel_OsBufSelect_Cnt_M_u08         3           MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         896076           Rte_inst_Sa_MtrVel         tgt_Rte_inst_Sa_MtrVel           T_MtrVelBlendTblY_Uls_u2p14[0]         0           T_MtrVelBlendTblY_Uls_u2p14[1]         16384           k_GearRatio_Uls_f32         0.2           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBendTblX_MtrRadpS_u12p4[1]         5           tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value         1		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       6256         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       6318         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       6380         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       6442         MtrVel_HwVel_HwRadpS_M_f32       -15.40252668         MtrVel_MtrVelMtRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsbufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosBechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBendTblX_MtrRadpS_u12p4[1]       5         t_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       6318         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       6380         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       6442         MtrVel_HwRadpS_M_f32       -15.40252668         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBend_TblX_MtrRadpS_u12p4[1]       5         t_MtrVelBend_TblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       6380         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       6442         MtrVel_HwRadpS_M_f32       -15.40252668         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBendTblX_MtrRadpS_u12p4[1]       5         ty_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       6442         MtrVel_HwVel_HwRadpS_M_f32       -15.40252668         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBendTblX_MtrRadpS_u12p4[1]       5         tg_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1	MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6318
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       6442         MtrVel_HwVel_HwRadpS_M_f32       -15.40252668         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         t_MtrVelBendTblX_MtrRadpS_u12p4[1]       5         tg_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1	MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6380
MtrVel_HwVel_HwRadpS_M_f32       -15.40252668         MtrVel_MtrVelMRF_MtrRadpS_M_f32       -227.1569001         MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_Uls_u2p14[0]       0         T_MtrVelBlendTblY_Uls_u2p14[1]       16384         k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tg_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		6442
MtrVel_MtrVelMRF_MtrRadpS_M_f32         -227.1569001           MtrVel_OldPosBuf_Cnt_M_u08         0           MtrVel_OsBufSelect_Cnt_M_u08         3           MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32         6           MtrVel_PrevMtrPosSampleTime2_uS_M_u32         896076           Rte_Inst_Sa_MtrVel         tgt_Rte_Inst_Sa_MtrVel           T_MtrVelBlendTblY_Uls_u2p14[0]         0           T_MtrVelBlendTblY_Uls_u2p14[1]         16384           k_GearRatio_Uls_f32         0.2           t_MtrVelBlendTblX_MtrRadpS_u12p4[0]         5           t_MtrVelBlendTblX_MtrRadpS_u12p4[1]         5           tg_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value         1		
MtrVel_OldPosBuf_Cnt_M_u08       0         MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_Uls_u2p14[0]       0         T_MtrVelBlendTblY_Uls_u2p14[1]       16384         k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel_OsBufSelect_Cnt_M_u08       3         MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32       6         MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_UIs_u2p14[0]       0         T_MtrVelBlendTblY_UIs_u2p14[1]       16384         k_GearRatio_UIs_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32       896076         Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_Uls_u2p14[0]       0         T_MtrVelBlendTblY_Uls_u2p14[1]       16384         k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
Rte_Inst_Sa_MtrVel       tgt_Rte_Inst_Sa_MtrVel         T_MtrVelBlendTblY_Uls_u2p14[0]       0         T_MtrVelBlendTblY_Uls_u2p14[1]       16384         k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
T_MtrVelBlendTblY_Uls_u2p14[0]       0         T_MtrVelBlendTblY_Uls_u2p14[1]       16384         k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1	MtrVel_PrevMtrPosSampleTime2_uS_M_u32	896076
T_MtrVelBlendTblY_Uls_u2p14[0]       0         T_MtrVelBlendTblY_Uls_u2p14[1]       16384         k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1	Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[1] 16384 k_GearRatio_Uls_f32 0.2 t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 5 t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 5 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 1		
k_GearRatio_Uls_f32       0.2         t_MtrVelBlendTblX_MtrRadpS_u12p4[0]       5         t_MtrVelBlendTblX_MtrRadpS_u12p4[1]       5         tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value       1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0] 5  t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 5  tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 1		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1] 5 tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 1		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value 1		
<u> </u>		
LINE AND	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_mtrvei_Per1_mecnmtrPos2Timestamp_USec_u32.value 897976	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	897976





Name	Input Value		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt	_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_	USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16	3	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_	f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_	<u>f</u> 32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwR	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRa	ndpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	29.8089962	29.80897903 ± 0.0625	~
MtrVel_MtrVelMRF_MtrRadpS_M_f32	149.044983	149.0448914 ± 0.0625	~
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	~
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	897976	897976	~
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	29.8089962	29.80897903 ± 0.009	~
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	~
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	~
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	29.8089962	29.80897903 ± 0.009	~
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value	149.044983	149.0448914 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	•

Test Step 3.3 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.12
MtrVel3_PosBuffer_Rad_M_f32[0][1]	1.13
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1.14
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1.15
MtrVel3_PosBuffer_Rad_M_f32[0][4]	1.16
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1.17
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1.18
MtrVel3_PosBuffer_Rad_M_f32[0][7]	1.19
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1.2
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1.21
MtrVel3_PosBuffer_Rad_M_f32[1][2]	1.22
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.23
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1.24
MtrVel3_PosBuffer_Rad_M_f32[1][5]	1.25
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1.26
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1.27
MtrVel3_PosBuffer_Rad_M_f32[2][0]	1.28
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1.29
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1.3
MtrVel3_PosBuffer_Rad_M_f32[2][3]	1.31
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1.32
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1.33
MtrVel3_PosBuffer_Rad_M_f32[2][6]	1.34
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1.35
MtrVel3_PosBuffer_Rad_M_f32[3][1]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1.37
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1.38
MtrVel3_PosBuffer_Rad_M_f32[3][4]	1.39
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1.4
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1.41
MtrVel3_PosBuffer_Rad_M_f32[3][7]	1.42
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	166

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	65268		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	65330		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65392		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65454		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65516		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	42		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	104		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	2042		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	2104		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2166		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1732		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1794		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1856		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1918		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1980		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]  MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7] MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	800		
MtrVel_HwRadpS_M_f32	-35.5619548		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-30.3019346 -890.9661543		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	1		
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	1200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
T_MtrVelBlendTblY_Uls_u2p14[0]	0		
T_MtrVelBlendTblY_Uls_u2p14[1]	16384		
k_GearRatio_Uls_f32	0.12		
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	2		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	2		
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	-1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	3000		
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	0		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cr	nt_s08	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp	_USec_u32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p1	6	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS	_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_MtrR	adpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_HwVel_HwRadpS_M_f32	42	42 ± 0.0625	-
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-555.555542	-555.555542 ± 0.0625	<b>→</b>
MtrVel_OldPosBuf_Cnt_M_u08	1	1	-
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>→</b>
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0	0 ± 0.0625	-
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	3000	3000	-
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	-
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	555.555542	555.555542 ± 0.0625	<b>✓</b>
	-555.555542	-555.555542 ± 0.0625	-
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-333.333342		
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	42	42 ± 0.009	•



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	•
RegressionFit	1	RegressionFit	1	•
MtrVelBlend	1	MtrVelBlend	1	<b>✓</b>
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte Call MtrVel Per1 CP1 CheckpointReached	1	Rte Call MtrVel Per1 CP1 CheckpointReached	1	<b>✓</b>

est Step 3.4 (Repeat Count = 1)		
ame	Input Value	
ltrVel3_OsBufPos_Cnt_M_u08[0]	0	
ltrVel3_OsBufPos_Cnt_M_u08[1]	2	
trVel3_OsBufPos_Cnt_M_u08[2]	4	
trVel3_OsBufPos_Cnt_M_u08[3]	7	
trVel3_PosBuffer_Rad_M_f32[0][0]	2.14	
trVel3_PosBuffer_Rad_M_f32[0][1]	2.15	
trVel3_PosBuffer_Rad_M_f32[0][2]	2.16	
trVel3_PosBuffer_Rad_M_f32[0][3]	2.17	
trVel3_PosBuffer_Rad_M_f32[0][4]	2.18	
trVel3_PosBuffer_Rad_M_f32[0][5]	2.19	
trVel3_PosBuffer_Rad_M_f32[0][6]	2.2	
rVel3_PosBuffer_Rad_M_f32[0][7]	2.21	
trVel3_PosBuffer_Rad_M_f32[1][0]	2.22	
trVel3_PosBuffer_Rad_M_f32[1][1]	2.23	
trVel3_PosBuffer_Rad_M_f32[1][2]	2.24	
trVel3_PosBuffer_Rad_M_f32[1][3]	2.25	
rVel3_PosBuffer_Rad_M_f32[1][4]	2.26	
rVel3_PosBuffer_Rad_M_f32[1][5]	2.27	
rVel3_PosBuffer_Rad_M_f32[1][6]	2.28	
trVel3_PosBuffer_Rad_M_f32[1][7]	2.29	
trVel3_PosBuffer_Rad_M_f32[2][0]	2.3	
trVel3_PosBuffer_Rad_M_f32[2][1]	2.31	
rVel3_PosBuffer_Rad_M_f32[2][2]	2.32	
rVel3_PosBuffer_Rad_M_f32[2][3]	2.33	
rVel3_PosBuffer_Rad_M_f32[2][4]	2.34	
rVel3_PosBuffer_Rad_M_f32[2][5]	2.35	
trVel3_PosBuffer_Rad_M_f32[2][6]	2.35	
trVel3_PosBuffer_Rad_M_f32[2][7]	2.37	
trVel3_PosBuffer_Rad_M_f32[3][0]	2.38	
trVel3_PosBuffer_Rad_M_f32[3][1]	2.39	
trVel3_PosBuffer_Rad_M_f32[3][2]	2.39	
trVel3_PosBuffer_Rad_M_f32[3][3]	2.4	
trVel3_PosBuffer_Rad_M_f32[3][4]	2.41	
trVel3_PosBuffer_Rad_M_f32[3][5]	2.42	
trVel3_PosBuffer_Rad_M_f32[3][6]	2.43	
rVel3_PosBuffer_Rad_M_f32[3][7]	2.44	
trVel3_TimeBuffer_uS_M_u16p0[0][0]	250	
rVel3_TimeBuffer_uS_M_u16p0[0][1]	65352	
rVel3_TimeBuffer_uS_M_u16p0[0][2]	65414	
rVel3 TimeBuffer uS M u16p0[0][3]	65476	
rVel3_TimeBuffer_uS_M_u16p0[0][4]	2	
rVel3_TimeBuffer_uS_M_u16p0[0][5]	64	
rVel3_TimeBuffer_uS_M_u16p0[0][6]	126	
rVel3_TimeBuffer_uS_M_u16p0[0][7]	188	
rVel3_TimeBuffer_uS_M_u16p0[1][0]	1626	
rVel3_TimeBuffer_uS_M_u16p0[1][1]	1688	
rVel3_TimeBuffer_uS_M_u16p0[1][2]	1750	
rVel3_TimeBuffer_uS_M_u16p0[1][3]	1316	
rVel3_TimeBuffer_uS_M_u16p0[1][4]	1378	
rVel3 TimeBuffer uS M u16p0[1][5]	1440	
rVel3_TimeBuffer_uS_M_u16p0[1][6]	1502	
rVel3_TimeBuffer_uS_M_u16p0[1][7]	1564	
rVel3_TimeBullet_u3_M_u16p0[1][7]	110	
rVel3_TimeBuller_u5_M_u16p0[2][0] rVel3_TimeBuffer_uS_M_u16p0[2][1]	220	
	330	
trVel3_TimeBuffer_uS_M_u16p0[2][2]	440	
rVel3_TimeBuffer_uS_M_u16p0[2][3]		
trVel3_TimeBuffer_uS_M_u16p0[2][4]	550	
trVel3_TimeBuffer_uS_M_u16p0[2][5]	660	

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Name	Input Value			
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	880			
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	110			
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	220			
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	330			
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	440			
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	550			
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	660			
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	770			
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	880			
MtrVel_HwVel_HwRadpS_M_f32	10.93737113			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-573.3094931			
MtrVel_OldPosBuf_Cnt_M_u08	0			
MtrVel_OsBufSelect_Cnt_M_u08	1			
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	3.6			
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	293048			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
T_MtrVelBlendTblY_Uls_u2p14[0]	0			
T_MtrVelBlendTblY_Uls_u2p14[1]	16384			
k_GearRatio_Uls_f32	0.13			
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	4	4		
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	4			
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1	1		
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	294848			
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	65535			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_	_Cnt_s08		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRad	dpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestar	mp_USec_u32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2_Rev_u0p16	tgt_MtrVel_Per1_MechMtrPos2_Rev_u0	)p16		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCRF_MtrRad	pS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMRF_MtrRad	pS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandwheelVel_H	lwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotorVelMRF_M	trRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel HwVel HwRadpS M f32	42	42 ± 0.0625	-	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350	1350 ± 0.0625	•	
MtrVel_OldPosBuf_Cnt_M_u08	1	1	•	
MtrVel_OsBufSelect_Cnt_M_u08	2	2	•	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	6.28308964	6.283089433 ± 0.0625	•	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	294848	294848	•	
tgt MtrVel Per1 HandwheelVel HwRadpS f32.value	42	42 ± 0.009	-	
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tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value		42	42 ± 0.009		<b>✓</b>
tgt_MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32.value		1350	1350 ± 0.0625		•
Test Step Call Trace					<b>✓</b>
Actual Function	Count	Expected Function		Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_Check	pointReached	1	~
CalcCoarseVel	1	CalcCoarseVel		1	~
RegressionFit	1	RegressionFit		1	~
MtrVelBlend	1	MtrVelBlend		1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt		1	~

Rte\_Call\_MtrVel\_Per1\_CP1\_CheckpointReached

1350 ± 0.0625

1350 ± 0.0625

1350

1350

Test Step 3.5 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.3	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.51	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	2.64	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2.77	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.9	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3.03	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	3.16	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.29	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.42	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3.55	

tgt\_MtrVel\_Per1\_MotorVelCRF\_MtrRadpS\_f32.value

tgt\_MtrVel\_Per1\_MotorVelMRF\_MtrRadpS\_f32.value

Rte\_Call\_MtrVel\_Per1\_CP1\_CheckpointReached





Name	Input Value
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3.68
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3.81
MtrVel3_PosBuffer_Rad_M_f32[1][4]	3.94
MtrVel3_PosBuffer_Rad_M_f32[1][5]	4.07
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.2
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.33
MtrVel3_PosBuffer_Rad_M_f32[2][0]	4.46
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4.59
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4.72
MtrVel3_PosBuffer_Rad_M_f32[2][3]	4.85
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4.98
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5.11
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5.24 5.37
MtrVel3_PosBuffer_Rad_M_f32[2][7] MtrVel3_PosBuffer_Rad_M_f32[3][0]	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5.63
MtrVel3_PosBuffer_Rad_M_f32[3][2]	5.76
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5.89
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.02
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3.3
MtrVel3_PosBuffer_Rad_M_f32[3][6]	3.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3.5
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9770
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9832
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9398
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9460
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9522
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9584
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9646
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9708
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	20450
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20401
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	20350
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	20306
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	20250 20200
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]  MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	20156
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	20107
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	35035
MtrVel3 TimeBuffer uS M u16p0[2][1]	35000
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	34070
MtrVel3 TimeBuffer uS M u16p0[2][3]	34008
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	33900
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	33845
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	33000
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	32906
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	8928
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	8990
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9052
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9114
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9176
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	8742
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	8804
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	8866
MtrVel_HwVel_HwRadpS_M_f32	-5.642567992
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1302.259389
MtrVel_OldPosBuf_Cnt_M_u08	3
MtrVel_OsBufSelect_Cnt_M_u08  AttrVel_DrovMtrDecMachMtrDec2_Red_M_f22	0
MtrVel_PrevMtrPosSempleTime3_us_M_u32	3.5
MtrVel_PrevMtrPosSampleTime2_uS_M_u32  Pte_Inst_Sa_MtrVel	85754  tot Pte Inst Sa Mtr\/el
Rte_Inst_Sa_MtrVel T_MtrVelBlendTblY_Uls_u2p14[0]	tgt_Rte_Inst_Sa_MtrVel
T_MtrVelBlendTblY_Uls_u2p14[1]	16384
k_GearRatio_UIs_f32	0.25
t_MtrVelBlendTblX_MtrRadpS_u12p4[0]	6
t_MtrVelBlendTblX_MtrRadpS_u12p4[1]	6
tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08.value	1
tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32.value	87654
tgt_MtrVel_Per1_MechMtrPos2_Rev_u0p16.value	6554
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08	tgt_MtrVel_Per1_AsstAssemblyPolarity_Cnt_s08
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_HandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MechMtrPos2Timestamp_USec_u32	tgt_MtrVel_Per1_MechMtrPos2Timestamp_USec_u32





Name	Input Value			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelCRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelCF	tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_MotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_MotorVelMF	RF_MtrRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32	tgt_MtrVel_Per1_SysCHandv	wheelVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per1_SysCMotorVelMRF_MtrRadpS_f32	tgt_MtrVel_Per1_SysCMotor	VelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_HwVel_HwRadpS_M_f32	-42	-42 ± 0.0625	~	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350	-1350 ± 0.0625	<b>✓</b>	
MtrVel_OldPosBuf_Cnt_M_u08	0	0	<b>✓</b>	
MtrVel_OsBufSelect_Cnt_M_u08	1	1	✓	
MtrVel_PrevMtrPosMechMtrPos2_Rad_M_f32	0.628356874	0.62835688 ± 0.0625	✓	
MtrVel_PrevMtrPosSampleTime2_uS_M_u32	87654	87654	✓	
tgt_MtrVel_Per1_HandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓	
tgt_MtrVel_Per1_MotorVelCRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	✓	
tgt_MtrVel_Per1_MotorVelMRF_MtrRadpS_f32.value	-1350	-1350 ± 0.0625	<b>✓</b>	
tgt_MtrVel_Per1_SysCHandwheelVel_HwRadpS_f32.value	-42	-42 ± 0.009	✓	
tqt MtrVel Per1 SysCMotorVelMRF MtrRadpS f32.value	-1350	-1350 ± 0.0625	<b>✓</b>	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP0_CheckpointReached	1	~
CalcCoarseVel	1	CalcCoarseVel	1	~
RegressionFit	1	RegressionFit	1	~
MtrVelBlend	1	MtrVelBlend	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per1_CP1_CheckpointReached	1	<b>✓</b>

CalcCoarseVel

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 Project
 MtrVel\_Digi

 Module
 MtrVel\_Digi

 Test Object
 CalcCoarseVel

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description/Specification		
Name	Text	
Module 'MtrVel_Digi'	Name of Tester:Sunita Kore Code File(s) Under Test:Sa_MtrVel.c Code File(s) Under Test:Sa_MtrVel.c Code File(s) Version:5 Module Design Document:MotorVelocity_MDD.doc Module Design Document:MotorVelocity_MDD.doc Module Design Document:MotorVelocity_MDD.doc Module Design Document:MotorVelocity_MDD.doc Module Design Document Version:3 Unit Test Plan Version:3 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):1394 Total RAM Used (Bytes):42 Total CALS Used (Bytes):54 Special Test Requirements: Test Date:12/26/2014 Comments: "Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.  Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSampleTime2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.  Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.  Note4: In function ""RegressionFit"", the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"' has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements.  Note5: ""CBD_Sandbox_dbg.map" is embedded for reference."	

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	

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Attributes	
Name	Value
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



# Test Case 1: Boundary Check Specification Performance Metrics: (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 31 Cycles TS 1.2 31 Cycles TS 1.2 31 Cycles TS 1.4 31 Cycles TS 1.4 31 Cycles TS 1.5 31 Cycles TS 1.6 31 Cycles TS 1.6 31 Cycles TS 1.7 31 Cycles TS 1.8 31 Cycles TS 1.8 31 Cycles TS 1.8 31 Cycles TS 1.1 31 Cycles TS 1.1 31 Cycles TS 1.1 31 Cycles TS 1.2 31 Cycles TS 1.3 1 Cycles TS 1.3 1 Cycles TS 1.1 31 Cycles TS 1.1 31 Cycles TS 1.2 31 Cycles TS 1.1 31 Cycles TS 1.2 31 Cycles TS 1.1 31 Cycles TS 1.2 31 Cycles TS 1.3 1 Cycles TS 1.3 1 Cycles TS 1.5 1 Cycles TS 1.6 31 Cycles TS 1.7 DeltaPos Rad T [32 = min TS 1.2 DeltaPos Rad T [32 = zero TS 1.6 DeltaPos Rad T [32 = zero TS 1.6 DeltaPos Rad T [32 = zero TS 1.7 DeltaPos Rad T [32 = neg TS 1.8 DeltaTime\_uS T [32 = min TS 1.9 DeltaTime\_uS T [32 = min TS 1.9 DeltaTime\_uS T [32 = min TS 1.9 DeltaTime\_uS T [32 = min

Test Step 1.1 (Repeat Count = 1)			~
Name	Input Value		
DeltaPos_Rad_T_f32	-6.283185307		
DeltaTime_uS_T_f32	1800		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-3490.65869	-3490.658504 ± 0.0625	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DeltaPos_Rad_T_f32	6.283185307		
DeltaTime_uS_T_f32	2200		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	2855.99341	2855.993321 ± 0.0625	~

Test Step 1.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DeltaPos_Rad_T_f32	-6.283185307		
DeltaTime_uS_T_f32	1900		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-3306.9397	-3306.939635 ± 0.0625	~

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	6.283185307		
DeltaTime_uS_T_f32	2100.45		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	2991.35205	2991.351999 ± 0.0625	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	0		
DeltaTime_uS_T_f32	1937		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	0	0 ± 0.0625	~

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CalcCoarseVel

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	2.2		
DeltaTime_uS_T_f32	2038		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	1079.48975	1079.489696 ± 0.0625	~

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	-1.6		
DeltaTime_uS_T_f32	2047.56		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-781.417847	-781.4178827 ± 0.0625	~

Test Step 1.8 (Repeat Count = 1)			~
Name	Input Value		
DeltaPos_Rad_T_f32	2.12		
DeltaTime_uS_T_f32	1800		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	1177.77783	1177.777778 ± 0.0625	~

Test Step 1.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
DeltaPos_Rad_T_f32	4.12		
DeltaTime_uS_T_f32	2200		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	1872.72729	1872.727273 ± 0.0625	•

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
DeltaPos_Rad_T_f32	-1.9		
DeltaTime_uS_T_f32	2000.5		
Name	Actual Value	Expected Value	Result
CalcCoarseVel()	-949.762573	-949.762573 ± 0.0625	~

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RegressionFit

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	RegressionFit

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MftVel_Dig\include -I\$(PROJECTROOT)\MftVel_Dig\include -I\$(Compiler Install Path\)include -I\$(PROJECTROOT)\MftVel_Dig\include -I\$(Compiler Install Path\)include -I\$(Compiler Inst

Comments/Description/Spe	ecification
Name	Text





Module 'Mtr/Vel\_Digi'

Name of Tester:Sunita Kore
Code File(s) Version:5
Module Design Document:Motor/Velocity\_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:4
Unit Test Plan Version:4
Unit Test Plan Version:5
Unit Test Plan Version:4
Unit Test Plan Version:4
Unit Test Plan Version:5
Unit Test Plan Version:4
Unit Test Plan Version:5
Unit Test Plan Ver

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 1288 Cycles TS 1.2 1365 Cycles

VECTOR DESCRIPTION: Description

TS 1.1 "Shortest Execution Path:

(PosDiff\_Rad\_T\_f32 > D\_PI\_ULS\_F32) = False;

(PosDiff\_Rad\_T\_f32 <= -D\_PI\_ULS\_F32) = FALSE

(SumDeltaSqT\_uSsq\_T\_f32 > FLT\_EPSILON) = TRUE"

TS 1.2 "Longest Execution Path:

(PosDiff\_Rad\_T\_f32 > D\_PI\_ULS\_F32) = True

(SumDeltaSqT\_uSsq\_T\_f32 > FLT\_EPSILON) = TRUE"

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3 PosBuffer Rad M f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
	6
MtrVel3_PosBuffer_Rad_M_f32[1][3] MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2
MtrVel3_PosBuffer_Rad_M_f32[1][5]	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200
MtrVel3 TimeBuffer uS M u16p0[1][3]	500
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900
MtrVel3 TimeBuffer uS M u16p0[2][0]	100
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]  MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200
MtrVel3 TimeBuffer uS M u16p0[2][2]	300
MtrVel3 TimeBuffer uS M u16p0[2][3]	400
INIU VEID_TIITIEDUTIEL_UO_IVI_U TOPO[Z][O]	700

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel_OsBufSelect_Cnt_M_u08	1	1	~
RegressionFit()	-833.333313	-833.3333333 ± 0.0625	~

Test Step 1.2 (Repeat Count = 1)	🗸
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2
MtrVel3 OsBufPos Cnt M u08[3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3 PosBuffer Rad M f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
	4
MtrVel3_PosBuffer_Rad_M_f32[2][5]  MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][6] MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60
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#### RegressionFit

Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	~
RegressionFit()	-600.838257	-600.8382387 ± 0.0625	-

#### **Test Case 2**

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 2.1 1288 Cycles TS 2.2 1307 Cycles TS 2.3 1365 Cycles TS 2.4 1356 Cycles TS 2.5 1290 Cycles

Description

VECTOR DESCRIPTION:

TS 2.1"(PosDiff\_Rad\_T\_f32 > D\_PI\_ULS\_F32) = TRUE; (PosDiff\_Rad\_T\_f32 <= -D\_PI\_ULS\_F32) = FALSE" TS 2.2"(PosDiff\_Rad\_T\_f32 > D\_PI\_ULS\_F32) = FALSE; (PosDiff\_Rad\_T\_f32 <= -D\_PI\_ULS\_F32) = TRUE;" TS 2.3(SumDeltaSqT\_uSsq\_T\_f32 > FLT\_EPSILON) = TRUE TS 2.4(PosDiff\_Rad\_T\_f32 <= -D\_PI\_ULS\_F32) = TRUE TS 2.5(SumDeltaSqT\_uSsq\_T\_f32 > FLT\_EPSILON) = FALSE

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3 PosBuffer Rad M f32[3][4]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	0	0	~
MtrVel OsBufSelect Cnt M u08	1	1	_
RegressionFit()	-833.333313	-833.333333 ± 0.0625	

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6		
	5		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	35		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]  MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	9390		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	9460		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	9285		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	
RegressionFit()	631.181824	631.1818724 ± 0.0625	
regression it()	031.101024	031.1010724 ± 0.0023	

Test Step 2.3 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	1	
MtrVel3_OsBufPos_Cnt_M_u08[2]	2	
MtrVel3_OsBufPos_Cnt_M_u08[3]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5	

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		1,3	
Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9745		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9760		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	9775		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	9805		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10775		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3 TimeBuffer uS M u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3 TimeBuffer uS M u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0 2		
MtrVel_OsBufSelect_Cnt_M_u08			1_
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	2	2	
MtrVel_OsBufSelect_Cnt_M_u08	3	3	•
RegressionFit()	-600.838257	-600.8382387 ± 0.0625	

Test Step 2.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	

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RegressionFit		( &	ACITAL
Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3 PosBuffer Rad M f32[3][4]	5.5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.4		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3 TimeBuffer uS M u16p0[2][5]	600		
MtrVel3 TimeBuffer uS M u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3 TimeBuffer uS M u16p0[3][4]	1300		
MtrVel3 TimeBuffer uS M u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	0	0	Resul
MtrVel_OsBufSelect_Cnt_M_u08	1	1	
RegressionFit()	475.663788	475.6636057 ± 0.0625	
ivediceonitel()	4/0.003/00	473.0030037 ± 0.0025	

Test Step 2.5 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	0	
MtrVel3 OsBufPos Cnt M u08[2]	0	

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			10.10
Name	Input Value		
MtrVel3_OsBufPos_Cnt_M_u08[3]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	0		
MtrVel3 TimeBuffer uS M u16p0[0][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	0		
MtrVel3 TimeBuffer uS M u16p0[1][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	0		
MtrVel3 TimeBuffer uS M u16p0[2][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	0		
MtrVel3 TimeBuffer uS M u16p0[2][4]	0		
MtrVel3 TimeBuffer uS M u16p0[2][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	0		
MtrVel3 TimeBuffer uS M u16p0[2][7]	0		
	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]  MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]  MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	0		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
		2	
MtrVel_OsBufSelect_Cnt_M_u08 RegressionFit()	2	0 ± 0.0625	<b>~</b>



#### **Test Case 3**

#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 3.1 1281 Cycles
TS 3.2 1266 Cycles
TS 3.2 1266 Cycles
TS 3.3 1334 Cycles
TS 3.4 1432 Cycles
TS 3.5 1310 Cycles
TS 3.6 1422 Cycles
TS 3.6 1422 Cycles
TS 3.8 1316 Cycles
TS 3.9 1315 Cycles
TS 3.10 1354 Cycles
TS 3.11 1310 Cycles
TS 3.12 1389 Cycles
TS 3.14 1301 Cycles
TS 3.14 1301 Cycles
TS 3.15 1303 Cycles
TS 3.15 1303 Cycles
TS 3.16 1288 Cycles

TS 3.16 1288 Cycles TS 3.17 1288 Cycles

#### Description

#### VECTOR DESCRIPTION:

TS 3.1 "all min

(Cross buffer timestamp = 250)"

TS 3.2"all max

(Cross buffer timestamp = 4000)"
TS 3.3"MtrVel\_OsBufSelect\_Cnt\_M\_u08 = min

(Cross buffer timestamp = 600)"
TS 3.4"MtrVel\_OsBufSelect\_Cnt\_M\_u08 = max

(Cross buffer timestamp = 340)"

TS 3.5"MtrVel\_OsBufSelect\_Cnt\_M\_u08 = mid (Cross buffer timestamp = 1000)"

TS 3.6"MtrVel3\_OsBufPos\_Cnt\_M\_u08[4] = min

(Cross buffer timestamp = 800)"
TS 3.7"MtrVel3\_OsBufPos\_Cnt\_M\_u08[4] = max
(Cross buffer timestamp = 450)"

(Cross buffer timestamp = 450)\*
TS 3.8"MtrVel3\_OsBufPos\_Cnt\_M\_u08[4] = mid
(Cross buffer timestamp = 500)\*
TS 3.9"MtrVel\_OldPosBuf\_Cnt\_M\_u08 = min
(Cross buffer timestamp = 950)\*
TS 3.10"MtrVel\_OldPosBuf\_Cnt\_M\_u08 = max
(Cross buffer timestamp = 420)\*

TS 3.11"MtrVel\_OldPosBuf\_Cnt\_M\_u08 = mid

(Cross buffer timestamp = 380)"
TS 3.12"MtrVel3\_TimeBuffer\_uS\_M\_u16p0[4][8] = min
(Cross buffer timestamp = 250)"

TS 3.13"MtrVel3\_TimeBuffer\_uS\_M\_u16p0[4][8] = max (Cross buffer timestamp = 4000)"

TS 3.14"MtrVel3\_TimeBuffer\_uS\_M\_u16p0[4][8] = mid

(Cross buffer timestamp = 600)"
TS 3.15"MtrVel3\_PosBuffer\_Rad\_M\_f32[4][8] = min (Cross buffer timestamp = 340)"

Test Step 3.1 (Repeat Count = 1)

TS 3.16"MtrVel3\_PosBuffer\_Rad\_M\_f32[4][8] = max (Cross buffer timestamp = 1000)"

TS 3.17"MtrVel3\_PosBuffer\_Rad\_M\_f32[4][8] = mid (Cross buffer timestamp = 950)"

Input Value
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0
0

0

0

0

MtrVel3\_PosBuffer\_Rad\_M\_f32[2][2]

MtrVel3\_PosBuffer\_Rad\_M\_f32[2][3]

MtrVel3\_PosBuffer\_Rad\_M\_f32[2][4]

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490		
	750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	680		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	690		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
RegressionFit()	0	0 ± 0.0625	~

Test Step 3.2 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	7	
MtrVel3_OsBufPos_Cnt_M_u08[1]	7	
MtrVel3_OsBufPos_Cnt_M_u08[2]	7	
MtrVel3_OsBufPos_Cnt_M_u08[3]	7	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433	

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	12000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	11875		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	11750		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	11625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	11500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	11375		
MtrVel3 TimeBuffer uS M u16p0[0][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	60000		
MtrVel3 TimeBuffer uS M u16p0[1][1]	59875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	59750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	59625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	59500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	59375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	59250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	59125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	2025		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	2150		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	2275		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	2400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	2525		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	2650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	2775		
MtrVel3 TimeBuffer uS M u16p0[2][7]	2900		
	6025		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0] MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1] MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	6275		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400 6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		1_
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	•
RegressionFit()	0	0 ± 0.0625	•

Test Step 3.3 (Repeat Count = 1)		✓
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	3	
MtrVel3_OsBufPos_Cnt_M_u08[3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6	

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	5.5		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	2.4		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	1200		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	1300		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	1400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1600		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	0		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	0	0	-
MtrVel_OsBufSelect_Cnt_M_u08	1	1	•
		475.6636057 ± 0.0625	

Test Step 3.4 (Repeat Count = 1)	✓
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
MtrVel3_OsBufPos_Cnt_M_u08[1]	5
MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[3]	7
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5.98
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4

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Regressioner		<u>'</u>	OIL CTUOID
Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2.7		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5.6		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1.6		
MtrVel3 PosBuffer Rad M f32[1][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4.6		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5.099999		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3 PosBuffer Rad M f32[3][0]	6.28000021		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5.4		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	35		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	65501		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	65466		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	65431		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	65396		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	65361		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	65326		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	65291		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	65256		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	65221		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	65186		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	65151		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	65116		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	65081		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	9320		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	9355		
MtrVel3_TimeBuffer_uS_M_uTop0[2][1]  MtrVel3_TimeBuffer_uS_M_uTop0[2][2]	9390		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2] MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	9425		
	9425		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]  MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9495		
	9495		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	9530 9285		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]			
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9695		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9730		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9765		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9870		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	•
RegressionFit()	-1097.93042	-1097.93042	

Test Step 3.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4

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RegressionFit		(MAC)	COL
Name	Input Value		
MtrVel3 OsBufPos Cnt M u08[1]	5		
MtrVel3_OsBufPos_Cnt_M_u08[2]	6		
MtrVel3_OsBufPos_Cnt_M_u08[3]	7		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][0] MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]  MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64520 64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3 TimeBuffer uS M u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer uS M u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3 TimeBuffer uS M u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	~
RegressionFit()	-657.894653	-657.8947368 ± 0.0625	•



Marvis Columbia: CM   MidRN	Test Step 3.6 (Repeat Count = 1)	V
Invest   Designer		Input Value
Moved Scales Co. M. (2017)  Moved Scales Co. M. (2018)  Moved Scales Co. M. (2019)  Mo		•
Ministry   Designation   Des		
Marvell, Postularie, Ped M. P. (1921)  Marvell, Postularie, Ped M.		0
Marvels, Peschiller, Read M. (2007)  Marvels, Peschiller, Read M. (2017)  Marvels, Pe	MtrVel3_OsBufPos_Cnt_M_u08[3]	0
Marves   Postular   Past   M. 1020 19	MtrVel3_PosBuffer_Rad_M_f32[0][0]	3.6
Marves   Pastuline   Raid   M. 1920 19   1.5	MtrVel3_PosBuffer_Rad_M_f32[0][1]	
Marvell Peabline Real M. 120[16]  Marvell Peabline Real M. 120[16]  Marvell Peabline Real M. 120[16]  Marvell Peabline Real M. 120[17]  5   Section Real M. 120[17]  6   Section Real M. 120[17]  7   Section Real M. 120[17]  8   Section Real M. 120[17]  9   Section Real M. 120[17]  9   Section Real M. 120[17]  10   Section Real M. 120[17]  11   Section Real M. 120[17]  12   Section Real M. 120[17]  13   Section Real M. 120[17]  14   Section Real M. 120[17]  15   Section Real M. 120[17]  16   Section Real M. 120[17]  17   Section Real M. 120[17]  18   Section Real M. 120[17]  19   Section Real M. 120[17]  10   Section Real M. 120[17]  10   Section Real M. 120[17]  11   Section Real M. 120[17]  12   Section Real M. 120[17]  13   Section Real M. 120[17]  14   Section Real M. 120[17]  15   Section Real M. 120[17]  16   Section Real M. 120[17]  17   Section Real M. 120[17]  18   Section Real M. 120[17]  19   Section Real M. 120[17]  10   Section Real M. 120[17]  10   Section Real M. 120[17]  11   Section Real M. 120[17]  12   Section Real M. 120[17]  13   Section Real M. 120[17]  14   Section Real M. 120[17]  15   Section Real M. 120[17]  16   Section Real M. 120[17]  17   Section Real M. 120[17]  18   Section Rea		
Marcias   Pendulum Fand   M. 1920 85   0   1		
MoVeS   Pedalifier Rad   M. 1920 17   0.5		
Mirves   Designation   Rad   M. (2011)		
Mirves   Postularis   Rad   M. (2011)		
Mirviss   Postularis Rad, M. 2021 15   0 2   0 2   0 2   0 2   0 2   0 3   0		
MANYAS   Posturer Ray   M. 1921 19   3   1   1   1   1   1   1   1   1   1		6
Merical Possibler Rad M. 52(1)	MtrVel3_PosBuffer_Rad_M_f32[1][2]	0.2
Minyres   Possither Rad   M. 1921 19   5	MtrVel3_PosBuffer_Rad_M_f32[1][3]	2
Marvis2  Posturis Rad M. 7821191   S		
MinVeS   Position   Red   M. 120 10		
Mirvis Posibiliter Rad M. 1920/19  Mirvis Prosibiliter Rad M. 19		
Name		
MAY-MS_PosBuffer_Rad_M_R22[2]   37		
Minvest		
MINVAS POSBuffer, Rad. M. 1822[15] 0 MINVAS POSBuffer, Rad. M. 1822[15] 1 MINVAS POSBuffer, Rad. M. 1822[15] 2 MINVAS POSBuffer, Rad. M. 1822[17] 2 MINVAS POSBuffer, Rad. M. 1822[17] 3 MINVAS POSBuffer, Rad. M. 1823[17] 0 MINVAS POSBuffer, Rad. M. 1823[17] 0 MINVAS POSBuffer, Rad. M. 1823[17] 1 MINVAS POSBuffer, Rad. M. 1823[18] 1 MINVAS POSBuffer, Rad. M. 1823[17] 1 MINVAS POSBuffer, M. M. 1823[17] 1 MINVAS POSBuf		
MrVv83_PosBuffer_Rad_M_S22[6]   1		
MirVel3_PosBuffer_Rad_M_S20[0]   3   1   1   1   1   1   1   1   1   1	MtrVel3_PosBuffer_Rad_M_f32[2][5]	0
Mrtv812 PosBuffer, Rad M. 152(3)(1)   0   0   0   0   0   0   0   0   0	MtrVel3_PosBuffer_Rad_M_f32[2][6]	1
MitVes1_PosBuffer_Rad_M_52(3)[1] 5 MitVes1_PosBuffer_Rad_M_52(3)[2] 5 MitVes1_PosBuffer_Rad_M_52(3)[3] 6 MitVes1_PosBuffer_Rad_M_52(3)[3] 1 MitVes1_PosBuffer_Rad_M_52(3)[3] 1 MitVes1_PosBuffer_Rad_M_52(3)[6] 2,7 MitVes1_PosBuffer_Rad_M_52(3)[6] 2,7 MitVes1_PosBuffer_Rad_M_52(3)[6] 3 MitVes1_PosBuffer_Rad_M_52(3)[6] 3 MitVes1_PosBuffer_Rad_M_52(3)[7] 0 MitVes1_PosBuffer_Rad_M_52(3)[7] 0 MitVes1_PosBuffer_Rad_M_52(3)[7] 0 MitVes1_PosBuffer_Rad_M_52(3)[7] 0 MitVes1_TimeBuffer_US_M_U16p(0)[7] 200 MitVes1_TimeBuffer_US_M_U16p(0)[7] 200 MitVes1_TimeBuffer_US_M_U16p(0)[7] 375 MitVes1_TimeBuffer_US_M_U16p(0)[7] 31600 MitVes1_TimeBuffer_US_M_U16p(0)[7] 3175 MitVes1_TimeBuffer_US_M_U16p(0)[7]	MtrVel3_PosBuffer_Rad_M_f32[2][7]	
MrtVel3_PosBuffer_Rad_M_[52[3][2]         5           MrtVel3_PosBuffer_Rad_M_[52[3][4]         1           MrtVel3_PosBuffer_Rad_M_[52[3][4]         1           MrtVel3_PosBuffer_Rad_M_[52[3][5]         2.7           MrtVel3_PosBuffer_Rad_M_[52[3][7]         0           MrtVel3_PosBuffer_Rad_M_[52[3][7]         0           MrtVel3_TimeBuffer_u.S.M_u16p0[0][1]         800           MrtVel3_TimeBuffer_u.S.M_u16p0[0][1]         205           MrtVel3_TimeBuffer_u.S.M_u16p0[0][2]         290           MrtVel3_TimeBuffer_u.S.M_u16p0[0][3]         375           MrtVel3_TimeBuffer_u.S.M_u16p0[0][3]         375           MrtVel3_TimeBuffer_u.S.M_u16p0[0][3]         545           MrtVel3_TimeBuffer_u.S.M_u16p0[0][3]         630           MrtVel3_TimeBuffer_u.S.M_u16p0[0][7]         715           MrtVel3_TimeBuffer_u.S.M_u16p0[1][7]         1000           MrtVel3_TimeBuffer_u.S.M_u16p0[1][7]         1000           MrtVel3_TimeBuffer_u.S.M_u16p0[1][7]         1175           MrtVel3_TimeBuffer_u.S.M_u16p0[1][7]         1430           MrtVel3_TimeBuffer_u.S.M_u16p0[1][7]         1436           MrtVel3_TimeBuffer_u.S.M_u16p0[1][7]         1515           MrtVel3_TimeBuffer_u.S.M_u16p0[2][7]         6000           MrtVel3_TimeBuffer_u.S.M_u16p0[2][7]         6000 </td <td></td> <td></td>		
MtV483_PosBuffer_Rad_M_152(3)[3] 6 MtV483_PosBuffer_Rad_M_152(3)[5] 2.7 MtV481_PosBuffer_Rad_M_152(3)[5] 2.7 MtV481_PosBuffer_Rad_M_152(3)[5] 3 MtV481_PosBuffer_Rad_M_152(3)[5] 3 MtV481_PosBuffer_Rad_M_152(3)[7] 0 MtV483_TimeBuffer_uS_M_15(9)(0)[0] 800 MtV483_TimeBuffer_uS_M_15(9)(0)[1] 205 MtV481_TimeBuffer_uS_M_15(9)(0)[3] 375 MtV481_TimeBuffer_uS_M_15(9)(0)[3] 375 MtV481_TimeBuffer_uS_M_15(9)(0)[3] 375 MtV481_TimeBuffer_uS_M_15(9)(0)[3] 375 MtV481_TimeBuffer_uS_M_15(9)(0)[5] 545 MtV481_TimeBuffer_uS_M_15(9)(0)[5] 545 MtV481_TimeBuffer_uS_M_15(9)(0)[7] 715 MtV481_TimeBuffer_uS_M_15(9)(0)[7] 715 MtV481_TimeBuffer_uS_M_15(9)(1)[0] 1000 Mt		
Mitrives   PosBuffer   Rad   M.   123 3 4    1   27   3   3   3   3   3   3   3   3   3		
Mtrvel3_PosBuffer_Rad_M_[32]3[5]   2.7		
MttVel3_PosBuffer_Rad_M_[32]3[6]         3           MttVel3_TresBuffer_uS_M_u16p0[0]0         800           MttVel3_TresBuffer_uS_M_u16p0[0]1         205           MttVel3_TresBuffer_uS_M_u16p0[0]2         290           MttVel3_TresBuffer_uS_M_u16p0[0]3         375           MttVel3_TresBuffer_uS_M_u16p0[0]6         460           MttVel3_TresBuffer_uS_M_u16p0[0]6         630           MttVel3_TresBuffer_uS_M_u16p0[0]7         715           MttVel3_TresBuffer_uS_M_u16p0[0]7         715           MttVel3_TresBuffer_uS_M_u16p0[1]1         1005           MttVel3_TresBuffer_uS_M_u16p0[1]2         1090           MttVel3_TresBuffer_uS_M_u16p0[1]3         1175           MttVel3_TresBuffer_uS_M_u16p0[1]6         1345           MttVel3_TresBuffer_uS_M_u16p0[1]7         1400           MttVel3_TresBuffer_uS_M_u16p0[1]8         1175           MttVel3_TresBuffer_uS_M_u16p0[1]9         1200           MttVel3_TresBuffer_uS_M_u16p0[1]9         1345           MttVel3_TresBuffer_uS_M_u16p0[1]17         1515           MttVel3_TresBuffer_uS_M_u16p0[1]17         1516           MttVel3_TresBuffer_uS_M_u16p0[2]19         6000           MttVel3_TresBuffer_uS_M_u16p0[2]19         5910           MttVel3_TresBuffer_uS_M_u16p0[2]19         5880           MttVel		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]         800           MtrVel3_TimeBuffer_uS_M_u16p0[0][1]         205           MtrVel3_TimeBuffer_uS_M_u16p0[0][3]         375           MtrVel3_TimeBuffer_uS_M_u16p0[0][4]         460           MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         545           MtrVel3_TimeBuffer_uS_M_u16p0[0][6]         630           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1000           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1000           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1000           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1345           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1516           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1518           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         5510           MtrVel3_TimeBuffer_uS_M_u16p0[2][1]         6000           MtrVel3_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][3]         5880           MtrVel3_TimeBuffer_uS_M_u16p0[2][4]         591		3
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]         205           MtrVel3_TimeBuffer_uS_M_u16p0[0][2]         290           MtrVel3_TimeBuffer_uS_M_u16p0[0][4]         460           MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         545           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][5]         1345           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][8]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][7]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][8]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][8]         5940           MtrVel3_TimeBuffer_uS_M_u16p0[2][8]         5980           MtrVel3_TimeBuffer_uS_M_u16p0[2][8]         5880           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5790           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         57	MtrVel3_PosBuffer_Rad_M_f32[3][7]	0
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]         290           MtrVel3_TimeBuffer_uS_M_u16p0[0][3]         375           MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         460           MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         545           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1005           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1009           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][5]         1345           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][1]         6000           MtrVel3_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5700           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         5730           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         56	MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	800
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]         375           MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         545           MtrVel3_TimeBuffer_uS_M_u16p0[0][6]         630           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1260           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1345           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][4]         5940           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5700           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         5730           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]         460           MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         545           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1005           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1260           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][1]         6000           MtrVel3_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][3]         5940           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][7]         5820           MtrVel3_TimeBuffer_uS_M_u16p0[2][7]         5820           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5760           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         5730           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         5670           MtrVel3_TimeBuffer_uS_M_u16p0[3][6] <td< td=""><td></td><td></td></td<>		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]         545           MtrVel3_TimeBuffer_uS_M_u16p0[0][7]         715           MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1005           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][4]         1260           MtrVel3_TimeBuffer_uS_M_u16p0[1][5]         1345           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVel3_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][1]         6000           MtrVel3_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][3]         5940           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         5790           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5760           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5760           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5760           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         5670           MtrVel3_TimeBuffer_uS_M_u16p0[3][6] <t< td=""><td></td><td></td></t<>		
MtrVels_TimeBuffer_uS_M_u16p0[0]6]         630           MtrVels_TimeBuffer_uS_M_u16p0[0]7]         715           MtrVels_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVels_TimeBuffer_uS_M_u16p0[1][1]         1005           MtrVels_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVels_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVels_TimeBuffer_uS_M_u16p0[1][4]         1260           MtrVels_TimeBuffer_uS_M_u16p0[1][5]         1345           MtrVels_TimeBuffer_uS_M_u16p0[1][7]         1515           MtrVels_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVels_TimeBuffer_uS_M_u16p0[2][1]         6000           MtrVels_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVels_TimeBuffer_uS_M_u16p0[2][3]         5940           MtrVels_TimeBuffer_uS_M_u16p0[2][6]         5880           MtrVels_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVels_TimeBuffer_uS_M_u16p0[3][0]         5790           MtrVels_TimeBuffer_uS_M_u16p0[3][1]         5760           MtrVels_TimeBuffer_uS_M_u16p0[3][2]         5730           MtrVels_TimeBuffer_uS_M_u16p0[3][4]         5670           MtrVels_TimeBuffer_uS_M_u16p0[3][6]         5610           MtrVels_TimeBuffer_uS_M_u16p0[3][7]         5580		
MtrVel3_TimeBuffer_uS_M_u16p0[0]7       715         MtrVel3_TimeBuffer_uS_M_u16p0[1][0]       1600         MtrVel3_TimeBuffer_uS_M_u16p0[1][1]       1005         MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       1090         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       1175         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       1260         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       1345         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5610          MtrVel3_TimeBuffer_uS_M_u16p0		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]         1600           MtrVel3_TimeBuffer_uS_M_u16p0[1][1]         1005           MtrVel3_TimeBuffer_uS_M_u16p0[1][2]         1090           MtrVel3_TimeBuffer_uS_M_u16p0[1][3]         1175           MtrVel3_TimeBuffer_uS_M_u16p0[1][4]         1260           MtrVel3_TimeBuffer_uS_M_u16p0[1][5]         1345           MtrVel3_TimeBuffer_uS_M_u16p0[1][6]         1430           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][0]         6030           MtrVel3_TimeBuffer_uS_M_u16p0[2][1]         6000           MtrVel3_TimeBuffer_uS_M_u16p0[2][2]         5970           MtrVel3_TimeBuffer_uS_M_u16p0[2][4]         5910           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[2][6]         5850           MtrVel3_TimeBuffer_uS_M_u16p0[3][0]         5790           MtrVel3_TimeBuffer_uS_M_u16p0[3][1]         5760           MtrVel3_TimeBuffer_uS_M_u16p0[3][2]         5730           MtrVel3_TimeBuffer_uS_M_u16p0[3][4]         5670           MtrVel3_TimeBuffer_uS_M_u16p0[3][6]         5640           MtrVel3_TimeBuffer_uS_M_u16p0[3][7]         5580		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]       1090         MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       1175         MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       1260         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       1345         MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       1430         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]       1175         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       1260         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       1345         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580	MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1005
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]       1260         MtrVel3_TimeBuffer_uS_M_u16p0[1][5]       1345         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1430         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		1090
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       1345         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]       1430         MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]       1515         MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]       6030         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]       6000         MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]       5970         MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]       5940         MtrVel3_TimeBuffer_uS_M_u16p0[2][4]       5910         MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]       5880         MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		5940
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]       5850         MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580	MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	5910
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]       5820         MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580	MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	5880
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]       5790         MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]       5760         MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]       5730         MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]       5700         MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]       5670         MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]       5640         MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]       5610         MtrVel3_TimeBuffer_uS_M_u16p0[3][7]       5580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7] 5580		
MtrVel_OldPosBuf_Cnt_M_u08 0		5580
	MtrVel_OldPosBuf_Cnt_M_u08	0

2014-12-26, 17:50:56+0530



Name	Input Value		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>~</b>
RegressionFit()	815.12738	815.1272958 ± 0.0625	~

Name	Test Step 3.7 (Repeat Count = 1)	
MinVeSQ DSBUPPES, CPU, M. (1985)  7 MinVeSQ Possibure, East M. (1920)(1)  4 MinVeSQ Possibure, East M. (1920)(1)  5 MinVeSQ Possibure, East M. (1920)(1)  6 MinVeSQ Possibure, East M. (1920)(1)  6 MinVeSQ Possibure, East M. (1920)(1)  7 MinVeSQ Possibure, East M. (1920)(1)  8 MinVeSQ Possibure, Eas	Name	Input Value
Mirrivide Disublane, Circ M. 1008)  Mirrivide Disublane, Rad M. 102(0)(1)  Mirrivide Disublane, Rad M. 102(1)(1)  Mirrivide Disublane,	MtrVel3_OsBufPos_Cnt_M_u08[0]	7
Avives Positific Res JI, 120(11)  Avives	MtrVel3_OsBufPos_Cnt_M_u08[1]	7
Annes Problems (Paul M. 1920)01 Annes Problems (Paul M. 1920)11 Annes Problems (Paul M. 1920)1	/trVel3_OsBufPos_Cnt_M_u08[2]	7
MiNISE, POSIBIRE, RAM, M. (2010)1  MINISE, POSIBIRE, RAM, M. (2011)1  MINISE, POSIBIRE, RAM, M. (2012)1  MINISE, POSIBIRE, RAM, M. (2012)1  MINISE, POSIBIRE, RAM, M. (2012)11  MINISE, POSIBIRE, RA	/trVel3_OsBufPos_Cnt_M_u08[3]	7
Merkel Posituric Raul M. (2001)  Merkel Posituric Raul M. (2011)  Merkel Posituric Raul M. (2012)	/trVel3_PosBuffer_Rad_M_f32[0][0]	4
Invies_Positure_Road_M_S000[14]  2 mines_Positure_Road_M_S000[14]  2 mines_Positure_Road_M_S000[15]  3 mines_Positure_Road_M_S000[15]  4 mines_Positure_Road_M_S000[17]  5 mines_Positure_Road_M_S000[17]  6 mines_Positure_Road_M_S000[17]  6 mines_Positure_Road_M_S000[17]  7 mines_Positure_Road_M_S000[17]  1 mines_Positure_Road_M_S000[17]  2 mines_Positure_Road_M_S000[17]  1 mines	/trVel3_PosBuffer_Rad_M_f32[0][1]	5
Invited Positricr Read M. 1920[19]  whites Positricr Read M. 1920[19]  https://positricr.Read M. 1920[19]  https://positricr.Read M. 1920[19]  finvios. Positricr.Read M. 1920[19]  finvios. Positricr.Read M. 1920[19]  finvios. Positricr.Read M. 1920[19]  finvios. Positricr.Read M. 1920[19]  graves. Positricr.Read M. 1920[19]  graves. Positricr.Read M. 1920[19]  graves. Positricr.Read M. 1920[19]  finvios. Positricr.	/trVel3_PosBuffer_Rad_M_f32[0][2]	6
Intrinsia Possibutine (Paul M. (20018) 4 Minvilla, Possibutine (Paul M. (20018) 4 Minvilla, Possibutine (Paul M. (20018) 6 Minvilla, Possibutine (Paul M. (20018) 6 Minvilla, Possibutine (Paul M. (20018) 7 Minvilla, Possibutine (Paul M. (20018)) 7 Minvilla, Possi	/trVel3_PosBuffer_Rad_M_f32[0][3]	1
West Posturier, Pand, M. (2010)		2
Inviest Postulare, Paul, M. (2017)  for hirds Postulare, Paul, M. (2017)  for hirds, Postulare, Paul, M. (2012)  for hirds, Postulare, Paul, M. (2013)  for hirds, Postulare, Paul, M. (2018)  for hirds, Postulare, Paul, M. (2018)  for hirds, Postulare, Paul, M. (2019)  for hirds, Postulare, Paul, M. (20		3
Invest. Prositurier, Pand. M. (2011)  invest. Prositurier, Pand. M. (2012)  invest. Prositurier, Pand. M. (2010)  invest. Trimeburier, U.S. M. (1000)  invest.		4
Invived Positurier (Pand, M. 2011)11  1 (Mrived Positurier, Pand, M. 2011)12  1 (Mrived Positurier, Pand, M. 2011)21  1 (Mrived Positurier, Pand, M. 2012)21  1 (Mrived Positurier, Pand, M. 2012)31  1 (Mrive	/trVel3_PosBuffer_Rad_M_f32[0][7]	5
Invest. Possiurier, Pand. M. 2011191  1 (wirest. Possiurier, Pand. M. 2012191)  2 (wirest. Possiurier, Pand. M. 2012191)  3 (wirest. Possiurier, Pand. M. 2012191)  4 (wirest. Possiurier, Pand. M. 2012191)  4 (wirest. Possiurier, Pand. M. 2012191)  5 (wirest. Possiurier, Pand. M. 2012191)  1 (wirest. Possiurier, Pand. M. 2012191)  1 (wirest. Possiurier, Pand. M. 2012191)  2 (wirest. Possiurier, Pand. M. 2012191)		6
Invisit Speaking Rad M. [20] [1]  Whise Speaking Rad M. [20] [2]  Whise Speaking Rad M. [20] [		1
Invivid Posturiner, Rad. M. 122(113)  1		2
HIVESE, POSBUTIER, Rad, M. (221)16]  4 MIVESE, POSBUTIER, Rad, M. (321)16]  5 MIVESE, POSBUTIER, Rad, M. (321)17]  1 MIVESE, POSBUTIER, Rad, M. (321)18]  5 MIVESE, POSBUTIER, Rad, M. (321)18]  1 MIVESE, POSBUTIER, Rad, M. (321)18]  1 MIVESE, POSBUTIER, Rad, M. (321)18]  1 MIVESE, POSBUTIER, Rad, M. (321)19]  1 MIVESE, POSBUTIER, Rad, M. (320)19]  1 MIVESE, M. (1800)119]  1 MIVESE, POSBUTIER, RAM, M. (300)119]  1 MIVESE, M. (1800)119]  1 MIVE		
Intrivisia Posibuffer, Rad. M. (2021)[9]  10		4
Intrivisia Posiburifer, Rad. M., 182[116]  Intrivisia, Posiburifer, Rad. M., 182[210]  Intrivisia, Posiburifer, Rad. M., 182[210]  Intrivisia, Posiburifer, Rad. M., 182[210]  Intrivisia, Posiburifer, Rad. M., 182[212]  Intrivisia, Posiburifer, Rad. M., 182[212]  Intrivisia, Posiburifer, Rad. M., 182[212]  Intrivisia, Posiburifer, Rad. M., 182[213]  Intrivisia, Posiburifer, Rad. M., 182[216]  Intrivisia, Posiburifer, Rad. M., 182[216]  Intrivisia, Posiburifer, Rad. M., 182[216]  Intrivisia, Posiburifer, Rad. M., 182[217]  Intrivisia, Posiburifer, Rad. M., 182[217]  Intrivisia, Posiburifer, Rad. M., 182[310]  Intrivisia, Posiburifer, Rad. M., 182[310]  Intrivisia, Posiburifer, Rad. M., 182[311]  Intrivisia, Posiburifer, Rad. M., 182[31]  Intrivisia, Rad. M., 186[31]		
Intrivisia   Posiburifer   Rod M.   182  17    1   1   1   1   1   1   1   1		
Intrivisit		
Intrives PosBuffer, Rad M. [32[2]]  intrives PosBuffer, Rad M. [32[2]]  intrives PosBuffer, Rad M. [32[2]]  fittives PosBuffer, Rad M. [32[3]]  intrives PosBuffer, Rad M. [32[3]]  fittives PosBuffer, Rad M. [32[3]]  intrives PosBuffer, Rad M. [32[3]]  fittives PosBuffer, Rad M. [32		
Attives Posburfer, Rad, M, (32(2))(2)  Attives Posburfer, Rad, M, (32(2))(3)  Attives Posburfer, Rad, M, (32(2))(4)  Attives Posburfer, Rad, M, (32(2))(5)  Attives Posburfer, Rad, M, (32(2))(7)  Attives Posburfer, Rad, M, (32(2))(7)  Attives Posburfer, Rad, M, (32(2))(7)  Attives Posburfer, Rad, M, (32(3))(1)  Attives Posburfer, Rad, M, (32(3))(3)  Attives Posburfer, Rad, M, (32(3))(3)  Attives Posburfer, Rad, M, (32(3))(4)  Attives Posburfer, Rad, M, (32(3))(4)  Attives Posburfer, Rad, M, (32(3))(4)  Attives Posburfer, Rad, M, (32(3))(6)  Attives Posburfer, Rad, M, (32(3))(6)  Attives Posburfer, Rad, M, (32(3))(7)  Attives Posburfer,		
Intrivels   PosBuffer   Rad   M.		
Intrivels   PosBuffer, Rad   M.		
Intrivisia		
trivels PosBuffer Rad, M. [32 2 6]  1 trivels PosBuffer Rad, M. [32 2 7]  3 trivels PosBuffer Rad, M. [32 3 0]  4 trivels PosBuffer Rad, M. [32 3 1]  5 trivels PosBuffer Rad, M. [32 3 3]  1 trivels PosBuffer Rad, M. [32 3 4]  2 trivels PosBuffer Rad, M. [32 3 6]  3 trivels PosBuffer Rad, M. [32 3 6]  4 trivels PosBuffer Rad, M. [32 3 6]  4 trivels PosBuffer Rad, M. [32 3 6]  4 trivels PosBuffer Rad, M. [32 3 7]  5 trivels TrimeBuffer Las, M. [32 3 7]  5 trivels TrimeBuffer Las, M. [36 0 0]0]  1 trivels TrimeBuffer Las, M. [36 0 0]1  1 trivels TrimeBuffer Las, M. [36 0 0]2  1 trivels TrimeBuffer Las, M. [36 0 0]3  1 trivels TrimeBuffer Las, M. [36 0 0]5  1 trivels TrimeBuffer Las, M. [36 0 0]5  1 trivels TrimeBuffer Las, M. [36 0 0]5  1 trivels TrimeBuffer Las, M. [36 0 0]6  1 trivels TrimeBuffer Las, M. [36 0 0]7  1 trivels TrimeBuffer Las, M. [36 0 0]8  1 trivels TrimeBuffer Las, M. [36 0 0]12  1 trivels TrimeBuffer Las, M. [36 0 0]12  1 trivels TrimeBuffer Las, M. [36 0 0]12  1 trivels TrimeBuffer Las, M. [36 0 0]13  1 trivels TrimeBuffer Las, M. [36 0 0]15  1 trivels TrimeBuffer Las, M. [36 0 0]2]1  1 trivels TrimeBuffer Las, M. [36 0 0]2]2  1 trivels TrimeBuffer Las, M. [36 0 0]2]3  1 trivels		
Intrive 3		
Intrivels		
Intrivels   PosBuffer_Rad   M.		
Attivels   PosBuffer Rad   M.		
Airvela PosBuffer Rad M (32(3)[3])       1         Airvela PosBuffer Rad M (32(3)[5])       3         Airvela PosBuffer Rad M (32(3)[5])       4         Airvela PosBuffer Rad M (32(3)[7])       5         Airvela TimeBuffer us M (16p0(0)[0])       12000         Airvela TimeBuffer us M (16p0(0)[1])       11875         Airvela TimeBuffer us M (16p0(0)[2])       11750         Airvela TimeBuffer us M (16p0(0)[2])       11750         Airvela TimeBuffer us M (16p0(0)[3])       11625         Airvela TimeBuffer us M (16p0(0)[6])       11500         Airvela TimeBuffer us M (16p0(0)[6])       11250         Airvela TimeBuffer us M (16p0(0)[7])       11125         Airvela TimeBuffer us M (16p0(1)[1])       60000         Airvela TimeBuffer us M (16p0(1)[1])       59875         Airvela TimeBuffer us M (16p0(1)[1])       59875         Airvela TimeBuffer us M (16p0(1)[6])       59375         Airvela TimeBuffer us M (16p0(1)[6])       59375         Airvela TimeBuffer us M (16p0(1)[6])       59250         Airvela TimeBuffer us M (16p0(1)[6])       59250         Airvela TimeBuffer us M (16p0(1)[6])       59250         Airvela TimeBuffer us M (16p0(2)[4])       2025         Airvela TimeBuffer us M (16p0(2)[4])       2525         Airvela TimeBuffe		
AftrVeil3_PosBuffer_Rad_M_[32[3][4]         2           AftrVeil3_PosBuffer_Rad_M_[32[3][5]         3           AftrVeil3_PosBuffer_Rad_M_[32[3][7]         5           AftrVeil3_PosBuffer_US_M_u16p0[0][0]         12000           AftrVeil3_TimeBuffer_uS_M_u16p0[0][1]         11875           AftrVeil3_TimeBuffer_uS_M_u16p0[0][2]         11750           AftrVeil3_TimeBuffer_uS_M_u16p0[0][3]         11625           AftrVeil3_TimeBuffer_uS_M_u16p0[0][4]         11500           AftrVeil3_TimeBuffer_uS_M_u16p0[0][5]         11375           AftrVeil3_TimeBuffer_uS_M_u16p0[0][6]         11250           AftrVeil3_TimeBuffer_uS_M_u16p0[0][7]         11125           AftrVeil3_TimeBuffer_uS_M_u16p0[1][1]         60000           AftrVeil3_TimeBuffer_uS_M_u16p0[1][2]         59750           AftrVeil3_TimeBuffer_uS_M_u16p0[1][3]         59625           AftrVeil3_TimeBuffer_uS_M_u16p0[1][4]         59500           AftrVeil3_TimeBuffer_uS_M_u16p0[1][6]         59250           AftrVeil3_TimeBuffer_uS_M_u16p0[1][7]         59125           AftrVeil3_TimeBuffer_uS_M_u16p0[2][1]         205           AftrVeil3_TimeBuffer_uS_M_u16p0[2][1]         2150           AftrVeil3_TimeBuffer_uS_M_u16p0[2][6]         2275           AftrVeil3_TimeBuffer_uS_M_u16p0[2][6]         2275           AftrVeil3_		
Strivel3   PosBuffer Rad M   f32 3  5    3		
Attrivels_PosBuffer_Rad_M_732[3][6]   4		
Strivels		
12000		
1875		
11750   1175		
11625   11625   11625   11626   1162		
thrvel3_TimeBuffer_uS_M_u16p0[0][4]     11500       thrvel3_TimeBuffer_uS_M_u16p0[0][5]     11375       thrvel3_TimeBuffer_uS_M_u16p0[0][7]     11250       thrvel3_TimeBuffer_uS_M_u16p0[0][7]     11125       thrvel3_TimeBuffer_uS_M_u16p0[1][0]     60000       thrvel3_TimeBuffer_uS_M_u16p0[1][1]     59875       thrvel3_TimeBuffer_uS_M_u16p0[1][2]     59750       thrvel3_TimeBuffer_uS_M_u16p0[1][3]     59625       thrvel3_TimeBuffer_uS_M_u16p0[1][4]     59500       thrvel3_TimeBuffer_uS_M_u16p0[1][6]     59375       thrvel3_TimeBuffer_uS_M_u16p0[1][6]     59250       thrvel3_TimeBuffer_uS_M_u16p0[1][7]     59125       thrvel3_TimeBuffer_uS_M_u16p0[2][0]     2025       thrvel3_TimeBuffer_uS_M_u16p0[2][1]     2150       thrvel3_TimeBuffer_uS_M_u16p0[2][2]     2275       thrvel3_TimeBuffer_uS_M_u16p0[2][3]     2400       thrvel3_TimeBuffer_uS_M_u16p0[2][6]     2525       thrvel3_TimeBuffer_uS_M_u16p0[2][6]     2525       thrvel3_TimeBuffer_uS_M_u16p0[2][6]     2775       thrvel3_TimeBuffer_uS_M_u16p0[2][7]     2900       thrvel3_TimeBuffer_uS_M_u16p0[2][7]     2900       thrvel3_TimeBuffer_uS_M_u16p0[2][7]     2900       thrvel3_TimeBuffer_uS_M_u16p0[2][7]     2900       thrvel3_TimeBuffer_uS_M_u16p0[2][7]     2900       thrvel3_TimeBuffer_uS_M_u16p0[2][7]		
1176   1176		
trvel3_TimeBuffer_uS_M_u16p0[0][6]		
11125   1112		
September   Sept		
Section   Sect		
Sect		
Second		
Section   Sect		
ItrVel3_TimeBuffer_us_M_u16p0[1][6]       59250         ItrVel3_TimeBuffer_us_M_u16p0[1][7]       59125         ItrVel3_TimeBuffer_us_M_u16p0[2][0]       2025         ItrVel3_TimeBuffer_us_M_u16p0[2][1]       2150         ItrVel3_TimeBuffer_us_M_u16p0[2][2]       2275         ItrVel3_TimeBuffer_us_M_u16p0[2][3]       2400         ItrVel3_TimeBuffer_us_M_u16p0[2][4]       2525         ItrVel3_TimeBuffer_us_M_u16p0[2][5]       2650         ItrVel3_TimeBuffer_us_M_u16p0[2][6]       2775         ItrVel3_TimeBuffer_us_M_u16p0[3][0]       6025		
Section   Sect		
trVel3_TimeBuffer_uS_M_u16p0[2][0] 2025 trVel3_TimeBuffer_uS_M_u16p0[2][1] 2150 trVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 trVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 trVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 trVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 trVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 trVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 trVel3_TimeBuffer_uS_M_u16p0[3][0] 6025		
trVel3_TimeBuffer_us_M_u16p0[2][1] 2150 trVel3_TimeBuffer_us_M_u16p0[2][2] 2275 trVel3_TimeBuffer_us_M_u16p0[2][3] 2400 trVel3_TimeBuffer_us_M_u16p0[2][4] 2525 trVel3_TimeBuffer_us_M_u16p0[2][5] 2650 trVel3_TimeBuffer_us_M_u16p0[2][6] 2775 trVel3_TimeBuffer_us_M_u16p0[2][7] 2900 trVel3_TimeBuffer_us_M_u16p0[3][0] 6025		
trVel3_TimeBuffer_uS_M_u16p0[2][2] 2275 trVel3_TimeBuffer_uS_M_u16p0[2][3] 2400 trVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 trVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 trVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 trVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 trVel3_TimeBuffer_uS_M_u16p0[3][0] 6025		
trVel3_TimeBuffer_us_M_u16p0[2][3] 2400 trVel3_TimeBuffer_us_M_u16p0[2][4] 2525 trVel3_TimeBuffer_us_M_u16p0[2][5] 2650 trVel3_TimeBuffer_us_M_u16p0[2][6] 2775 trVel3_TimeBuffer_us_M_u16p0[2][7] 2900 trVel3_TimeBuffer_us_M_u16p0[3][0] 6025		
trVel3_TimeBuffer_uS_M_u16p0[2][4] 2525 trVel3_TimeBuffer_uS_M_u16p0[2][5] 2650 trVel3_TimeBuffer_uS_M_u16p0[2][6] 2775 trVel3_TimeBuffer_uS_M_u16p0[2][7] 2900 trVel3_TimeBuffer_uS_M_u16p0[3][0] 6025		
ItrVel3_TimeBuffer_uS_M_u16p0[2][5]       2650         ItrVel3_TimeBuffer_uS_M_u16p0[2][6]       2775         ItrVel3_TimeBuffer_uS_M_u16p0[2][7]       2900         ItrVel3_TimeBuffer_uS_M_u16p0[3][0]       6025		
AttrVel3_TimeBuffer_uS_M_u16p0[2][6]       2775         AttrVel3_TimeBuffer_uS_M_u16p0[2][7]       2900         AttrVel3_TimeBuffer_uS_M_u16p0[3][0]       6025		
ItrVel3_TimeBuffer_uS_M_u16p0[2][7]       2900         ItrVel3_TimeBuffer_uS_M_u16p0[3][0]       6025		
ttr/Vel3_TimeBuffer_uS_M_u16p0[3][0] 6025		
ttrVel3_TimeBuffer_uS_M_u16p0[3][1] 6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2] 6275		

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	6525		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	6650		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	6775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	6900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	<b>✓</b>
RegressionFit()	517.699097	517.6990817 ± 0.0625	<b>✓</b>

Test Step 3.8 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	3	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	4	
MtrVel3_OsBufPos_Cnt_M_u08[3]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[0][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2	
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3	
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4	
MtrVel3_PosBuffer_Rad_M_f32[1][4]	5	
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6	
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1	
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4	
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5	
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6	
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1	
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2	
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3	
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6	
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1	
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2	
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3	
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4	
MtrVel3_PosBuffer_Rad_M_f32[3][6]	5	
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6	
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	6000	
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	6010	
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	6020	
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	6030	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	6040	
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	6050	
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	6060	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	6070	
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	700	
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	770	
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	840	
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	350	
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	420	
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	490	
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	560	
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	630	
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6080	
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6090	
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6100	
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6110	
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6120	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6130		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	6140		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	6150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1190		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1260		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	770		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	840		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	910		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	980		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	1120		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
RegressionFit()	510.956726	510.9567538 ± 0.0625	~

Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	1
MtrVel3_OsBufPos_Cnt_M_u08[2]	2
MtrVel3_OsBufPos_Cnt_M_u08[3]	3
MtrVel3_PosBuffer_Rad_M_f32[0][0]	1.6
MtrVel3_PosBuffer_Rad_M_f32[0][1]	2.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	3
MtrVel3_PosBuffer_Rad_M_f32[0][3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][4]	5
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	1
MtrVel3_PosBuffer_Rad_M_f32[0][7]	2
MtrVel3_PosBuffer_Rad_M_f32[1][0]	3.6
MtrVel3_PosBuffer_Rad_M_f32[1][1]	4
MtrVel3_PosBuffer_Rad_M_f32[1][2]	5
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6
MtrVel3_PosBuffer_Rad_M_f32[1][4]	1
MtrVel3_PosBuffer_Rad_M_f32[1][5]	2
MtrVel3_PosBuffer_Rad_M_f32[1][6]	3
MtrVel3_PosBuffer_Rad_M_f32[1][7]	4.6
MtrVel3_PosBuffer_Rad_M_f32[2][0]	5
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6
MtrVel3_PosBuffer_Rad_M_f32[2][2]	1
MtrVel3_PosBuffer_Rad_M_f32[2][3]	2
MtrVel3_PosBuffer_Rad_M_f32[2][4]	3.65
MtrVel3_PosBuffer_Rad_M_f32[2][5]	4
MtrVel3_PosBuffer_Rad_M_f32[2][6]	5
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6
MtrVel3_PosBuffer_Rad_M_f32[3][0]	1
MtrVel3_PosBuffer_Rad_M_f32[3][1]	2
MtrVel3_PosBuffer_Rad_M_f32[3][2]	3
MtrVel3_PosBuffer_Rad_M_f32[3][3]	4
	5.5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.4
MtrVel3_PosBuffer_Rad_M_f32[3][7]	9850
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	9745
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	9760
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	9775
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	9790 9805
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]  MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	9805
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	
MtrVel3_TimeBuffer_uS_M_u16p0[0][7] MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	9835
	10
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	20
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	30
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	40
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	50
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	60
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	70

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	160		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	-
MtrVel_OsBufSelect_Cnt_M_u08	3	3	-
RegressionFit()	-673.206726	-673.2066598 ± 0.0625	<b>✓</b>

Test Step 3.10 (Repeat Count = 1)		
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
/trVel3_OsBufPos_Cnt_M_u08[1]	2	
/trVel3_OsBufPos_Cnt_M_u08[2]	3	
/trVel3_OsBufPos_Cnt_M_u08[3]	4	
ftrVel3_PosBuffer_Rad_M_f32[0][0]	6	
1trVel3_PosBuffer_Rad_M_f32[0][1]	5.98	
ftrVel3_PosBuffer_Rad_M_f32[0][2]	4	
ftrVel3_PosBuffer_Rad_M_f32[0][3]	3	
1trVel3_PosBuffer_Rad_M_f32[0][4]	2.7	
1trVel3_PosBuffer_Rad_M_f32[0][5]	1	
ItrVel3_PosBuffer_Rad_M_f32[0][6]	6	
1trVel3_PosBuffer_Rad_M_f32[0][7]	5.6	
1trVel3_PosBuffer_Rad_M_f32[1][0]	4	
1trVel3_PosBuffer_Rad_M_f32[1][1]	3	
/trVel3_PosBuffer_Rad_M_f32[1][2]	2	
1trVel3_PosBuffer_Rad_M_f32[1][3]	1.6	
1trVel3_PosBuffer_Rad_M_f32[1][4]	6	
1trVel3_PosBuffer_Rad_M_f32[1][5]	5	
ltrVel3_PosBuffer_Rad_M_f32[1][6]	4.6	
ltrVel3_PosBuffer_Rad_M_f32[1][7]	3	
ItrVel3_PosBuffer_Rad_M_f32[2][0]	2	
1trVel3_PosBuffer_Rad_M_f32[2][1]	1	
ItrVel3_PosBuffer_Rad_M_f32[2][2]	6.0999999	
ItrVel3_PosBuffer_Rad_M_f32[2][3]	5	
ItrVel3_PosBuffer_Rad_M_f32[2][4]	4	
ItrVel3 PosBuffer Rad M f32[2][5]	3	
ItrVel3 PosBuffer Rad M f32[2][6]	2	
trVel3_PosBuffer_Rad_M_f32[2][7]	1	
ItrVel3_PosBuffer_Rad_M_f32[3][0]	6.23000002	
ItrVel3_PosBuffer_Rad_M_f32[3][1]	5	
1trVel3_PosBuffer_Rad_M_f32[3][2]	4	
ItrVel3_PosBuffer_Rad_M_f32[3][3]	3	
ItrVel3_PosBuffer_Rad_M_f32[3][4]	2	
ItrVel3_PosBuffer_Rad_M_f32[3][5]	1	
ItrVel3_PosBuffer_Rad_M_f32[3][6]	6	
ItrVel3_PosBuffer_Rad_M_f32[3][7]	5.4	
htrVel3_TimeBuffer_uS_M_u16p0[0][0]	8245	
ItrVel3_TimeBuffer_uS_M_u16p0[0][1]	8125	
trVel3_TimeBuffer_uS_M_u16p0[0][2]	8005	
trVel3_TimeBuffer_uS_M_u16p0[0][3]	7885	
ltrVel3_TimeBuffer_uS_M_u16p0[0][4]	7765	
ItrVel3_TimeBuffer_uS_M_u16p0[0][5]	7645	
ltrVel3_TimeBuffer_uS_M_u16p0[0][6]	7525	
/trVel3_TimeBuffer_uS_M_u16p0[0][7]	7405	
/trVel3 TimeBuffer uS M u16p0[1][0]	7285	

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	7165		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	7045		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	6925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	6805		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	6685		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	6565		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	6445		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	3640		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	3760		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	3880		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	3160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	3280		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	3400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	4020		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	4140		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	4260		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	4380		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	4500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3660		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3780		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3900		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	•
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~
RegressionFit()	-1009.09265	-1009.09265	~

Test Step 3.11 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	4
	5
MtrVel3_OsBufPos_Cnt_M_u08[1]  MtrVel3_OsBufPos_Cnt_M_u08[2]	6
MtrVel3_OsBufPos_Cnt_M_u08[2]	7
MtrVel3_OsBufPos_Cnt_M_u08[3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][0]	3
MtrVel3_PosBuffer_Rad_M_f32[0][1]	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6
MtrVel3_PosBuffer_Rad_M_f32[0][5]	
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	340
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	360
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	380

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Name	Input Value		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	400		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	420		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	64495		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	64520		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	64545		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	64570		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	64595		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	64620		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	64445		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	64470		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	64850		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	64875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	64900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	64925		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	64950		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	64975		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	65000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	64825		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	520		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	540		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	560		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	580		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	620		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	640		
MtrVel_OldPosBuf_Cnt_M_u08	1		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	~
RegressionFit()	-657.894653	-657.8947368 ± 0.0625	<b>✓</b>

Test Step 3.12 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	0
MtrVel3_OsBufPos_Cnt_M_u08[1]	0
MtrVel3_OsBufPos_Cnt_M_u08[2]	0
MtrVel3_OsBufPos_Cnt_M_u08[3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0.5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3.4
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2.3
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5.9
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3.5
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2.7		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	430		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	440		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	450		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	460		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	470		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	490		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	680		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	690		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	710		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	720		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	730		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	6500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	6400		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	6300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	6200		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	6100		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	6000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	5900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	5800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	20000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	19900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	19800		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	19700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	19600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	19500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	19400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	19300		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	~
RegressionFit()	1008.40723	1008.407346 ± 0.0625	<b>✓</b>

Test Step 3.13 (Repeat Count = 1)	• • • • • • • • • • • • • • • • • • •
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	4
MtrVel3_PosBuffer_Rad_M_f32[0][1]	5
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6
MtrVel3_PosBuffer_Rad_M_f32[0][3]	1
MtrVel3_PosBuffer_Rad_M_f32[0][4]	2
MtrVel3_PosBuffer_Rad_M_f32[0][5]	3
MtrVel3_PosBuffer_Rad_M_f32[0][6]	4
MtrVel3_PosBuffer_Rad_M_f32[0][7]	5
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6
MtrVel3_PosBuffer_Rad_M_f32[1][1]	1
MtrVel3_PosBuffer_Rad_M_f32[1][2]	2
MtrVel3_PosBuffer_Rad_M_f32[1][3]	3
MtrVel3_PosBuffer_Rad_M_f32[1][4]	4
MtrVel3_PosBuffer_Rad_M_f32[1][5]	5
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6
MtrVel3_PosBuffer_Rad_M_f32[1][7]	1
MtrVel3_PosBuffer_Rad_M_f32[2][0]	2
MtrVel3_PosBuffer_Rad_M_f32[2][1]	3
MtrVel3_PosBuffer_Rad_M_f32[2][2]	4
MtrVel3_PosBuffer_Rad_M_f32[2][3]	5
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6
MtrVel3_PosBuffer_Rad_M_f32[2][5]	1
MtrVel3_PosBuffer_Rad_M_f32[2][6]	2

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- 1.0g. 000.0 1.			
Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	5		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	4125		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	4000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	3875		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	3750		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	3625		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	3500		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	3375		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	3250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	3125		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	3000		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	2875		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	2750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	2625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	2500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	2375		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	2250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	7625		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	7750		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	7875		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	7125		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	7250		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	7375		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	7500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	11500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	11625		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	11750		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	11875		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	12000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	11125		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	11250		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	11375		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Resul
MtrVel_OldPosBuf_Cnt_M_u08	3	3	
MtrVel_OsBufSelect_Cnt_M_u08	0	0	•
RegressionFit()	125.000008	125 ± 0.0625	

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	1
MtrVel3_OsBufPos_Cnt_M_u08[1]	2
MtrVel3_OsBufPos_Cnt_M_u08[2]	3
MtrVel3_OsBufPos_Cnt_M_u08[3]	4
MtrVel3_PosBuffer_Rad_M_f32[0][0]	5
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6
MtrVel3_PosBuffer_Rad_M_f32[0][2]	1
MtrVel3_PosBuffer_Rad_M_f32[0][3]	2
MtrVel3_PosBuffer_Rad_M_f32[0][4]	3
MtrVel3_PosBuffer_Rad_M_f32[0][5]	4
MtrVel3_PosBuffer_Rad_M_f32[0][6]	5
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6
MtrVel3_PosBuffer_Rad_M_f32[1][0]	1
MtrVel3_PosBuffer_Rad_M_f32[1][1]	2
MtrVel3_PosBuffer_Rad_M_f32[1][2]	3
MtrVel3_PosBuffer_Rad_M_f32[1][3]	4
MtrVel3_PosBuffer_Rad_M_f32[1][4]	5
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6
MtrVel3_PosBuffer_Rad_M_f32[1][6]	1
MtrVel3_PosBuffer_Rad_M_f32[1][7]	2
MtrVel3_PosBuffer_Rad_M_f32[2][0]	3

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	4		
	5		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][7]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	1150 1200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	850		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]			
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	900		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	950		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	1000		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	1050		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	1100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	1700		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	1750		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	1800		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	1450		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	1500		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	1550		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	1600		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	1650		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	4328		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	4278		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	4228		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	4178		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	4128		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	4078		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	4028		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	3978		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	3928		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	3878		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	3828		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	3778		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	3728		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	3678		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	3628		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	3578		
MtrVel_OldPosBuf_Cnt_M_u08	0		
MtrVel_OsBufSelect_Cnt_M_u08	1		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	2	2	<b>V</b>
RegressionFit()	-357.669678	-357.6697277 ± 0.0625	-
rogrossion It()	307.003070	001.0001211 ± 0.0020	

Test Step 3.15 (Repeat Count = 1)	
Name	Input Value
MtrVel3_OsBufPos_Cnt_M_u08[0]	3
MtrVel3_OsBufPos_Cnt_M_u08[1]	4
MtrVel3_OsBufPos_Cnt_M_u08[2]	5
MtrVel3_OsBufPos_Cnt_M_u08[3]	6
MtrVel3_PosBuffer_Rad_M_f32[0][0]	0
MtrVel3_PosBuffer_Rad_M_f32[0][1]	0
MtrVel3_PosBuffer_Rad_M_f32[0][2]	0
MtrVel3_PosBuffer_Rad_M_f32[0][3]	0
MtrVel3_PosBuffer_Rad_M_f32[0][4]	0
MtrVel3_PosBuffer_Rad_M_f32[0][5]	0
MtrVel3_PosBuffer_Rad_M_f32[0][6]	0
MtrVel3_PosBuffer_Rad_M_f32[0][7]	0
MtrVel3_PosBuffer_Rad_M_f32[1][0]	0
MtrVel3_PosBuffer_Rad_M_f32[1][1]	0
MtrVel3_PosBuffer_Rad_M_f32[1][2]	0

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Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	0		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	0		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	0		
MtrVel3 PosBuffer Rad M f32[3][7]	0		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	160		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	170		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	180		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	190		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	200		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	210		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	220		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	230		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	240		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	250		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	260		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	270		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	280		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	290		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	8500		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	8600		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	8700		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	8800		
MtrVel3 TimeBuffer uS M u16p0[2][4]	8900		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	9000		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	8300		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	8400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9400		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9500		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9600		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9700		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9800		
MtrVel3 TimeBuffer uS M u16p0[3][5]	9900		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	10000		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9300		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value	Result
	3	-	Result
MtrVel_OldPosBuf_Cnt_M_u08	0	3	~
MtrVel_OsBufSelect_Cnt_M_u08 RegressionFit()	0	0 ± 0.0625	
Inchicopioni II()	U	0 ± 0.0020	

Test Step 3.16 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	1	
MtrVel3_OsBufPos_Cnt_M_u08[1]	2	
MtrVel3_OsBufPos_Cnt_M_u08[2]	0	
MtrVel3_OsBufPos_Cnt_M_u08[3]	2	
MtrVel3_PosBuffer_Rad_M_f32[0][0]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][1]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][2]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][3]	6.283089433	
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6.283089433	

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RegressionFit		TOACI CO	10
Name	Input Value		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	6.283089433		
MtrVel3 PosBuffer Rad M f32[3][4]	6.283089433		
MtrVel3 PosBuffer Rad M f32[3][5]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	6.283089433		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	6.283089433		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	999		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	888		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	777		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	555		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	444		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	333		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	222		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	925		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	825		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	725		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	625		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	525		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	425		
MtrVel3 TimeBuffer uS M u16p0[1][6]	325		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	225		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	666		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	232		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	294		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	356		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	418		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	480		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	542		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	604		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	1042		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	1104		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	1166		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	732		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	794		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	856		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	918		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	980		
MtrVel_OldPosBuf_Cnt_M_u08	2		
MtrVel_OsBufSelect_Cnt_M_u08	3		
Name	Actual Value	Expected Value R	Result
MtrVel_OldPosBuf_Cnt_M_u08	3	3	V
MtrVel_OsBufSelect_Cnt_M_u08	0	0	
RegressionFit()	0	0 ± 0.0625	Ĵ
regression n()	V	0 ± 0.0020	

Test Step 3.17 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel3_OsBufPos_Cnt_M_u08[0]	0	
MtrVel3_OsBufPos_Cnt_M_u08[1]	1	
MtrVel3 OsBufPos Cnt M u08[2]	2	

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		•	
Name	Input Value		
MtrVel3_OsBufPos_Cnt_M_u08[3]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[0][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[0][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[0][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[0][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[0][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[0][7]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][0]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][1]	5		
MtrVel3_PosBuffer_Rad_M_f32[1][2]	6		
MtrVel3_PosBuffer_Rad_M_f32[1][3]	1		
MtrVel3_PosBuffer_Rad_M_f32[1][4]	2		
MtrVel3_PosBuffer_Rad_M_f32[1][5]	3		
MtrVel3_PosBuffer_Rad_M_f32[1][6]	4		
MtrVel3_PosBuffer_Rad_M_f32[1][7]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][0]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][1]	1		
MtrVel3_PosBuffer_Rad_M_f32[2][2]	2		
MtrVel3_PosBuffer_Rad_M_f32[2][3]	3		
MtrVel3_PosBuffer_Rad_M_f32[2][4]	4		
MtrVel3_PosBuffer_Rad_M_f32[2][5]	5		
MtrVel3_PosBuffer_Rad_M_f32[2][6]	6		
MtrVel3_PosBuffer_Rad_M_f32[2][7]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][0]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][1]	3		
MtrVel3_PosBuffer_Rad_M_f32[3][2]	4		
MtrVel3_PosBuffer_Rad_M_f32[3][3]	5		
MtrVel3_PosBuffer_Rad_M_f32[3][4]	6		
MtrVel3_PosBuffer_Rad_M_f32[3][5]	1		
MtrVel3_PosBuffer_Rad_M_f32[3][6]	2		
MtrVel3_PosBuffer_Rad_M_f32[3][7]	3		
MtrVel3_TimeBuffer_uS_M_u16p0[0][0]	10		
MtrVel3_TimeBuffer_uS_M_u16p0[0][1]	20		
MtrVel3_TimeBuffer_uS_M_u16p0[0][2]	30		
MtrVel3_TimeBuffer_uS_M_u16p0[0][3]	40		
MtrVel3_TimeBuffer_uS_M_u16p0[0][4]	50		
MtrVel3_TimeBuffer_uS_M_u16p0[0][5]	60		
MtrVel3_TimeBuffer_uS_M_u16p0[0][6]	70		
MtrVel3_TimeBuffer_uS_M_u16p0[0][7]	80		
MtrVel3_TimeBuffer_uS_M_u16p0[1][0]	90		
MtrVel3_TimeBuffer_uS_M_u16p0[1][1]	100		
MtrVel3_TimeBuffer_uS_M_u16p0[1][2]	110		
MtrVel3_TimeBuffer_uS_M_u16p0[1][3]	120		
MtrVel3_TimeBuffer_uS_M_u16p0[1][4]	130		
MtrVel3_TimeBuffer_uS_M_u16p0[1][5]	140		
MtrVel3_TimeBuffer_uS_M_u16p0[1][6]	150		
MtrVel3_TimeBuffer_uS_M_u16p0[1][7]	160		
MtrVel3_TimeBuffer_uS_M_u16p0[2][0]	10770		
MtrVel3_TimeBuffer_uS_M_u16p0[2][1]	10785		
MtrVel3_TimeBuffer_uS_M_u16p0[2][2]	10800		
MtrVel3_TimeBuffer_uS_M_u16p0[2][3]	10695		
MtrVel3_TimeBuffer_uS_M_u16p0[2][4]	10710		
MtrVel3_TimeBuffer_uS_M_u16p0[2][5]	10725		
MtrVel3_TimeBuffer_uS_M_u16p0[2][6]	10740		
MtrVel3_TimeBuffer_uS_M_u16p0[2][7]	10755		
MtrVel3_TimeBuffer_uS_M_u16p0[3][0]	9805		
MtrVel3_TimeBuffer_uS_M_u16p0[3][1]	9820		
MtrVel3_TimeBuffer_uS_M_u16p0[3][2]	9835		
MtrVel3_TimeBuffer_uS_M_u16p0[3][3]	9850		
MtrVel3_TimeBuffer_uS_M_u16p0[3][4]	9745		
MtrVel3_TimeBuffer_uS_M_u16p0[3][5]	9760		
MtrVel3_TimeBuffer_uS_M_u16p0[3][6]	9775		
MtrVel3_TimeBuffer_uS_M_u16p0[3][7]	9790		
MtrVel_OldPosBuf_Cnt_M_u08	3		
MtrVel_OsBufSelect_Cnt_M_u08	2		
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	2	2	~
MtrVel_OsBufSelect_Cnt_M_u08	3	3	<b>-</b>
RegressionFit()	263.157898	263.1578947 ± 0.0625	

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MtrVel\_Per2

Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Per2

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MfrVel_Dig\\u00e4utp\contract -I\$(PROJECTROOT)\MfrVel_Dig\\u00e4utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MfrVel_Dig\\u00e4include -I\$(Compiler Install Path\u00e4\u00farcude

Comments/Description/Spe	ecification
Name	Text



Module 'MtrVel\_Digi'

Name of Tester:Sunita Kore
Code File(s) Under Test:Sa\_MtrVel.c
Code File(s) Version:5
Module Design Document:MotorVelocity\_MDD.doc
Module Design Document Version:6
Data Dictionary Version:3
Unit Test Plan Version:2
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS470\_4.9.5
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31
Total FLASH Used (Bytes):42
Total CALS Used (Bytes):42
Total CALS Used (Bytes):54
Special Test Requirements:
Test Date:12/26/2014
Comments:
"Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.

Note2: In function ""MtrVel\_Per1"", the values of variable ""MechMtrPos2Timestamp\_USec\_u32"" and
""MtrVel\_PrevMtrPosSampleTime2\_uS\_M\_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2\_us\_T\_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.

Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.

Note4: In function ""RegressionFit"", the path 'P5' i.e. ""(SumDeltaSqT\_uSsq\_T\_f32 > FLT\_EPSILON) = FALSE"" has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3\_TimeBuffer\_uS\_M\_u16p0[4][8]"") and have been given value as '0' for all array elements.

Note5: ""CBD\_Sandbox\_dbg.map"" is embedded for reference."

<b>Attributes</b>		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS 1.1 2900 Cycles TS 1.2 2725 Cycles

Description VECTOR DESCRIPTION:

TS 1.1 "Shortest Execution Path:

 $\label{eq:limit_final} if(MtrVelCorrLimDiff\_MtrRadpS\_T\_f32 > k\_MtrVelCorrLim\_MtrRadpS\_f32) = False if(DiagFailed\_m(MtrVel\_DiffAcc\_Cnt\_M\_u16,k\_MtrVelCorrLim\_Cnt\_Str) == TRUE) = False if(HwVelCorrLimDiff\_MtrRadpS\_T\_f32 > k\_HwVelCorrLim\_HwRadpS\_f32) = True$ 

I(HwvelCorrLimDiff\_MtrRadpS\_T\_132 > k\_HwvelCorrLim\_HwRadpS\_f32)=True (DiagFailed\_m(MtrVel\_HwVelDiffAcc\_Cnt\_M\_u16,k\_HwVelCorrLim\_Cnt\_Str) == TRUE)=FALSE if(NTCCount\_Cnt\_T\_u8 > D\_ZERO\_CNT\_U8)=False if(((TRUE == MtrPosFault1\_Cnt\_T\_lgc)=True)||(TRUE == MtrPosFault2\_Cnt\_T\_lgc))"
TS 1.2 "Longest Execution Path: (MtrVelCorrLimDiff\_MtrRadpS\_T\_132 > k\_MtrVelCorrLim\_MtrRadpS\_f32)=TRUE if(DiagFailed\_m(MtrVel\_DiffAcc\_Cnt\_M\_u16,k\_MtrVelCorrLim\_Cnt\_Str) == TRUE)=True if(HwVelCorrLimDiff\_MtrRadpS\_T\_f32 > k\_HwVelCorrLim\_HwRadpS\_f32)=False if(DiagFailed\_m(MtrVel\_DiffAcc\_Cnt\_M\_u16,k\_HwVelCorrLim\_Cnt\_Str) == TRUE)=True if(NTCCount\_Cnt\_T\_u8 > D\_ZERO\_CNT\_U8)=True if(NTCCount\_Cnt\_T\_u8 > D\_ZERO\_CNT\_U8)=True if((TRUE == MtrPosFault1\_Cnt\_T\_lgc\_)||(TRUE == MtrPosFault2\_Cnt\_T\_lgc\_)|=Selse"

if( (TRUE == MtrPosFault1\_Cnt\_T\_lgc) ||( TRUE == MtrPosFault2\_Cnt\_T\_lgc))=>False"

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16 116			
MtrVel_HwVelDiffAcc_Cnt_M_u16	142		
MtrVel_HwVel_HwRadpS_M_f32	-14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	233		
k_HwVelCorrLim_Cnt_Str.PStep	82		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	-23		
k_MtrVelCorrLim_Cnt_Str.Threshold	124		
k_MtrVelCorrLim_Cnt_Str.PStep	194		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	330		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 1.2 (Repeat Count = 1)	<b>✓</b>	
Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	110	
MtrVel_HwVelDiffAcc_Cnt_M_u16	128	
MtrVel_HwVel_HwRadpS_M_f32	-32	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	116	
k_HwVelCorrLim_Cnt_Str.PStep	19	
k_HwVelCorrLim_Cnt_Str.NStep	10	
k_HwVelCorrLim_HwRadpS_f32	19	
k_MtrVelCorrLim_Cnt_Str.Threshold	2	
k_MtrVelCorrLim_Cnt_Str.PStep	77	
k_MtrVelCorrLim_Cnt_Str.NStep	1	

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MtrVel\_Per2

Name	Input Value		
k_MtrVelCorrLim_MtrRadpS_f32	-1110		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	118	118	<b>~</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

MtrVel\_Per2

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Test Case 2: Boundary Check

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MtrVel\_Per2



#### Specification

Performance Metrics
(With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS 2.1 2801 Cycles
TS 2.2 2744 Cycles
TS 2.3 2573 Cycles
TS 2.4 2572 Cycles
TS 2.6 2575 Cycles
TS 2.6 2575 Cycles
TS 2.6 2575 Cycles
TS 2.7 2573 Cycles
TS 2.8 2572 Cycles
TS 2.8 2572 Cycles
TS 2.9 2621 Cycles
TS 2.9 2621 Cycles
TS 2.10 2602 Cycles
TS 2.11 2701 Cycles
TS 2.12 2644 Cycles
TS 2.13 2635 Cycles
TS 2.14 2687 Cycles
TS 2.15 2644 Cycles
TS 2.16 2643 Cycles
TS 2.17 2623 Cycles
TS 2.18 2625 Cycles
TS 2.19 2622 Cycles
TS 2.29 2622 Cycles
TS 2.29 2622 Cycles
TS 2.20 2622 Cycles
TS 2.21 2686 Cycles
TS 2.22 2622 Cycles
TS 2.23 2614 Cycles
TS 2.24 2625 Cycles
TS 2.25 2645 Cycles
TS 2.262 2677 Cycles
TS 2.27 2639 Cycles
TS 2.28 2657 Cycles
TS 2.29 2588 Cycles
TS 2.29 2588 Cycles
TS 2.29 2580 Cycles
TS 2.20 2595 Cycles
TS 2.21 2660 Cycles
TS 2.23 2660 Cycles
TS 2.33 2622 Cycles
TS 2.34 2625 Cycles
TS 2.35 2660 Cycles
TS 2.362 2660 Cycles
TS 2.37 2625 Cycles
TS 2.38 2622 Cycles
TS 2.39 2600 Cycles
TS 2.39 2600 Cycles
TS 2.30 2600 Cycles
TS 2.31 2595 Cycles
TS 2.32 2660 Cycles
TS 2.34 2622 Cycles
TS 2.35 2623 Cycles
TS 2.362 2620 Cycles
TS 2.37 2625 Cycles
TS 2.38 2622 Cycles
TS 2.39 2600 Cycles
TS 2.39 2600 Cycles
TS 2.39 2601 Cycles
TS 2.39 2603 Cycles
TS 2.39 2603 Cycles
TS 2.39 2603 Cycles
TS 2.30 2625 Cycles
TS 2.31 2626 Cycles
TS 2.32 2626 Cycles
TS 2.34 2620 Cycles
TS 2.35 2631 Cycles
TS 2.44 2690 Cycles
TS 2.45 2632 Cycles
TS 2.47 2632 Cycles
TS 2.48 2631 Cycles
TS 2.49 2634 Cycles
TS 2.49 2634 Cycles
TS 2.55 2631 Cycles
TS 2.55 2631 Cycles
TS 2.55 2631 Cycles
TS 2.55 2632 Cycles
TS 2.55 2631 Cycles
TS 2.55 2636 Cycles



#### **Description** VECTOR DESCRIPTION:

VECTOR DESCRIPTION:
TS1.1 all min TS1.2 all max TS1.3 SysCDiagMtrVelMRF_MtrRadpS_f32=min TS1.4 SysCDiagMtrVelMRF_MtrRadpS_f32=max TS1.5 SysCDiagMtrVelMRF_MtrRadpS_f32=max TS1.5 SysCDiagMtrVelMRF_MtrRadpS_f32=mos TS1.6 SysCDiagMtrVelMRF_MtrRadpS_f32=neg TS1.8 SysCDiagMtrVelMRF_MtrRadpS_f32=neg TS1.8 SysCDiagHwVel_HwRadpS_f32=min TS1.9 SysCDiagHwVel_HwRadpS_f32=min TS1.10 SysCDiagHwVel_HwRadpS_f32=neg TS1.11 SysCDiagHwVel_HwRadpS_f32=neg TS1.12 SysCDiagHwVel_HwRadpS_f32=neg TS1.13 Rte_Call_NxtrDiagMgr_GetNTCFailed[2]=min TS1.14 Rte_Call_NxtrDiagMgr_GetNTCFailed[2]=max TS1.15 MtrVel_MtrVelMRF_MtrRadpS_M_f32=min TS1.16 MtrVel_MtrVelMRF_MtrRadpS_M_f32=min TS1.18 MtrVel_MtrVelMRF_MtrRadpS_M_f32=neg TS1.19 MtrVel_MtrVelMRF_MtrRadpS_M_f32=neg TS1.20 k_MtrVelCorrLim_MtrRadpS_f32=min TS1.21 k_MtrVelCorrLim_MtrRadpS_f32=max TS1.22 k_MtrVelCorrLim_MtrRadpS_f32=neg TS1.23 k_MtrVelCorrLim_MtrRadpS_f32=neg TS1.24 k_MtrVelCorrLim_MtrRadpS_f32=neg TS1.25 MtrVel_DiffAcc_Cnt_M_u16=max TS1.27 MtrVel_DiffAcc_Cnt_M_u16=max TS1.28 k_MtrVelCorrLim_Cnt_Str.Threshold=min TS1.29 k_MtrVelCorrLim_Cnt_Str.Threshold=max TS1.31 k_MtrVelCorrLim_Cnt_Str.Threshold=max TS1.32 k_MtrVelCorrLim_Cnt_Str.Threshold=pos TS1.31 k_MtrVelCorrLim_Cnt_Str.Pstep=min TS1.32 k_MtrVelCorrLim_Cnt_Str.Pstep=max TS1.33 k_MtrVelCorrLim_Cnt_Str.Pstep=pos TS1.34 k_MtrVelCorrLim_Cnt_Str.Pstep=pos
TS1.19 MtrVel_MtrVelMRF_MtrRadpS_M_f32=neg TS1.20 k_MtrVelCorrLim_MtrRadpS_f32=min
TS1.22 k MtrVelCorrLim MtrRadpS f32=mid
TS1.24 k_MtrVelCorrLim_MtrRadpS_f32=neg
TS1.29 k MtrVelCorrLim Cnt Str.Threshold=max
TS1.30 k MtrVelCorrLim Cnt Str.Threshold=pos
TS1.31 k_MtrVelCorrLim_Cnt_Str.Pstep=min
IS1.32 k_MtrVolCorrLim_Cnt_Str.Pstep=max
TS1.33 k_MtrVelCorrl im_Cnt_Str.Pstep=pos
TS1.35 k_MtrVelCorrLim_Cnt_Str.Nstep=max TS1.36 k_MtrVelCorrLim_Cnt_Str.Nstep=pos
TS1.37 MtrVel_HwVel_HwRadpS_M_f32=min
TS1.39 MtrVel_HwVel_HwRadpS_M_f32=mid TS1.40 MtrVel_HwVel_HwRadpS_M_f32=pos
TS1.41 MtrVel HwVel HwRadpS M f32=neg
TS1.42 k_HwVelCorrLim_HwRadpS_f32=min
TS1.43 k_HwVelCorrLim_HwRadpS_f32=max
TS1.44 k_HwVelCorrLim_HwRadpS_f32=mid
TS1.45 k_HwVelCorrLim_HwRadpS_f32=pos TS1.46 k_HwVelCorrLim_HwRadpS_f32=neg
TS1.47 MtrVel HwVelDiffAcc Cnt M u16=min
TS1.48 MtrVel_HwVelDiffAcc_Cnt_M_u16=max
TS1.49 MtrVel_HwVelDiffAcc_Cnt_M_u16=pos
TS1.50 k_HwVelCorrLim_Cnt_Str.Threshold=min TS1.51 k HwVelCorrLim Cnt Str.Threshold=max
TS1.53 k HwVelCorrLim Cnt Str.Pstep=min
TS1.54 k_HwVelCorrLim_Cnt_Str.Pstep=max
TS1.55 k_HwVelCorrLim_Cnt_Str.Pstep=pos
TS1.56 k_HwVelCorrLim_Cnt_Str.Nstep=min TS1.57 k_HwVelCorrLim_Cnt_Str.Nstep=max
TS1.58 k_HwVelCorrLim_Cnt_Str.Nstep=pos
. =

Test Step 2.1 (Repeat Count = 1)			<b>y</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	0		
MtrVel_HwVelDiffAcc_Cnt_M_u16	0		
MtrVel_HwVel_HwRadpS_M_f32	-42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF	_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	<b>~</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>





Name	Actual Value	Expected Value	Result
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)			~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	255		
MtrVel_HwVelDiffAcc_Cnt_M_u16	255		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	235	235	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	235	235	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	10		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	-32		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	116		
k_HwVelCorrLim_Cnt_Str.PStep	19		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	19		
k_MtrVelCorrLim_Cnt_Str.Threshold	2		
k_MtrVelCorrLim_Cnt_Str.PStep	77		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	-1110		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	<b>✓</b>





Name	Actual Value	Expected Value	Result
MtrVel_HwVelDiffAcc_Cnt_M_u16	15	15	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>~</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	12		
MtrVel_HwVelDiffAcc_Cnt_M_u16	38		
MtrVel_HwVel_HwRadpS_M_f32	-30		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-960		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	129		
k_HwVelCorrLim_Cnt_Str.PStep	26		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	21		
k_MtrVelCorrLim_Cnt_Str.Threshold	5		
k_MtrVelCorrLim_Cnt_Str.PStep	90		
k_MtrVelCorrLim_Cnt_Str.NStep	2		
k_MtrVelCorrLim_MtrRadpS_f32	-950		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-39		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRa	ndpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_f	/trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	5	5	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>~</b>	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•	

Test Step 2.5 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	25
MtrVel_HwVelDiffAcc_Cnt_M_u16	51
MtrVel_HwVel_HwRadpS_M_f32	-28
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-920
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	142
k_HwVelCorrLim_Cnt_Str.PStep	33
k_HwVelCorrLim_Cnt_Str.NStep	12
k_HwVelCorrLim_HwRadpS_f32	23.4500008
k_MtrVelCorrLim_Cnt_Str.Threshold	9
k_MtrVelCorrLim_Cnt_Str.PStep	103
k_MtrVelCorrLim_Cnt_Str.NStep	3
k_MtrVelCorrLim_MtrRadpS_f32	-790
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-37.5499992
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	0
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc





Name	Input Value		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	9	9	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	39	39	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	38		
MtrVel_HwVelDiffAcc_Cnt_M_u16	64		
MtrVel_HwVel_HwRadpS_M_f32	-26		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-330.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	155		
k_HwVelCorrLim_Cnt_Str.PStep	40		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	25		
k_MtrVelCorrLim_Cnt_Str.Threshold	19		
k_MtrVelCorrLim_Cnt_Str.PStep	116		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	-630.340027		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-35		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1000.5		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_M	ftrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	19	19	<b>✓</b>
MtrVel_HwVelDiffAcc_Cnt_M_u16	51	51	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.7 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
MtrVel_DiffAcc_Cnt_M_u16	51	
MtrVel_HwVelDiffAcc_Cnt_M_u16	77	
MtrVel_HwVel_HwRadpS_M_f32	-24.5599995	
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-840	
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel	
k_HwVelCorrLim_Cnt_Str.Threshold	168	
k_HwVelCorrLim_Cnt_Str.PStep	47	
k_HwVelCorrLim_Cnt_Str.NStep	14	
k_HwVelCorrLim_HwRadpS_f32	27	
k_MtrVelCorrLim_Cnt_Str.Threshold	26	
k_MtrVelCorrLim_Cnt_Str.PStep	129	
k_MtrVelCorrLim_Cnt_Str.NStep	5	

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MtrVel\_Per2

Name	Input Value		
k_MtrVelCorrLim_MtrRadpS_f32	-470		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-33		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1000.5		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	26	26	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	63	63	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>~</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	64		
MtrVel_HwVelDiffAcc_Cnt_M_u16	90		
MtrVel_HwVel_HwRadpS_M_f32	-22		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-800		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	181		
k_HwVelCorrLim_Cnt_Str.PStep	54		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	29		
k_MtrVelCorrLim_Cnt_Str.Threshold	33		
k_MtrVelCorrLim_Cnt_Str.PStep	142		
k_MtrVelCorrLim_Cnt_Str.NStep	6		
k_MtrVelCorrLim_MtrRadpS_f32	-310		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	lpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	33	33	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	75	75	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	77
MtrVel_HwVelDiffAcc_Cnt_M_u16	103
MtrVel_HwVel_HwRadpS_M_f32	-20
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-760
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel
k_HwVelCorrLim_Cnt_Str.Threshold	194
k_HwVelCorrLim_Cnt_Str.PStep	61
k_HwVelCorrLim_Cnt_Str.NStep	16



MtrVel\_Per2

Name	Input Value		
k_HwVelCorrLim_HwRadpS_f32	31		
k_MtrVelCorrLim_Cnt_Str.Threshold	40		
k_MtrVelCorrLim_Cnt_Str.PStep	155		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-150.669998		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-870		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mt	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	40	40	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	164	164	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 2.10 (Repeat Count = 1)			
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	90		
MtrVel_HwVelDiffAcc_Cnt_M_u16	116		
MtrVel_HwVel_HwRadpS_M_f32	-18.5400009		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-720.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	207		
k_HwVelCorrLim_Cnt_Str.PStep	68		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-25.4500008		
k_MtrVelCorrLim_Cnt_Str.Threshold	47		
k_MtrVelCorrLim_Cnt_Str.PStep	168		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-790		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRa	adpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_l	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	47	47	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	184	184	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt MtrVel Per2 HwVelValid Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
MtrVel_DiffAcc_Cnt_M_u16	103
MtrVel_HwVelDiffAcc_Cnt_M_u16	129
MtrVel_HwVel_HwRadpS_M_f32	-16
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-680

tgt\_MtrVel\_Per2\_HwVelValid\_Cnt\_lgc.value

MtrVel\_Per2

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Name Input Value Rte\_Inst\_Sa\_MtrVel tgt\_Rte\_Inst\_Sa\_MtrVel  $k\_HwVelCorrLim\_Cnt\_Str.Threshold$ 220 k\_HwVelCorrLim\_Cnt\_Str.PStep 75  $k\_HwVelCorrLim\_Cnt\_Str.NStep$ 18 k\_HwVelCorrLim\_HwRadpS\_f32 -24  $k\_MtrVelCorrLim\_Cnt\_Str.Threshold$ 54 k\_MtrVelCorrLim\_Cnt\_Str.PStep 181  $k\_MtrVelCorrLim\_Cnt\_Str.NStep$ 9 k\_MtrVelCorrLim\_MtrRadpS\_f32 170  $tgt\_MtrVel\_Per2\_SysCDiagHwVel\_HwRadpS\_f32.value$ 20.5 tgt\_MtrVel\_Per2\_SysCDiagMtrVelMRF\_MtrRadpS\_f32.value -710.869995  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per2\_HwVelValid\_Cnt\_lgc$ tgt\_MtrVel\_Per2\_HwVelValid\_Cnt\_lgc  $tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per2\_SysCDiagHwVel\_HwRadpS\_f32$ tgt\_MtrVel\_Per2\_SysCDiagHwVel\_HwRadpS\_f32 tgt\_Rte\_Inst\_Sa\_MtrVel.MtrVel\_Per2\_SysCDiagMtrVelMRF\_MtrRadpS\_f32  $tgt\_MtrVel\_Per2\_SysCDiagMtrVelMRF\_MtrRadpS\_f32$ **Expected Value** Result Name **Actual Value** MtrVel\_DiffAcc\_Cnt\_M\_u16 94 94 MtrVel\_HwVelDiffAcc\_Cnt\_M\_u16 204 204  $Rte\_Call\_Sa\_MtrVel\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 204 204 Rte\_Call\_Sa\_MtrVel\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) 1 1  $Rte\_Call\_Sa\_MtrVel\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 1

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	116		
MtrVel_HwVelDiffAcc_Cnt_M_u16	142		
MtrVel_HwVel_HwRadpS_M_f32	-14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	233		
k_HwVelCorrLim_Cnt_Str.PStep	82		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	-23		
k_MtrVelCorrLim_Cnt_Str.Threshold	61		
k_MtrVelCorrLim_Cnt_Str.PStep	194		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	330		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20.5		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_M	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>	



Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	129		
MtrVel_HwVelDiffAcc_Cnt_M_u16	155		
MtrVel_HwVel_HwRadpS_M_f32	-12		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-600		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	246		
k_HwVelCorrLim_Cnt_Str.PStep	89		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	-22		
k_MtrVelCorrLim_Cnt_Str.Threshold	68		
k_MtrVelCorrLim_Cnt_Str.PStep	207		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	490		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	32.8800011		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-550		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	118	118	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	244	244	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.14 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	142		
MtrVel_HwVelDiffAcc_Cnt_M_u16	168		
MtrVel_HwVel_HwRadpS_M_f32	-10.3400002		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-560.450012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	250		
k_HwVelCorrLim_Cnt_Str.PStep	96		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-21.4500008		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	650.669983		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-470.660004		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	130	130	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	250	250	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>	



Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	155		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	-8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	251		
k_HwVelCorrLim_Cnt_Str.PStep	103		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-20		
k_MtrVelCorrLim_Cnt_Str.Threshold	82		
k_MtrVelCorrLim_Cnt_Str.PStep	233		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	810		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-28		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-390		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	82	82	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	251	251	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	168		
MtrVel_HwVelDiffAcc_Cnt_M_u16	194		
MtrVel_HwVel_HwRadpS_M_f32	-6		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	110		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	-19		
k_MtrVelCorrLim_Cnt_Str.Threshold	89		
k_MtrVelCorrLim_Cnt_Str.PStep	246		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	970		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-26		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-310		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	89	89	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
$Rte\_Call\_Sa\_MtrVel\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_Igc.value	0	0	<b>~</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	181		
MtrVel_HwVelDiffAcc_Cnt_M_u16	207		
MtrVel_HwVel_HwRadpS_M_f32	-4		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	12		
k_HwVelCorrLim_Cnt_Str.PStep	117		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	-18		
k_MtrVelCorrLim_Cnt_Str.Threshold	96		
k_MtrVelCorrLim_Cnt_Str.PStep	250		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-24		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	96	96	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	12	12	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	194		
MtrVel_HwVelDiffAcc_Cnt_M_u16	220		
MtrVel_HwVel_HwRadpS_M_f32	-2.33999991		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	500.799988		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	25		
k_HwVelCorrLim_Cnt_Str.PStep	124		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-17.6539993		
k_MtrVelCorrLim_Cnt_Str.Threshold	103		
k_MtrVelCorrLim_Cnt_Str.PStep	251		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	20.5599995		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-22.3400002		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-150		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw\	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr\	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	103	103	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	25	25	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
			✓





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt MtrVel Per2 HwVelValid Cnt lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	207		
MtrVel_HwVelDiffAcc_Cnt_M_u16	233		
MtrVel_HwVel_HwRadpS_M_f32	0		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-500.700012		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	38		
k_HwVelCorrLim_Cnt_Str.PStep	131		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	-16		
k_MtrVelCorrLim_Cnt_Str.Threshold	110		
k_MtrVelCorrLim_Cnt_Str.PStep	10		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	30		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-70.4000015		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	110	110	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	38	38	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•	



Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	220		
MtrVel_HwVelDiffAcc_Cnt_M_u16	246		
MtrVel_HwVel_HwRadpS_M_f32	2		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-200		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	51		
k_HwVelCorrLim_Cnt_Str.PStep	138		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	-15		
k_MtrVelCorrLim_Cnt_Str.Threshold	117		
k_MtrVelCorrLim_Cnt_Str.PStep	12		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-18		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	10		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	117	117	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	51	51	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~		
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~		

Test Step 2.21 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	233		
MtrVel_HwVelDiffAcc_Cnt_M_u16	250		
MtrVel_HwVel_HwRadpS_M_f32	4		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-100.339996		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	64		
k_HwVelCorrLim_Cnt_Str.PStep	145		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	-14		
k_MtrVelCorrLim_Cnt_Str.Threshold	124		
k_MtrVelCorrLim_Cnt_Str.PStep	25		
k_MtrVelCorrLim_Cnt_Str.NStep	19		
k_MtrVelCorrLim_MtrRadpS_f32	1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-16		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	90		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVeIMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	214	214	<b>~</b>
MtrVel_HwVelDiffAcc_Cnt_M_u16	64	64	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>	



Test Step 2.22 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	246		
MtrVel_HwVelDiffAcc_Cnt_M_u16	251		
MtrVel_HwVel_HwRadpS_M_f32	6.67000008		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-50		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	16		
k_HwVelCorrLim_Cnt_Str.PStep	152		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-13		
k_MtrVelCorrLim_Cnt_Str.Threshold	131		
k_MtrVelCorrLim_Cnt_Str.PStep	38		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	0		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-14		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	170.759995		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	131	131	•
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	16	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.23 (Repeat Count = 1)			
Name	Input Value		
MtrVel DiffAcc Cnt M u16	250		
MtrVel HwVelDiffAcc Cnt M u16	169		
MtrVel HwRadpS M f32	8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	0		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	159		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	-12.4499998		
k_MtrVelCorrLim_Cnt_Str.Threshold	138		
k_MtrVelCorrLim_Cnt_Str.PStep	51		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	250.199997		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	250	250	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	28	28	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•





Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.24 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	251		
MtrVel_HwVelDiffAcc_Cnt_M_u16	172		
MtrVel_HwVel_HwRadpS_M_f32	10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	50		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	32		
k_HwVelCorrLim_Cnt_Str.PStep	166		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-11		
k_MtrVelCorrLim_Cnt_Str.Threshold	145		
k_MtrVelCorrLim_Cnt_Str.PStep	64		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	-250.800003		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-10.9499998		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	330		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	145	145	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	32	32	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>	

MtrVel	
MtrVel	
HwVelValid_Cnt_lgc	
SysCDiagHwVel_HwRadpS_f32	
SysCDiagMtrVeIMRF_MtrRadpS_f32	
Expected Value	ue Result
16	~
50	<b>✓</b>
204	<b>✓</b>
_	16 50





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.26 (Repeat Count = 1)			✓	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	255			
MtrVel_HwVelDiffAcc_Cnt_M_u16	178			
MtrVel_HwVel_HwRadpS_M_f32	14			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	150			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	68			
k_HwVelCorrLim_Cnt_Str.PStep	9			
k_HwVelCorrLim_Cnt_Str.NStep	2			
k_HwVelCorrLim_HwRadpS_f32	-9			
k_MtrVelCorrLim_Cnt_Str.Threshold	159			
k_MtrVelCorrLim_Cnt_Str.PStep	28			
k_MtrVelCorrLim_Cnt_Str.NStep	3			
k_MtrVelCorrLim_MtrRadpS_f32	55.6699982			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-6			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	490	490		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	oS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mt	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	159	159	<b>✓</b>	
MtrVel_HwVelDiffAcc_Cnt_M_u16	68	68	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>





Test Step 2.27 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	120		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	16		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	200.449997		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	86		
k_HwVelCorrLim_Cnt_Str.PStep	13		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	-8		
k_MtrVelCorrLim_Cnt_Str.Threshold	166		
k_MtrVelCorrLim_Cnt_Str.PStep	32		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	68		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-4		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	570.880005		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	152	152	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	86	86	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.28 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	16		
MtrVel_HwVelDiffAcc_Cnt_M_u16	184		
MtrVel_HwVel_HwRadpS_M_f32	18		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	250		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	16		
k_HwVelCorrLim_Cnt_Str.PStep	17		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	-7		
k_MtrVelCorrLim_Cnt_Str.Threshold	0		
k_MtrVelCorrLim_Cnt_Str.PStep	50		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	79		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-2.3499999		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	650		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	16	16	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~		
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>		



Test Step 2.29 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	28		
MtrVel_HwVelDiffAcc_Cnt_M_u16	187		
MtrVel_HwVel_HwRadpS_M_f32	20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	300		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	21		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-6.78000021		
k_MtrVelCorrLim_Cnt_Str.Threshold	255		
k_MtrVelCorrLim_Cnt_Str.PStep	68		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	112		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	730		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	96	96	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	28	28	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.30 (Repeat Count = 1)			V	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	32			
MtrVel_HwVelDiffAcc_Cnt_M_u16	190			
MtrVel_HwVel_HwRadpS_M_f32	22			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	350.779999			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	32			
k_HwVelCorrLim_Cnt_Str.PStep	25			
k_HwVelCorrLim_Cnt_Str.NStep	6			
k_HwVelCorrLim_HwRadpS_f32	-5			
k_MtrVelCorrLim_Cnt_Str.Threshold	120			
k_MtrVelCorrLim_Cnt_Str.PStep	86			
k_MtrVelCorrLim_Cnt_Str.NStep	11			
k_MtrVelCorrLim_MtrRadpS_f32	145			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	2			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	810			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	118	118	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	32	32	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.31 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	50		
MtrVel_HwVelDiffAcc_Cnt_M_u16	193		
MtrVel_HwVel_HwRadpS_M_f32	24.5599995		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	400		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	50		
k_HwVelCorrLim_Cnt_Str.PStep	29		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	-4		
k_MtrVelCorrLim_Cnt_Str.Threshold	169		
k_MtrVelCorrLim_Cnt_Str.PStep	0		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	178		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	4		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	50	50	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>~</b>	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Test Step 2.32 (Repeat Count = 1)			<u> </u>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	68		
MtrVel_HwVelDiffAcc_Cnt_M_u16	196		
MtrVel_HwVel_HwRadpS_M_f32	26		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	450		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	68		
k_HwVelCorrLim_Cnt_Str.PStep	33		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	-3		
k_MtrVelCorrLim_Cnt_Str.Threshold	172		
k_MtrVelCorrLim_Cnt_Str.PStep	255		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	211.654999		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	6		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	970		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	172	172	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	68	68	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>→</b>
			•





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt MtrVel Per2 HwVelValid Cnt lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.33 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	86		
MtrVel_HwVelDiffAcc_Cnt_M_u16	199		
MtrVel_HwVel_HwRadpS_M_f32	28		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	500		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	86		
k_HwVelCorrLim_Cnt_Str.PStep	37		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	-2		
k_MtrVelCorrLim_Cnt_Str.Threshold	175		
k_MtrVelCorrLim_Cnt_Str.PStep	100		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	244		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	8.43999958		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1050.88		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	175	175	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	86	86	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

est Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.34 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	104		
MtrVel_HwVelDiffAcc_Cnt_M_u16	202		
MtrVel_HwVel_HwRadpS_M_f32	30		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	550		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	104		
k_HwVelCorrLim_Cnt_Str.PStep	41		
k_HwVelCorrLim_Cnt_Str.NStep	13		
k_HwVelCorrLim_HwRadpS_f32	-1.34000003		
k_MtrVelCorrLim_Cnt_Str.Threshold	178		
k_MtrVelCorrLim_Cnt_Str.PStep	169		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	277		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1130		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	178	178	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	104	104	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	122		
MtrVel_HwVelDiffAcc_Cnt_M_u16	205		
MtrVel_HwVel_HwRadpS_M_f32	32		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	600.559998		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	122		
k_HwVelCorrLim_Cnt_Str.PStep	45		
k_HwVelCorrLim_Cnt_Str.NStep	14		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	181		
k_MtrVelCorrLim_Cnt_Str.PStep	172		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	310.559998		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1210		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	181	181	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	122	122	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	140		
MtrVel_HwVelDiffAcc_Cnt_M_u16	208		
MtrVel_HwVel_HwRadpS_M_f32	34.5600014		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	650		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	140		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	15		
k_HwVelCorrLim_HwRadpS_f32	1		
k_MtrVelCorrLim_Cnt_Str.Threshold	184		
k_MtrVelCorrLim_Cnt_Str.PStep	175		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	343		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	14		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1290		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	184	184	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	140	140	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.37 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	158		
MtrVel_HwVelDiffAcc_Cnt_M_u16	211		
MtrVel_HwVel_HwRadpS_M_f32	-42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	700		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	158		
k_HwVelCorrLim_Cnt_Str.PStep	53		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	2		
k_MtrVelCorrLim_Cnt_Str.Threshold	187		
k_MtrVelCorrLim_Cnt_Str.PStep	178		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	376		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	16		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	50.6599998		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRa	dpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_N	trRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	187	187	<b>✓</b>
MtrVel_HwVelDiffAcc_Cnt_M_u16	158	158	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	<b>✓</b>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>~</b>

Test Step 2.38 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	176		
MtrVel_HwVelDiffAcc_Cnt_M_u16	214		
MtrVel_HwVel_HwRadpS_M_f32	42		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	750		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	176		
k_HwVelCorrLim_Cnt_Str.PStep	57		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	3		
k_MtrVelCorrLim_Cnt_Str.Threshold	190		
k_MtrVelCorrLim_Cnt_Str.PStep	181		
k_MtrVelCorrLim_Cnt_Str.NStep	14		
k_MtrVelCorrLim_MtrRadpS_f32	409		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	18		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	110		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	190	190	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	176	176	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Test Step 2.39 (Repeat Count = 1)			~
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	194		
MtrVel_HwVelDiffAcc_Cnt_M_u16	5		
MtrVel_HwVel_HwRadpS_M_f32	0		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	800.650024		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	194		
k_HwVelCorrLim_Cnt_Str.PStep	61		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	4.55999994		
k_MtrVelCorrLim_Cnt_Str.Threshold	193		
k_MtrVelCorrLim_Cnt_Str.PStep	184		
k_MtrVelCorrLim_Cnt_Str.NStep	15		
k_MtrVelCorrLim_MtrRadpS_f32	442		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20.9799995		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	170		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	193	193	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	66	66	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
			<b>✓</b>





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt MtrVel Per2 HwVelValid Cnt lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.40 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	212		
MtrVel_HwVelDiffAcc_Cnt_M_u16	16		
MtrVel_HwVel_HwRadpS_M_f32	20.1000004		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	850		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	212		
k_HwVelCorrLim_Cnt_Str.PStep	65		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	5		
k_MtrVelCorrLim_Cnt_Str.Threshold	196		
k_MtrVelCorrLim_Cnt_Str.PStep	187		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	475		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	22		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	196	196	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	



Test Step 2.41 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	230		
MtrVel_HwVelDiffAcc_Cnt_M_u16	27		
MtrVel_HwVel_HwRadpS_M_f32	-20.8999996		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	900		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	3		
k_HwVelCorrLim_Cnt_Str.PStep	4		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	6		
k_MtrVelCorrLim_Cnt_Str.Threshold	199		
k_MtrVelCorrLim_Cnt_Str.PStep	190		
k_MtrVelCorrLim_Cnt_Str.NStep	17		
k_MtrVelCorrLim_MtrRadpS_f32	508.649994		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	24		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	290.033997		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	199	199	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.42 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	248		
MtrVel_HwVelDiffAcc_Cnt_M_u16	38		
MtrVel_HwVel_HwRadpS_M_f32	-41.2400017		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	950		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	7		
k_HwVelCorrLim_Cnt_Str.PStep	19		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-32		
k_MtrVelCorrLim_Cnt_Str.Threshold	202		
k_MtrVelCorrLim_Cnt_Str.PStep	193		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	541		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	26		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	202	202	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	7	7	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.43 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	5		
MtrVel_HwVelDiffAcc_Cnt_M_u16	49		
MtrVel_HwVel_HwRadpS_M_f32	-39		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1000.45001		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	9		
k_HwVelCorrLim_Cnt_Str.PStep	34		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	32		
k_MtrVelCorrLim_Cnt_Str.Threshold	205		
k_MtrVelCorrLim_Cnt_Str.PStep	196		
k_MtrVelCorrLim_Cnt_Str.NStep	19		
k_MtrVelCorrLim_MtrRadpS_f32	574		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	28.4500008		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	201	201	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	9	9	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.44 (Repeat Count = 1)			<b>~</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	9		
MtrVel_HwVelDiffAcc_Cnt_M_u16	60		
MtrVel_HwVel_HwRadpS_M_f32	-37		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1050		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
k_HwVelCorrLim_Cnt_Str.Threshold	21		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	208		
k_MtrVelCorrLim_Cnt_Str.PStep	199		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	607		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	21	21	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.45 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	13		
MtrVel_HwVelDiffAcc_Cnt_M_u16	71		
MtrVel_HwVel_HwRadpS_M_f32	-35		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1100		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	33		
k_HwVelCorrLim_Cnt_Str.PStep	64		
k_HwVelCorrLim_Cnt_Str.NStep	3		
k_HwVelCorrLim_HwRadpS_f32	10.6000004		
k_MtrVelCorrLim_Cnt_Str.Threshold	211		
k_MtrVelCorrLim_Cnt_Str.PStep	202		
k_MtrVelCorrLim_Cnt_Str.NStep	0		
k_MtrVelCorrLim_MtrRadpS_f32	640		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	32		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	590.887024		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	13	13	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	33	33	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~	

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	17		
MtrVel_HwVelDiffAcc_Cnt_M_u16	82		
MtrVel_HwVel_HwRadpS_M_f32	-33		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1150		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	45		
k_HwVelCorrLim_Cnt_Str.PStep	79		
k_HwVelCorrLim_Cnt_Str.NStep	5		
k_HwVelCorrLim_HwRadpS_f32	-10.3000002		
k_MtrVelCorrLim_Cnt_Str.Threshold	214		
k_MtrVelCorrLim_Cnt_Str.PStep	205		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	767.650024		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	34		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	590		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_C	cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw\	/el_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr\	/elMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	7	7	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	45	45	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt MtrVel Per2 HwVelValid Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 2.47 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	21		
MtrVel_HwVelDiffAcc_Cnt_M_u16	0		
MtrVel_HwVel_HwRadpS_M_f32	13		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1200.44995		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	57		
k_HwVelCorrLim_Cnt_Str.PStep	94		
k_HwVelCorrLim_Cnt_Str.NStep	7		
k_HwVelCorrLim_HwRadpS_f32	7		
k_MtrVelCorrLim_Cnt_Str.Threshold	217		
k_MtrVelCorrLim_Cnt_Str.PStep	208		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	706		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	36		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	650		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	20	20	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	57	57	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•	





Test Step 2.48 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	25		
MtrVel_HwVelDiffAcc_Cnt_M_u16	255		
MtrVel_HwVel_HwRadpS_M_f32	12.4499998		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1250		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	69		
k_HwVelCorrLim_Cnt_Str.PStep	109		
k_HwVelCorrLim_Cnt_Str.NStep	9		
k_HwVelCorrLim_HwRadpS_f32	8		
k_MtrVelCorrLim_Cnt_Str.Threshold	220		
k_MtrVelCorrLim_Cnt_Str.PStep	211		
k_MtrVelCorrLim_Cnt_Str.NStep	3		
k_MtrVelCorrLim_MtrRadpS_f32	739		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	38.4399986		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	710		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	22	22	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	69	69	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.49 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	29		
MtrVel_HwVelDiffAcc_Cnt_M_u16	120		
MtrVel_HwVel_HwRadpS_M_f32	11		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1300		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	81		
k_HwVelCorrLim_Cnt_Str.PStep	124		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	9.97000027		
k_MtrVelCorrLim_Cnt_Str.Threshold	223		
k_MtrVelCorrLim_Cnt_Str.PStep	214		
k_MtrVelCorrLim_Cnt_Str.NStep	5		
k_MtrVelCorrLim_MtrRadpS_f32	772		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	40		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	770.653992		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	24	24	<b>✓</b>
MtrVel_HwVelDiffAcc_Cnt_M_u16	81	81	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.50 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	33		
MtrVel_HwVelDiffAcc_Cnt_M_u16	19		
MtrVel_HwVel_HwRadpS_M_f32	10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	25.6700001		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	0		
k_HwVelCorrLim_Cnt_Str.PStep	139		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	10		
k_MtrVelCorrLim_Cnt_Str.Threshold	226		
k_MtrVelCorrLim_Cnt_Str.PStep	217		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	805		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	830		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	26	26	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.51 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	37		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	9		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	75		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	255		
k_HwVelCorrLim_Cnt_Str.PStep	154		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	11		
k_MtrVelCorrLim_Cnt_Str.Threshold	229		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	838.450012		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	28	28	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	179	179	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.52 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	41		
MtrVel_HwVelDiffAcc_Cnt_M_u16	31		
MtrVel_HwVel_HwRadpS_M_f32	5.55999994		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	125		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	169		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	12		
k_MtrVelCorrLim_Cnt_Str.Threshold	232		
k_MtrVelCorrLim_Cnt_Str.PStep	223		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	871		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVeIMRF_MtrRadpS_f32.value	950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	30	30	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	100	100	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	45		
MtrVel_HwVelDiffAcc_Cnt_M_u16	37		
MtrVel_HwVel_HwRadpS_M_f32	7		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	175.759995		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	13.75		
k_MtrVelCorrLim_Cnt_Str.Threshold	235		
k_MtrVelCorrLim_Cnt_Str.PStep	226		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	904		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-40.5499992		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1010		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	27	27	•
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	•
Rte Call Sa MtrVel NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	204	204	•





Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt MtrVel Per2 HwVelValid Cnt Igc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.54 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	49		
MtrVel_HwVelDiffAcc_Cnt_M_u16	43		
MtrVel_HwVel_HwRadpS_M_f32	4		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	225		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	15		
k_HwVelCorrLim_Cnt_Str.PStep	255		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	14		
k_MtrVelCorrLim_Cnt_Str.Threshold	238		
k_MtrVelCorrLim_Cnt_Str.PStep	229		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	937.450012		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	9		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1070		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	29	29	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.55 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	53		
MtrVel_HwVelDiffAcc_Cnt_M_u16	49		
MtrVel_HwVel_HwRadpS_M_f32	3.55999994		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	275		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	28		
k_HwVelCorrLim_Cnt_Str.PStep	50		
k_HwVelCorrLim_Cnt_Str.NStep	12		
k_HwVelCorrLim_HwRadpS_f32	15		
k_MtrVelCorrLim_Cnt_Str.Threshold	241		
k_MtrVelCorrLim_Cnt_Str.PStep	232		
k_MtrVelCorrLim_Cnt_Str.NStep	4		
k_MtrVelCorrLim_MtrRadpS_f32	970		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	10		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1130		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	49	49	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	37	37	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 2.56 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	57		
MtrVel_HwVelDiffAcc_Cnt_M_u16	55		
MtrVel_HwVel_HwRadpS_M_f32	2		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	325.670013		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	41		
k_HwVelCorrLim_Cnt_Str.PStep	16		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	16.8700008		
k_MtrVelCorrLim_Cnt_Str.Threshold	12		
k_MtrVelCorrLim_Cnt_Str.PStep	235		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	1003		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	11.4499998		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1190.87		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	49	49	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	55	55	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•



Test Step 2.57 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	61		
MtrVel_HwVelDiffAcc_Cnt_M_u16	61		
MtrVel_HwVel_HwRadpS_M_f32	1		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	375		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	54		
k_HwVelCorrLim_Cnt_Str.PStep	22		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	17		
k_MtrVelCorrLim_Cnt_Str.Threshold	18		
k_MtrVelCorrLim_Cnt_Str.PStep	238		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	1036.56006		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	45	45	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	41	41	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.58 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	65		
MtrVel_HwVelDiffAcc_Cnt_M_u16	67		
MtrVel_HwVel_HwRadpS_M_f32	15.4300003		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	425		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	67		
k_HwVelCorrLim_Cnt_Str.PStep	37		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	18		
k_MtrVelCorrLim_Cnt_Str.Threshold	66		
k_MtrVelCorrLim_Cnt_Str.PStep	241		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	1069		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	13		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1310		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtrl	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	53	53	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	57	57	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~		
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•		

# 

Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	10		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25		
MtrVel_HwVel_HwRadpS_M_f32	-32		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1000		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	116		
k_HwVelCorrLim_Cnt_Str.PStep	19		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	19		
k_MtrVelCorrLim_Cnt_Str.Threshold	2		
k_MtrVelCorrLim_Cnt_Str.PStep	77		
k_MtrVelCorrLim_Cnt_Str.NStep	1		
k_MtrVelCorrLim_MtrRadpS_f32	-1110		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-41		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwF	RadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_	MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	2	2	✓
MtrVel_HwVelDiffAcc_Cnt_M_u16	15	15	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	103		
MtrVel_HwVelDiffAcc_Cnt_M_u16	129		
MtrVel_HwVel_HwRadpS_M_f32	-16		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-680		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	220		
k_HwVelCorrLim_Cnt_Str.PStep	75		
k_HwVelCorrLim_Cnt_Str.NStep	18		
k_HwVelCorrLim_HwRadpS_f32	-24		
k_MtrVelCorrLim_Cnt_Str.Threshold	54		
k_MtrVelCorrLim_Cnt_Str.PStep	181		
k_MtrVelCorrLim_Cnt_Str.NStep	9		
k_MtrVelCorrLim_MtrRadpS_f32	170		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-710		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_Igc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	94	94	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	204	204	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>		
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~		
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~		

Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	77		
MtrVel_HwVelDiffAcc_Cnt_M_u16	103		
MtrVel_HwVel_HwRadpS_M_f32	-20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-760		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI		
k_HwVelCorrLim_Cnt_Str.Threshold	194		
k_HwVelCorrLim_Cnt_Str.PStep	61		
k_HwVelCorrLim_Cnt_Str.NStep	16		
k_HwVelCorrLim_HwRadpS_f32	31		
k_MtrVelCorrLim_Cnt_Str.Threshold	40		
k_MtrVelCorrLim_Cnt_Str.PStep	155		
k_MtrVelCorrLim_Cnt_Str.NStep	7		
k_MtrVelCorrLim_MtrRadpS_f32	-150		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	42		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-870		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt	_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVe	I_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVe	IMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	40	40	•
MtrVel_HwVelDiffAcc_Cnt_M_u16	164	164	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•
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MtrVel\_Per2

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	90		
MtrVel_HwVelDiffAcc_Cnt_M_u16	116		
MtrVel_HwVel_HwRadpS_M_f32	-18		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-720		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	207		
k_HwVelCorrLim_Cnt_Str.PStep	68		
k_HwVelCorrLim_Cnt_Str.NStep	17		
k_HwVelCorrLim_HwRadpS_f32	-25		
k_MtrVelCorrLim_Cnt_Str.Threshold	47		
k_MtrVelCorrLim_Cnt_Str.PStep	168		
k_MtrVelCorrLim_Cnt_Str.NStep	8		
k_MtrVelCorrLim_MtrRadpS_f32	10		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	0		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-790		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRad	oS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_Mtr	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	47	47	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	184	184	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>	



Test Step 3.5 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	12		
MtrVel_HwVelDiffAcc_Cnt_M_u16	38		
MtrVel_HwVel_HwRadpS_M_f32	-30		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-960		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	129		
k_HwVelCorrLim_Cnt_Str.PStep	26		
k_HwVelCorrLim_Cnt_Str.NStep	11		
k_HwVelCorrLim_HwRadpS_f32	21		
k_MtrVelCorrLim_Cnt_Str.Threshold	5		
k_MtrVelCorrLim_Cnt_Str.PStep	90		
k_MtrVelCorrLim_Cnt_Str.NStep	2		
k_MtrVelCorrLim_MtrRadpS_f32	-950		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-39		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1350		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	5	5	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	27	27	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	142		
MtrVel_HwVelDiffAcc_Cnt_M_u16	168		
MtrVel_HwVel_HwRadpS_M_f32	-10		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-560		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	250		
k_HwVelCorrLim_Cnt_Str.PStep	96		
k_HwVelCorrLim_Cnt_Str.NStep	0		
k_HwVelCorrLim_HwRadpS_f32	-21		
k_MtrVelCorrLim_Cnt_Str.Threshold	75		
k_MtrVelCorrLim_Cnt_Str.PStep	220		
k_MtrVelCorrLim_Cnt_Str.NStep	12		
k_MtrVelCorrLim_MtrRadpS_f32	650		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	130	130	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	250	250	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>



Test Step 3.7 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	116		
MtrVel_HwVelDiffAcc_Cnt_M_u16	142		
MtrVel_HwVel_HwRadpS_M_f32	-14		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-640		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	233		
k_HwVelCorrLim_Cnt_Str.PStep	82		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	-23		
k_MtrVelCorrLim_Cnt_Str.Threshold	61		
k_MtrVelCorrLim_Cnt_Str.PStep	194		
k_MtrVelCorrLim_Cnt_Str.NStep	10		
k_MtrVelCorrLim_MtrRadpS_f32	330		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-630		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	106	106	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	224	224	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	155		
MtrVel_HwVelDiffAcc_Cnt_M_u16	181		
MtrVel_HwVel_HwRadpS_M_f32	-8		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	251		
k_HwVelCorrLim_Cnt_Str.PStep	103		
k_HwVelCorrLim_Cnt_Str.NStep	10		
k_HwVelCorrLim_HwRadpS_f32	-20		
k_MtrVelCorrLim_Cnt_Str.Threshold	82		
k_MtrVelCorrLim_Cnt_Str.PStep	233		
k_MtrVelCorrLim_Cnt_Str.NStep	13		
k_MtrVelCorrLim_MtrRadpS_f32	810		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-28		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-390		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	82	82	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	251	251	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_Igc.value	0	0	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 3.9 (Repeat Count = 1)			✓	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	37	37		
MtrVel_HwVelDiffAcc_Cnt_M_u16	25			
MtrVel_HwVel_HwRadpS_M_f32	9			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	75			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	255			
k_HwVelCorrLim_Cnt_Str.PStep	154			
k_HwVelCorrLim_Cnt_Str.NStep	20			
k_HwVelCorrLim_HwRadpS_f32	11			
k_MtrVelCorrLim_Cnt_Str.Threshold	229			
k_MtrVelCorrLim_Cnt_Str.PStep	220			
k_MtrVelCorrLim_Cnt_Str.NStep	9			
k_MtrVelCorrLim_MtrRadpS_f32	838			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-20			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	890			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	28	28	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	179	179	<b>✓</b>	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.10 (Repeat Count = 1)			<b>∨</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	45		
MtrVel_HwVelDiffAcc_Cnt_M_u16	37		
MtrVel_HwVel_HwRadpS_M_f32	7		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	175		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	10		
k_HwVelCorrLim_Cnt_Str.PStep	0		
k_HwVelCorrLim_Cnt_Str.NStep	8		
k_HwVelCorrLim_HwRadpS_f32	13		
k_MtrVelCorrLim_Cnt_Str.Threshold	235		
k_MtrVelCorrLim_Cnt_Str.PStep	226		
k_MtrVelCorrLim_Cnt_Str.NStep	18		
k_MtrVelCorrLim_MtrRadpS_f32	904		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-40		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1010		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_0	Cnt_lgc	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHw	Vel_HwRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtr	VelMRF_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	27	27	-
MtrVel_HwVelDiffAcc_Cnt_M_u16	10	10	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>✓</b>
			•

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Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt MtrVel Per2 HwVelValid Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	•

Test Step 3.11 (Repeat Count = 1)			<b>~</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	41		
MtrVel_HwVelDiffAcc_Cnt_M_u16	31		
MtrVel_HwVel_HwRadpS_M_f32	5		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	125		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	100		
k_HwVelCorrLim_Cnt_Str.PStep	169		
k_HwVelCorrLim_Cnt_Str.NStep	4		
k_HwVelCorrLim_HwRadpS_f32	12		
k_MtrVelCorrLim_Cnt_Str.Threshold	232		
k_MtrVelCorrLim_Cnt_Str.PStep	223		
k_MtrVelCorrLim_Cnt_Str.NStep	11		
k_MtrVelCorrLim_MtrRadpS_f32	871		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	950		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lge		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_H	wRadpS_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMR	F_MtrRadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	30	30	<b>✓</b>
MtrVel_HwVelDiffAcc_Cnt_M_u16	100	100	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	<b>✓</b>





Test Step 3.12 (Repeat Count = 1)			V
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	61		
MtrVel_HwVelDiffAcc_Cnt_M_u16	61		
MtrVel_HwVel_HwRadpS_M_f32	1		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	375		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	54		
k_HwVelCorrLim_Cnt_Str.PStep	22		
k_HwVelCorrLim_Cnt_Str.NStep	20		
k_HwVelCorrLim_HwRadpS_f32	17		
k_MtrVelCorrLim_Cnt_Str.Threshold	18		
k_MtrVelCorrLim_Cnt_Str.PStep	238		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	1036		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	12		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	1250		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadps	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	45	45	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	41	41	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.13 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	0			
MtrVel_HwVelDiffAcc_Cnt_M_u16	175			
MtrVel_HwVel_HwRadpS_M_f32	12.3999996			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	100			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel			
k_HwVelCorrLim_Cnt_Str.Threshold	50			
k_HwVelCorrLim_Cnt_Str.PStep	5			
k_HwVelCorrLim_Cnt_Str.NStep	1			
k_HwVelCorrLim_HwRadpS_f32	-10			
k_MtrVelCorrLim_Cnt_Str.Threshold	152			
k_MtrVelCorrLim_Cnt_Str.PStep	16			
k_MtrVelCorrLim_Cnt_Str.NStep	1	1		
k_MtrVelCorrLim_MtrRadpS_f32	40			
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-8			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	410			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	16	16	<b>→</b>	
MtrVel_HwVelDiffAcc_Cnt_M_u16	50	50	-	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	-	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	•	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	0	0	-	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>



Test Step 3.14 (Repeat Count = 1)			✓
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	9		
MtrVel_HwVelDiffAcc_Cnt_M_u16	60		
MtrVel_HwVel_HwRadpS_M_f32	-37		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	1050		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	21		
k_HwVelCorrLim_Cnt_Str.PStep	49		
k_HwVelCorrLim_Cnt_Str.NStep	1		
k_HwVelCorrLim_HwRadpS_f32	0		
k_MtrVelCorrLim_Cnt_Str.Threshold	208		
k_MtrVelCorrLim_Cnt_Str.PStep	199		
k_MtrVelCorrLim_Cnt_Str.NStep	20		
k_MtrVelCorrLim_MtrRadpS_f32	607		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	30		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	470		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	0	0	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	21	21	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	•
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

Test Step 3.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
MtrVel_DiffAcc_Cnt_M_u16	212		
MtrVel_HwVelDiffAcc_Cnt_M_u16	16		
MtrVel_HwVel_HwRadpS_M_f32	20		
MtrVel_MtrVelMRF_MtrRadpS_M_f32	850		
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVel		
k_HwVelCorrLim_Cnt_Str.Threshold	212		
k_HwVelCorrLim_Cnt_Str.PStep	65		
k_HwVelCorrLim_Cnt_Str.NStep	19		
k_HwVelCorrLim_HwRadpS_f32	5		
k_MtrVelCorrLim_Cnt_Str.Threshold	196		
k_MtrVelCorrLim_Cnt_Str.PStep	187		
k_MtrVelCorrLim_Cnt_Str.NStep	16		
k_MtrVelCorrLim_MtrRadpS_f32	475		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	22		
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	230		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwRadp	S_f32	
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrF	RadpS_f32	
Name	Actual Value	Expected Value	Result
MtrVel_DiffAcc_Cnt_M_u16	196	196	~
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	<b>✓</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>~</b>
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_MtrVel_Per2_HwVelValid_Cnt_Igc.value	0	0	<b>✓</b>





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call MtrVel Per2 CP1 CheckpointReached	1	Rte Call MtrVel Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 3.16 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
MtrVel_DiffAcc_Cnt_M_u16	0			
MtrVel_HwVelDiffAcc_Cnt_M_u16	0			
MtrVel_HwVel_HwRadpS_M_f32	-42			
MtrVel_MtrVelMRF_MtrRadpS_M_f32	-1350			
Rte_Inst_Sa_MtrVel	tgt_Rte_Inst_Sa_MtrVeI			
k_HwVelCorrLim_Cnt_Str.Threshold	0			
k_HwVelCorrLim_Cnt_Str.PStep	0			
k_HwVelCorrLim_Cnt_Str.NStep	0			
k_HwVelCorrLim_HwRadpS_f32	-32			
k_MtrVelCorrLim_Cnt_Str.Threshold	0			
k_MtrVelCorrLim_Cnt_Str.PStep	0			
k_MtrVelCorrLim_Cnt_Str.NStep	0			
k_MtrVelCorrLim_MtrRadpS_f32	-1118	-1118		
tgt_MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32.value	-42			
tgt_MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32.value	-1350			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_HwVelValid_Cnt_lgc	tgt_MtrVel_Per2_HwVelValid_Cnt_lgc			
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagHwVel_HwRadpS_f32	tgt_MtrVel_Per2_SysCDiagHwVel_HwF	RadpS_f32		
tgt_Rte_Inst_Sa_MtrVel.MtrVel_Per2_SysCDiagMtrVelMRF_MtrRadpS_f32	tgt_MtrVel_Per2_SysCDiagMtrVelMRF	_MtrRadpS_f32		
Name	Actual Value	Expected Value	Result	
MtrVel_DiffAcc_Cnt_M_u16	0	0	~	
MtrVel_HwVelDiffAcc_Cnt_M_u16	0	0	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	204	204	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3	3	✓	
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
tgt_MtrVel_Per2_HwVelValid_Cnt_lgc.value	1	1	✓	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP0_CheckpointReached	1	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	Rte_Call_Sa_MtrVel_NxtrDiagMgr_GetNTCFailed	2	~
Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrVel_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	Rte_Call_MtrVel_Per2_CP1_CheckpointReached	1	~

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Project	MtrVel_Digi
Module	MtrVel_Digi
Test Object	MtrVel_Init

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrVel_Digi
Configuration File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrVel_Digi\src\Sa_MtrVel.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dstatic= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT) \MtrVel_Digi\utp\contract -I\$(PROJECTROOT)\MtrVel_Digi\utp\contract\Sa_MtrVel -I\$(PROJECTROOT)\MtrVel_Digi\include -I\$(Compiler Install Path)\include

Comments/Description	
Name	Text
Name  Module 'MtrVel_Digi'	Name of Tester:Sunita Kore Code File(s) Under Test:Sa_MtrVel.c Code File(s) Under Test:Sa_MtrVel.c Code File(s) Version:5 Module Design Document:MotorVelocity_MDD.doc Module Design Document Version:6 Data Dictionary Version:3 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):1394 Total CALS Used (Bytes):42 Total CALS Used (Bytes):54 Special Test Requirements: Test Date:12/26/2014 Comments: "Note1: Inline functions defined in ""Globalmacro.h"" are not unit tested.  Note2: In function ""MtrVel_Per1"", the values of variable ""MechMtrPos2Timestamp_USec_u32"" and ""MtrVel_PrevMtrPosSample Time2_uS_M_u32"" are taken slightly different from the actual range, to get value of ""DeltaTime2_uS_T_f32"" in the range of ""1800 - 2200"" as per the guidelines mentioned in MDD.  Note3: In function ""CalcCoarseVel"", the return value is going beyond given range and it's accepted because it is later limited to +/- 1350.  Note4: In function ""RegressionFit" , the path 'P5' i.e. ""(SumDeltaSqT_uSsq_T_f32 > FLT_EPSILON) = FALSE"' has been covered by not following the specific guidelines mentioned in the MDD(to calculate values of variable ""MtrVel3_TimeBuffer_uS_M_u16p0[4][8]"") and have been given value as '0' for all array elements.  Note5: ""CBD_Sandbox_dbg.map"" is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

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MtrVel\_Init



Attributes		
Name	Value	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\CBD_MtrVel_Digi\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	

Description



### Test Case 1: Boundary Check

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles: TS 1.1 6.00 Cycles VECTOR DESCRIPTION:

TS1.1 Output checked

Test Step 1.1 (Repeat Count = 1)			
Name	Actual Value	Expected Value	Result
MtrVel_OldPosBuf_Cnt_M_u08	1	1	~
MtrVel_OsBufSelect_Cnt_M_u08	0	0	~