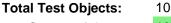


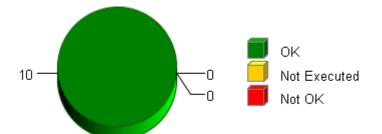
### **Summary**

## **Overall Test Object Results (including Coverage)**



Successful: 10
Failed: 0
Not Executed: 0

**Date:** 2014-09-04 **Time:** 15:57:41+0530



## **Selected Project Items**

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos Init1"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_Per1"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_Per2"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_Per3"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_Per4"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_CustClrTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_CustSetTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_NxtClearTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/AbsHwPos\_SCom\_NxtSetTrim"

Test Object "CBD\_UnitTest/AbsHwPos\_Tcl2IVd/TrimNotPerfDiag"

### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

### **Batch Operation Settings**

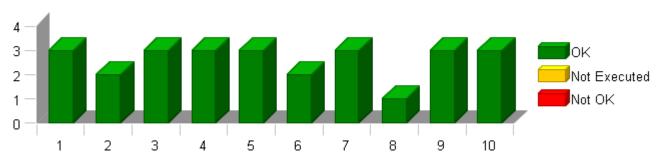
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

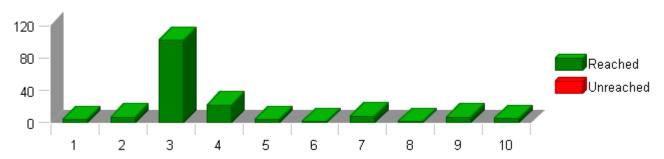
### Test Case Results for Each Test Object (without Coverage)





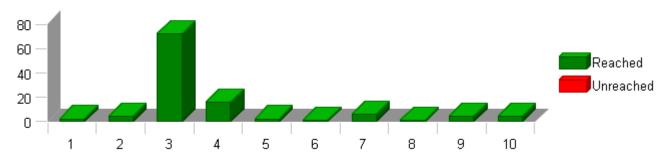
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

## Statement (C0) Coverage: Total Statements for Each Test Object



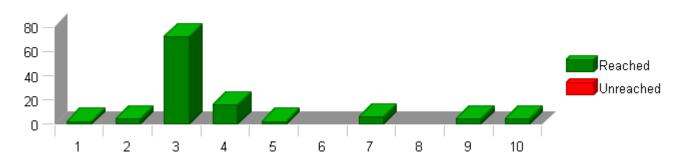
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

## **Decision Coverage: Total Decision Outcomes for Each Test Object**

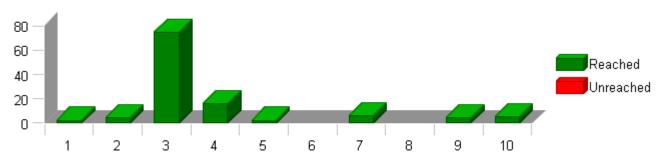


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



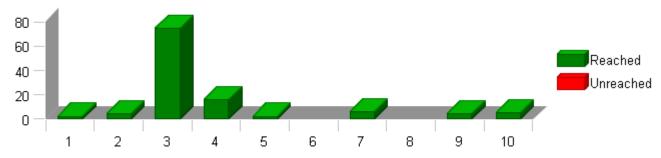
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### **TEST OVERVIEW REPORT**

2014-09-04, 15:57:41+0530

Razorcat

Project AbsHwPos\_Tcl2cVd

## **Test Object List**

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases Result
	AbsHwPos_Tcl2cVd	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
	AbsHwPos_Tcl2IVd	100 %	100 %	100 %	100 %	100 %	26 of 26 passed 💌
1	AbsHwPos_Init1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
2	AbsHwPos_Per1	100 %	100 %	100 %	100 %	100 %	2 of 2 passed
3	AbsHwPos Per2	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
4	AbsHwPos Per3	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
5	AbsHwPos Per4	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
6	AbsHwPos SCom CustClrTrim	100 %	100 %	-	-	-	2 of 2 passed
7	AbsHwPos SCom CustSetTrim	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
8	AbsHwPos SCom NxtClearTrim	100 %	100 %	-	-	-	1 of 1 passed
9	AbsHwPos SCom NxtSetTrim	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
10	<u>TrimNotPerfDiag</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed

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2014-09-04, 15:56:57+0530





 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 TrimNotPerfDiag

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

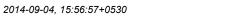
### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Approx AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes					
Name	Value				
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5				
Float Precision	9				
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj				
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src				
Library	libc.a				
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd				
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>				
Time Enabled	false				

TrimNotPerfDiag





Attributes					
Name	Value				
Time Unit	Cycles				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				



#### **Test Case 1: Metrics Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 740.00 Cycles TS1.2 1728.00 Cycles

Description Vector Description:

TS 1.1 Shortest Execution Path:

(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=False, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And\_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>True

TS 1.2 Longest Execution Path:

(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And \_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>False

Test Step 1.1 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~



#### **Test Case 2: Boundary Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 740.00 Cycles
TS2.2 1728.00 Cycles
TS2.3 739.00 Cycles
TS2.4 1731.00 Cycles
TS2.5 739.00 Cycles
TS2.6 1521.00 Cycles
TS2.7 739.00 Cycles
TS2.8 1731.00 Cycles
TS2.8 1731.00 Cycles
TS2.9 739.00 Cycles
TS2.10 1520.00 Cycles

#### Vector Description: Description

TS 2.1 All Min TS 2.2 All Max

TS 2.2 All Max
TS 2.3 ManufMode\_Cnt\_T\_enum=>Min
TS 2.4 ManufMode\_Cnt\_T\_enum=>Max
TS 2.5 ManufMode\_Cnt\_T\_enum=>Pos
TS 2.6 Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Min
TS 2.7 Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Max
TS 2.8 Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Pos
TS 2.9 k\_UseTurnsCntr\_Cnt\_lgc=>Min
TS 2.10 k\_UseTurnsCntr\_Cnt\_lgc=>Max

Test Step 2.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	*none*	<b>✓</b>

7	Test Step Call Trace			•		<b>✓</b>
4	ctual Function	Count	Expected Function	Count	Resu	ılt
*	none*	0	*** No Call Expected ***	0		<b>✓</b>

Test Step 2.2 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	2			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	1			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.3 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	





Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 2.4 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	2			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	1			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	1			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	_





Test Step 2.7 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2449		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1284		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6258		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<u> </u>





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

### Test Case 3: Path Test

✓

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 741.00 Cycles TS3.2 1727.00 Cycles TS3.3 1731.00 Cycles

Description

Vector Description:

TS 3.1 "(k\_UseTurnsCntr\_Cnt\_Igc == TRUE)=False, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And \_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>True"
TS 3.2 "(k\_UseTurnsCntr\_Cnt\_Igc == TRUE)=True, ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And \_ (Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <> D\_TRIMPERFORMED\_CNT\_U16))=>False"
TS 3.3 "(k\_UseTurnsCntr\_Cnt\_Igc == TRUE)=True,( (ManufMode\_Cnt\_T\_enum != ManufacturingMode) && (Rte\_Pim\_EOLVehCntrOffset()->EOLHwPosTrimPerformed\_Cnt\_u16 != D\_TRIMPERFORMED\_CNT\_U16))=>False"

Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
*none*	0	*** No Call Expected ***	0	~

Test Step 3.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	_

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

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TrimNotPerfDiag

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

AbsHwPos\_SCom\_CustClrTrim

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_CustClrTrim

### Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

### **Statistics**

Total Testcases	2	
Successful	2	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

AbsHwPos\_SCom\_CustClrTrim

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Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### **Test Case 1: Metrics Test**

AbsHwPos\_SCom\_CustClrTrim

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 792.00 Cycles TS1.2 1012.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

If ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And D\_TRIMPERFORMED\_CNT\_U16))=>False"

(Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <>

TS1.2 Longest Execution Path:

If ((ManufMode\_Cnt\_T\_enum <> ManufacturingMode) And D\_TRIMPERFORMED\_CNT\_U16))=>True"

(Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 <>

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	47		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 1.2 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	e_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~



# AbsHwPos\_SCom\_CustClrTrim

### Test Case 2: Boundary Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 792.00 Cycles TS2.2 1012.00 Cycles TS2.3 792.00 Cycles TS2.4 1012.00 Cycles TS2.5 792.00 Cycles

Description

Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ManufMode\_Cnt\_enum==>Min
TS2.4 ManufMode\_Cnt\_enum==>Max
TS2.5 ManufMode\_Cnt\_enum==>Pos

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos	s_ManufMode_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tgt Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_	ManufMode_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tat Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data

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Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1542		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Manu	fMode_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	47		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>
tat Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

AbsHwPos\_SCom\_CustClrTrim

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AbsHwPos\_Per3

 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per3

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false

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AbsHwPos\_Per3

Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### **Test Case 1: Metrics Test**

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 1029.00 Cycles TS1.2 1574.00 Cycles

Description

Vector Description:

TS1.1"Shortest Execution Path:
if (I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=>False"
TS1.2"Longest Execution Path:
if (I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=>True
if (I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=>True
(FALSE == AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc)=True
if (Abs\_f32\_m(I2CHwAbsPos\_HwDeg\_T\_f32 - NewRelHwPos\_HwDeg\_T\_f32) >= k\_HWAtoMtrADiffLimit\_HwDeg\_f32)=>False
(AbsHwPos\_HwAtoMtrAFItAcc\_Cnt\_M\_u16 == 0U)=True

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbs	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	<b>✓</b>
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	125		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	420		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	182		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	<b>✓</b>
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

## Test Case 2: Boundary Test

#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1029.00 Cycles
TS2.2 1585.00 Cycles
TS2.3 1575.00 Cycles
TS2.3 1575.00 Cycles
TS2.4 969.00 Cycles
TS2.5 1565.00 Cycles
TS2.6 1119.00 Cycles
TS2.7 1104.00 Cycles
TS2.7 1104.00 Cycles
TS2.9 1060.00 Cycles
TS2.10 1056.00 Cycles
TS2.11 1549.00 Cycles
TS2.11 1549.00 Cycles
TS2.12 1056.00 Cycles
TS2.13 1086.00 Cycles
TS2.14 1056.00 Cycles
TS2.15 1069.00 Cycles
TS2.15 1069.00 Cycles
TS2.15 1069.00 Cycles
TS2.16 1056.00 Cycles
TS2.17 1549.00 Cycles
TS2.18 1549.00 Cycles
TS2.19 1060.00 Cycles
TS2.19 1060.00 Cycles
TS2.19 1060.00 Cycles
TS2.20 1529.00 Cycles
TS2.21 1060.00 Cycles
TS2.22 1069.00 Cycles
TS2.23 1060.00 Cycles
TS2.25 1549.00 Cycles
TS2.26 1056.00 Cycles
TS2.27 1056.00 Cycles
TS2.28 1034.00 Cycles
TS2.28 1034.00 Cycles
TS2.29 1056.00 Cycles
TS2.31 949.00 Cycles
TS2.31 949.00 Cycles
TS2.31 949.00 Cycles
TS2.33 1056.00 Cycles
TS2.33 1549.00 Cycles
TS2.33 1549.00 Cycles
TS2.34 1069.00 Cycles
TS2.35 1549.00 Cycles

#### Description

Vector Description:

TS2.1All Min TS2.2All Max TS2.3I2CHwAbsPosValid Cnt lgc=>True TS2.4I2CHwAbsPosValid\_Cnt\_lgc=>False TS2.5I2CHwAbsPos\_HwDeg\_f32==>Min TS2.6I2CHwAbsPos\_HwDeg\_f32==>Max TS2.7I2CHwAbsPos\_HwDeg\_f32==>Zero TS2.8l2CHwAbsPos\_HwDeg\_f32==>Pos TS2.9l2CHwAbsPos\_HwDeg\_f32==>Neg TS2.10AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Min TS2.11AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Max TS2.12AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Zero TS2.13AbsHwPos\_RelHwPos\_HwDeg\_M\_f32==>Pos TS2.14AbsHwPos\_RelHwPos\_HwDeg\_M\_[32==>Neg TS2.15AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16==>Min TS2.16AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16==>Max TS2.17AbsHwPos\_HwAtoMtrAFltAcc\_Cnt\_M\_u16==>Pos TS2.18k\_HWAtoMtrADiffLimit\_HwDeg\_f32==>Min TS2.19k\_HWAtoMtrADiffLimit\_HwDeg\_f32==>Max TS2.20k\_HWAtoMtrADiffLimit\_HwDeg\_f32==>Pos TS2.21k\_HwAtoMtrAError\_str.Threshold==>Min TS2.22k\_HwAtoMtrAError\_str.Threshold==>Max TS2.23k\_HwAtoMtrAError\_str.Threshold==>Pos TS2.24k\_HwAtoMtrAError\_str.PStep==>Min TS2.25k\_HwAtoMtrAError\_str.PStep==>Max TS2.26k\_HwAtoMtrAError\_str.PStep==>Pos TS2.20K\_HWAtoMtrAError\_str.PStep==>Pos
TS2.27K\_HWAtoMtrAError\_str.NStep==>Min
TS2.28K\_HWAtoMtrAError\_str.NStep==>Max
TS2.29K\_HWAtoMtrAError\_str.NStep==>Pos
TS2.30AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc==>Min
TS2.31AbsHwPos\_MtrToHwAOfst\_HwDeg\_M\_f32==>Min
TS2.32AbsHwPos\_MtrbHwAOfst\_HwDeg\_M\_f32==>Min TS2.33AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Max TS2.34AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Pos TS2.35AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Zero TS2.36AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32==>Neg

Test Step 2.1 (Repeat Count = 1)		
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	

AbsHwPos\_Per3

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Name	Input Value		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	11026 ± 0.09	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	-

Test Step 2.3 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996	
AbsHwPos_RelHwPos_HwDeg_M_f32	420	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5	
k_HwAtoMtrAError_str.Threshold	470	
k_HwAtoMtrAError_str.PStep	124	
k_HwAtoMtrAError_str.NStep	182	
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1	





Name	Input Value		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

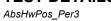
Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	148		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6214.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	-329.299988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	355.600006		
k_HwAtoMtrAError_str.Threshold	493		
k_HwAtoMtrAError_str.PStep	48		
k_HwAtoMtrAError_str.NStep	173		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-457		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	.bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	.bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6214.20996	-6214.20996 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	*none*	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	1718		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2553.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	238.600006		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	378.700012		
k_HwAtoMtrAError_str.Threshold	516		
k_HwAtoMtrAError_str.PStep	66		
k_HwAtoMtrAError_str.NStep	164		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAl	osPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAl	osPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516	516	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2553.20996	-2553.20996 ± 0.009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	194		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1107.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	147.899994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	10		
k_HwAtoMtrAError_str.NStep	155		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	39	39	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-752.099976	-752.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	217		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	57.2000008		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994		
k_HwAtoMtrAError_str.Threshold	562		
k_HwAtoMtrAError_str.PStep	77		
k_HwAtoMtrAError_str.NStep	146		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsF	PosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsF	Pos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	294	294	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	4768.79004 ± 0.009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	<b>✓</b>



Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	2840		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	8429.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	-33.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	448		
k_HwAtoMtrAError_str.Threshold	654		
k_HwAtoMtrAError_str.PStep	91		
k_HwAtoMtrAError_str.NStep	137		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	145.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	2703	2703	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-179.100006	-179.100006 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	263		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1245.22998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1000		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	471.100006		
k_HwAtoMtrAError_str.Threshold	677		
k_HwAtoMtrAError_str.PStep	105		
k_HwAtoMtrAError_str.NStep	128		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-558.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	135	135	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1245.22998	1245.22998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.10 (Repeat Count = 1)		
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	286	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-800.210022	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	494.200012	
k_HwAtoMtrAError_str.Threshold	7000	
k_HwAtoMtrAError_str.PStep	119	



AbsHwPos\_Per3

Name	Input Value		
k_HwAtoMtrAError_str.NStep	119		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	123.800003		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	167	167	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10248.7998	-10248.7998 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.11 (Repeat Count = 1)			<b>√</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	3709		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2143.19995		
AbsHwPos_RelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	517.299988		
k_HwAtoMtrAError_str.Threshold	125		
k_HwAtoMtrAError_str.PStep	133		
k_HwAtoMtrAError_str.NStep	110		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	756		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125	125	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2143.19995	-2143.19995 ± 0.009	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	_

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	332		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-3486.18994		
AbsHwPos_RelHwPos_HwDeg_M_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	540.400024		
k_HwAtoMtrAError_str.Threshold	148		
k_HwAtoMtrAError_str.PStep	147		
k_HwAtoMtrAError_str.NStep	101		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-465.5		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbs	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	231	231	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos MtrtoHwAOfst HwDeg M f32	465.5	465.5 ± 0.0009	•

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	355		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-4829.18018		
AbsHwPos_RelHwPos_HwDeg_M_f32	465.299988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	563.5		
k_HwAtoMtrAError_str.Threshold	1718		
k_HwAtoMtrAError_str.PStep	161		
k_HwAtoMtrAError_str.NStep	92		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	312.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbs	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	516	516	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-4829.18018	-4829.18018 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

Name	Input Value		
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	378		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6172.16992		
AbsHwPos_RelHwPos_HwDeg_M_f32	-88.9899979		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	586.599976		
k_HwAtoMtrAError_str.Threshold	194		
k_HwAtoMtrAError_str.PStep	175		
k_HwAtoMtrAError_str.NStep	83		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-469.399994		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	295	295	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	380.410004	380.410004 ± 0.0009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•



Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7515.16016		
AbsHwPos_RelHwPos_HwDeg_M_f32	101.400002		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	212.399994		
k_HwAtoMtrAError_str.Threshold	217		
k_HwAtoMtrAError_str.PStep	189		
k_HwAtoMtrAError_str.NStep	74		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	626.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	189	189	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7515.16016	-7515.16016 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.16 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-8858.15039		
AbsHwPos_RelHwPos_HwDeg_M_f32	-457		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	62.7999992		
k_HwAtoMtrAError_str.Threshold	2840		
k_HwAtoMtrAError_str.PStep	203		
k_HwAtoMtrAError_str.NStep	65		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-782.799988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65470	65470	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	325.799988	325.799988 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.17 (Repeat Count = 1)		•
Name	Input Value	
AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16	4475	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10201.1396	
AbsHwPos_RelHwPos_HwDeg_M_f32	123.800003	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	655.900024	
k_HwAtoMtrAError_str.Threshold	263	





Name	Input Value		
k HwAtoMtrAError str.PStep	217		
_ ,			
k_HwAtoMtrAError_str.NStep	105		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	692.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	263	263	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10201.1396	-10201.1396 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)			<u> </u>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	470		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	700.22998		
AbsHwPos_RelHwPos_HwDeg_M_f32	756		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	286		
k_HwAtoMtrAError_str.PStep	231		
k_HwAtoMtrAError_str.NStep	119		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-601.400024		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAl	osPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAl	osPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	286	286	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	700.22998	700.22998 ± 0.0009	·
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	·
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•			
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•			

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	493		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1897.31995		
AbsHwPos_RelHwPos_HwDeg_M_f32	-465.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	3709		
k_HwAtoMtrAError_str.PStep	245		
k_HwAtoMtrAError_str.NStep	133		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	510.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	osValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	os_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	360	360	•

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AbsHwPos\_Per3

Name	Actual Value	Expected Value	Result
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-976.200012	-976.200012 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	516		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	3094.40991		
AbsHwPos_RelHwPos_HwDeg_M_f32	312.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	894.01001		
k_HwAtoMtrAError_str.Threshold	332		
k_HwAtoMtrAError_str.PStep	236		
k_HwAtoMtrAError_str.NStep	147		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	420		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	332	332	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	3094.40991	3094.40991 ± 0.009	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•		
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~		

Test Step 2.21 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	5398		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4291.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	564		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	227		
k_HwAtoMtrAError_str.NStep	161		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-329.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwI	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	5237	5237	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	798.699951	798.700012 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>		

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	562		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	5488.58984		
AbsHwPos_RelHwPos_HwDeg_M_f32	1016.09998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1021.70001		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	218		
k_HwAtoMtrAError_str.NStep	175		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-238.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwI	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	780	780	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	5488.58984	5488.58984 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Test Step 2.23 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	585		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	6685.68018		
AbsHwPos_RelHwPos_HwDeg_M_f32	-782.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	179.399994		
k_HwAtoMtrAError_str.Threshold	378		
k_HwAtoMtrAError_str.PStep	182		
k_HwAtoMtrAError_str.NStep	189		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	147.899994		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsP	PosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsP	os_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	396	396	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-930.699951	-930.700012 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~			
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•			



Test Step 2.24 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	108		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	7882.77002		
AbsHwPos_RelHwPos_HwDeg_M_f32	692.099976		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	137.100006		
k_HwAtoMtrAError_str.Threshold	489		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	203		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	600		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	92.0999756	92.0999985 ± 0.00009	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6317		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	9079.86035		
AbsHwPos_RelHwPos_HwDeg_M_f32	756		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	694.799988		
k_HwAtoMtrAError_str.Threshold	646		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	217		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-33.5		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	646	646	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	9079.86035	9079.86035 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.26 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	654
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	10276.9502
AbsHwPos_RelHwPos_HwDeg_M_f32	819.900024
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HWAtoMtrADiffLimit_HwDeg_f32	123





Name	Input Value		
k_HwAtoMtrAError_str.Threshold	8974		
k_HwAtoMtrAError_str.PStep	191		
k_HwAtoMtrAError_str.NStep	199		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-897.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	455	455	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1717	1717 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Name	Input Value		
	677		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16			
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-5478.31982		
AbsHwPos_RelHwPos_HwDeg_M_f32	-883.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1310.19995		
k_HwAtoMtrAError_str.Threshold	424		
k_HwAtoMtrAError_str.PStep	173		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	465.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	677	677	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1349.09998	-1349.09998 ± 0.009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	<b>✓</b>

Test Step Call Trace   ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	7000		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1		
AbsHwPos_RelHwPos_HwDeg_M_f32	947.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1367.90002		
k_HwAtoMtrAError_str.Threshold	4477		
k_HwAtoMtrAError_str.PStep	164		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-88.9899979		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6745	6745	•
AbsHwPos MtrToHwAOfstLearnt Cnt M Iqc	1	1	<b>✓</b>

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AbsHwPos\_Per3

Name	Actual Value	Expected Value	Result
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1	1 ± 0.000009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.29 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	723		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1457.31995		
AbsHwPos_RelHwPos_HwDeg_M_f32	-1011.59998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1425.59998		
k_HwAtoMtrAError_str.Threshold	1470		
k_HwAtoMtrAError_str.PStep	155		
k_HwAtoMtrAError_str.NStep	66		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	75.75		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	657	657	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1087.34998	-1087.34998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>~</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.30 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1245.59998		
AbsHwPos_RelHwPos_HwDeg_M_f32	-465.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	378.700012		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	203		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	692.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1245.59998	-1245.59998 ± 0.009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~



Test Step 2.31 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	5398		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1247		
AbsHwPos_RelHwPos_HwDeg_M_f32	312.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	493		
k_HwAtoMtrAError_str.PStep	32		
k_HwAtoMtrAError_str.NStep	217		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-601.400024		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	<b>~</b>
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	<b>~</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1247	1247 ± 0.009	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.32 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	562		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994		
k_HwAtoMtrAError_str.Threshold	516		
k_HwAtoMtrAError_str.PStep	145		
k_HwAtoMtrAError_str.NStep	231		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	510.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516	516	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

Test Step 2.33 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	585
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026
AbsHwPos_RelHwPos_HwDeg_M_f32	1016.09998
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos





Name	Input Value		
k_HWAtoMtrADiffLimit_HwDeg_f32	448		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	99		
k_HwAtoMtrAError_str.NStep	245		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	420		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	340	340	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	596.099976	596.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.34 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	108		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	2145.66992		
AbsHwPos_RelHwPos_HwDeg_M_f32	-782.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	471.100006		
k_HwAtoMtrAError_str.Threshold	562		
k_HwAtoMtrAError_str.PStep	87		
k_HwAtoMtrAError_str.NStep	236		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-329.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	leg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	195	195	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	2145.66992	2145.66992 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	<b>✓</b>

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6317		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	0		
AbsHwPos_RelHwPos_HwDeg_M_f32	692.099976		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	494.200012		
k_HwAtoMtrAError_str.Threshold	654		
k_HwAtoMtrAError_str.PStep	128		
k_HwAtoMtrAError_str.NStep	227		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-238.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsI	PosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsI	Pos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	654	654	•

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AbsHwPos\_Per3

Name	Actual Value	Expected Value	Result
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	0	0 ± 0.000009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	654		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7894.12012		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	517.299988		
k_HwAtoMtrAError_str.Threshold	677		
k_HwAtoMtrAError_str.PStep	214		
k_HwAtoMtrAError_str.NStep	91		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	100		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	563	563	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	369.399994	369.399994 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

#### Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)  $\,$ 

CPU Cycles:

TS3.1 1029.00 Cycles TS3.2 1585.00 Cycles TS3.3 1575.00 Cycles TS3.4 1119.00 Cycles TS3.5 1104.00 Cycles

Description Vector Description:

TS3.1(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=False
TS3.2"(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE)=True
(FALSE == AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc)=False
(Abs\_f32\_m(I2CHwAbsPos\_HwDeg\_T\_f32 - NewRelHwPos\_HwDeg\_T\_f32) >= k\_HWAtoMtrADiffLimit\_HwDeg\_f32)=True
(DiagFailed\_m(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16, k\_HwAtoMtrAError\_str) == TRUE)=True"
TS3.3"(FALSE == AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc)=True
(Abs\_f32\_m(I2CHwAbsPos\_HwDeg\_T\_f32 - NewRelHwPos\_HwDeg\_T\_f32) >= k\_HWAtoMtrADiffLimit\_HwDeg\_f32)=False
(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16 == 0U)=True"
TS3.4(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16 == 0U)=False
TS3.5(DiagFailed\_m(AbsHwPos\_HwAtoMtrAFitAcc\_Cnt\_M\_u16, k\_HwAtoMtrAError\_str) == TRUE)=False

Test Step 3.1 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

AbsHwPos\_Per3

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Name Input Value Rte\_Inst\_Ap\_AbsHwPos  $tgt\_Rte\_Inst\_Ap\_AbsHwPos$ k\_HWAtoMtrADiffLimit\_HwDeg\_f32 k\_HwAtoMtrAError\_str.Threshold 0  $k\_HwAtoMtrAError\_str.PStep$ 0 k\_HwAtoMtrAError\_str.NStep 0 tgt\_AbsHwPos\_Per3\_I2CHwAbsPosValid\_Cnt\_lgc.value 0 tgt\_AbsHwPos\_Per3\_I2CHwAbsPos\_HwDeg\_f32.value -900 tgt\_AbsHwPos\_Per3\_I2CHwAbsPosValid\_Cnt\_Igc  $tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per3\_I2CHwAbsPosValid\_Cnt\_Igc$  $tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per3\_I2CHwAbsPos\_HwDeg\_f32$ tgt\_AbsHwPos\_Per3\_I2CHwAbsPos\_HwDeg\_f32 **Expected Value** AbsHwPos\_HwAtoMtrAFltAcc\_Cnt\_M\_u16 n n AbsHwPos\_MtrToHwAOfstLearnt\_Cnt\_M\_lgc 0 -11026 ± 0.09 AbsHwPos\_MtrtoHwAOfst\_HwDeg\_M\_f32 -11026  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 0 0 Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) 0 n

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

0

0

Test Step 3.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwD	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535	65535	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	11026 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	~



Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	420		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	182		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	194		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1107.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	147.899994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	10		
k_HwAtoMtrAError_str.NStep	155		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid	I_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_Hw	Deg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	39	39	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-752.099976	-752.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 3.5 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	217	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	
AbsHwPos_RelHwPos_HwDeg_M_f32	57.2000008	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994	
k_HwAtoMtrAError_str.Threshold	562	

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AbsHwPos\_Per3

Name	Input Value		
k_HwAtoMtrAError_str.PStep	77		
k_HwAtoMtrAError_str.NStep	146		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	294	294	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	4768.79004 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

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AbsHwPos\_Per4

 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per4

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>
Time Enabled	false

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AbsHwPos\_Per4



Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 1354.00 Cycles TS1.2 1341.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $If (Abs\_m(AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32) > k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32) => True"$ 

TS1.2 Longest Execution Path:

 $If (Abs\_m(AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32) > k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32) => False "$ 

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>~</b>

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	IIntDiagMaxRackLimit_HwDeg_f32 647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	-



#### **Test Case 2: Boundary Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1354.00 Cycles TS2.2 1346.00 Cycles TS2.3 1344.00 Cycles TS2.4 1344.00 Cycles TS2.5 1339.00 Cycles TS2.6 1344.00 Cycles TS2.7 1338.00 Cycles TS2.8 1344.00 Cycles TS2.8 1344.00 Cycles TS2.9 1344.00 Cycles TS2.10 1336.00 Cycles

#### Vector Description: Description

TS2.1 All Min TS2.2 All Max

TS2.3 AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32=>Min IS2.3 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.4 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.5 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.6 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.7 AbSHWPOS\_AlignedHWPOS\_HWDeg\_M\_f32=>Min
TS2.8 k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32=>Min
TS2.9 k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32=>Min
TS2.10 k\_KinmIntDiagMaxRackLimit\_HwDeg\_f32=>Min

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Name  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value	Expected Value	Result
		•	Result

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.3 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	1478		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rta Call An AhsHwPos NytrDiagMar SatNTCStatus(Status Cnt T anum)	1	1	_





Test Step Call Trace					<b>✓</b>
	Actual Function	Count	Expected Function	Count	Result
	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	99.0100021		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	•

Test Step 2.5 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	365.02		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	358.01001		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	_	

Test Step 2.7 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-254.12		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	2769		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~





Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>~</b>

Test Step 2.8 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	6585			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	•

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-7895.00977		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	100		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	487.24		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	<b>✓</b>



#### Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 1354.00 Cycles TS3.2 1341.00 Cycles

**Description** Vector Description:

 $\label{eq:total_$ 

Test Step 3.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	mIntDiagMaxRackLimit_HwDeg_f32 0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 3.2 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32 647			
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	~

AbsHwPos\_SCom\_NxtClearTrim

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Project AbsHwPos\_Tcl2cVd

Module AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_NxtClearTrim

#### Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

#### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester:Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

AbsHwPos\_SCom\_NxtClearTrim

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Attributes	
Name	Value
Workspage File	D.\ Symergy Work Area\CDD AbguyDog Tg12gVd\UnitTegtEny\ gonfig\UnE TMSE70 DEDIG WCD



**Test Case 1: Boundary Test** 

AbsHwPos\_SCom\_NxtClearTrim

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 381.00 Cycles TS1.2 381.00 Cycles TS1.3 381.00 Cycles TS1.4 381.00 Cycles TS1.5 381.00 Cycles

**Description** Vector Description:

TS1.1 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Min
TS1.2 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Max
TS1.3 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Mid
TS1.4 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Pos
TS1.5 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32=Neg

Test Step 1.1 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.3 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.4 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_VehCntrOffset_HwDeg_M_f32	12564.01
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos

AbsHwPos\_SCom\_NxtClearTrim

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Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte Call Ap AbsHwPos EOLVehCntrOffset WriteBlock	1	Rte Call Ap AbsHwPos EOLVehCntrOffset WriteBlock	1	_

Test Step 1.5 (Repeat Count = 1)			<b>✓</b>	
Name	Input Value			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1523.02			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

AbsHwPos\_SCom\_CustSetTrim

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Project AbsHwPos\_Tcl2cVd

Module AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_CustSetTrim

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>
Time Enabled	false

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Attributes		
Name	Value	
Time Unit	Cycles	
Timer Prescale	0	
Timer Resolution		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



# **Test Case 1: Metrics Test**

AbsHwPos\_SCom\_CustSetTrim

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 2163.00 Cycles TS1.2 2974.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $\label{eq:cont_u08} \begin{tabular}{ll} if (TurnsCntrValidiy_Cnt_T_u08 == D_TCVVALID_CNT_U08) => False \\ if (AbsHwPos_VehCntrValid_Cnt_M_lgc == FALSE) => True" \\ \end{tabular}$ 

TS1.2 Longest Execution Path:

 $if (Abs\_f32\_m(AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => True \\ if (AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc == FALSE) => False"$ 

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMod	e_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr\	/alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.0112959 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32

 $tgt\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16$ 

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468.0112959 ± 0.0009

Result

Name -	A -41 \( \sigma \)	Expected Value
Abshweos_Sconi_CustSetTilli		

1

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus

468.011292

43690

#### **Test Case 2: Boundary Test** Specification "Performance Metrics (With "None" Instrumentation and WithPS Environment"

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus

TS2.1 2163.00 Cycles 2387.00 Cycles 2753.00 Cycles 2387.00 Cycles TS2.2 TS2.3 TS2.4 TS2.5 2387.00 Cycles 2166.00 Cycles 2385.00 Cycles 2166.00 Cycles 2972.00 Cycles 2166.00 Cycles 2385.00 Cycles 2387.00 Cycles 2166.00 Cycles 2166.00 Cycles 2187.00 Cycles TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11 TS2.13 TS2.14 TS2.15 TS2.16 TS2.17 2385.00 Cycles 2758.00 Cycles 2380.00 Cycles 2165.00 Cycles TS2.18 TS2.19 TS2.20 2965.00 Cycles 2167.00 Cycles 2385.00 Cycles TS2.21 2758.00 Cycles TS2.22 2380.00 Cycles TS2.23 2165.00 Cycles

#### Description Vector Description:

TS2.1 All Min

TS2.2 All Max

TS2.3 ManufMode\_Cnt\_enum==>Min TS2.4 ManufMode\_Cnt\_enum==>Max TS2.5 ManufMode\_Cnt\_enum==>Pos

TS2.6 TurnsCntrValidity\_Cnt\_enum==>Pos
TS2.6 TurnsCntrValidity\_Cnt\_enum==>Min
TS2.7 TurnsCntrValidity\_Cnt\_enum==>Pos
TS2.8 TurnsCntrValidity\_Cnt\_enum==>Pos
TS2.9 AlignedCumMtrPos\_HwDeg\_M\_f32==>Min
TS2.10 AlignedCumMtrPos\_HwDeg\_M\_f32==>Pos
TS2.11 AlignedCumMtrPos\_HwDeg\_M\_f32==>Pos

IS2.11 AlignedCumMtrPos\_HwDeg\_M\_f32==>Pos TS2.12 AlignedCumMtrPos\_HwDeg\_M\_f32==>Zero TS2.13 AlignedCumMtrPos\_HwDeg\_M\_f32==>Neg TS2.14 k\_VehCntrOffValidLimit\_HwDeg\_f32==>Min TS2.15 k\_VehCntrOffValidLimit\_HwDeg\_f32==>Max TS2.16 k\_VehCntrOffSet\_HwDeg\_M\_f32==>Min TS2.17 VehCntrOffSet\_HwDeg\_M\_f32==>Min TS2.18 vehCntrOffSet\_HwDeg\_M\_f32==>Max

TS2.18 VehCntrOffset\_HwDeg\_M\_f32==>Max TS2.19 VehCntrOffset\_HwDeg\_M\_f32==>Pos

TS2.20 VehCntrOffset HwDeg M f32==>Zero
TS2.21 VehCntrOffset HwDeg M f32==>Neg
TS2.22 VehCntrValid Cnt\_M lgc==>Min

TS2.23 VehCntrValid\_Cnt\_M\_lgc==>Max

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos	_ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos	_TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	•

AbsHwPos\_SCom\_CustSetTrim

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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Man	ufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turn	sCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Ī	1	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	•

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2783.013736		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.475961		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	987.4038704		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~

AbsHwPos\_SCom\_CustSetTrim

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tat Pim FOI VehCntrOffset FOI HwPosTrimPerformed Cnt u16	0	0	_

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	•
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1298.706022		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1377.424898		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1209.602194		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	55		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos	_ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos	_TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	16		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

AbsHwPos\_SCom\_CustSetTrim

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Name	Actual Value	Expected Value	Result
tat Pim FOI VehCntrOffset FOI HwPosTrimPerformed Cnt u16	0	0	<b>y</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2075.327965		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-64.2319839		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMo	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCnt	rValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1386.308063		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	358		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2223.71791		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1012.772977		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1031.592314		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	673		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



Total Oders Cell Trees				
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	•
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>V</b>

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2778.262678		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	876.8872956		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1053.975584		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	988		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				
Count	Expected Function	Count	Result	
1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•	
1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
1	TrimNotPerfDiag	1	~	
1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
	Count  1  1  1  1  1  1	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 TrimNotPerfDiag	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 1 TrimNotPerfDiag 1	

Test Step 2.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1324.017901		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMo	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCnt	Validity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	409.8052046		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1303		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	123		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>

Test Step 2.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-524.0916791		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	771.3155495		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1618		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

「est Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.11 (Repeat Count = 1)			
Name	Input Value		_
	· ·		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.0112959 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.0112959 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1140.874864		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	988.8657172		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2248		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	64		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				
Count	Expected Function	Count	Result	
1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•	
1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
1	TrimNotPerfDiag	1	~	
1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
	Count  1  1  1  1  1  1	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 TrimNotPerfDiag	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 1 TrimNotPerfDiag 1	

T (0) 0 (0) (0)			
Test Step 2.13 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-619.01		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-452.81907		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMod	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr	Validity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	205.2756743		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2563		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	33		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	166.198763		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	404.8247678		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2878		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	185		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

			✓
Count	Expected Function	Count	Result
1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
1	TrimNotPerfDiag	1	~
1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
	Count  1  1  1  1  1  1	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 TrimNotPerfDiag	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 1 TrimNotPerfDiag 1

Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-612.0464776		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	870.1470994		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	nufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tur	nsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3193		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-612.046448	-612.0464776 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-612.046448	-612.0464776 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>

Test Step 2.16 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3508		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	78		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.17 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	907.8806047		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVal	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	575.5253226		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3823		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	50		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>

Test Step 2.18 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1563.765938		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	178.0013868		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4138		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.19 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2688.781728		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1172.12		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1164.878309		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4453		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>	

Test Step 2.20 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-941.7753812		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMo	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr	Validity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1122.769698		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4768		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	

Test Step 2.21 (Repeat Count = 1)			J.
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	933.2282791		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-578.13		
AbsHwPos VehCntrValid Cnt M lqc	0		
Rte Inst Ap AbsHwPos	tgt Rte Inst Ap AbsHwPos		
	· -	Cat anym data	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	ilidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1599.216564		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5083		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	933.228271	933.2282791 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	933.228271	933.2282791 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>	

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5398		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	14		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace					
Count	Expected Function	Count	Result		
1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~		
1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•		
1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~		
1	TrimNotPerfDiag	1	~		
1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~		
	Count  1  1  1  1  1  1	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 TrimNotPerfDiag	1 Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum 1 1 Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 1 1 Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1 1 TrimNotPerfDiag 1		

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	804.1952507		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1562.118949		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMod	le_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr	Validity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	904.2132521		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5713		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	225		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	<b>✓</b>



Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	•			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	<b>✓</b>			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~			
TrimNotPerfDiag	1	TrimNotPerfDiag	1	-			

# Test Case 3: Path Test Specification "Performance Metrics (With "None" Instrumentation and WithPS Environment" CPU Cycles: TS3.1 2163.00 Cycles TS3.2 2974.00 Cycles TS3.3 2973.00 Cycles TS3.3 2973.00 Cycles TS3.4 2980.00 Cycles Description Vector Description:

 $TS3.1 \quad \text{if (TurnsCntrValidity\_Cnt\_T\_u08} == D\_TCVVALID\_CNT\_U08) => False \\ TS3.2 \quad \text{"if (TurnsCntrValidity\_Cnt\_T\_u08} == D\_TCVVALID\_CNT\_U08) => True \\ \text{if (Abs\_f32\_m(AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False \\ \text{if (AbsHwPos\_VehCntrValid\_Cnt\_M\_igc} == FALSE] => True'' \\ TS3.3 \quad \text{"if (Abs_f32\_m(AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => True \\ \text{if (AbsHwPos\_VehCntrValid\_Cnt\_M\_igc} == FALSE) => False'' \\ TS3.4 \quad \text{"((ManufMode\_Cnt\_T\_enum != ManufacturingMode)} = True&& \\ \text{(Rte\_Pim\_EOLVehCntrOffset()} >> EOLHwPosTrimPerformed\_Cnt\_u16 != D\_TRIMPERFORMED\_CNT\_U16)} ) = False'' \\ \end{aligned}$ 

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Man	ufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turr	sCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~			
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~			

Test Step 3.2 (Repeat Count = 1)		<b>~</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2783.013736	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.475961	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	987.4038704	

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Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	•

Test Step Call Trace		<b>✓</b>		
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	468.0112959		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.0678937		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Mar	ufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turn	nsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.491538		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.0112959 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.0112959 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	-

Test Step 3.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	153.14	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.475961	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	425.12	
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	

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Name	Input Value		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	153.139999	153.14 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	153.139999	153.14 ± 0.0009	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per2

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3
Successful	3
Failed	0
Not Executed	0

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Library	libc.a	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>	
Time Enabled	false	

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AbsHwPos\_Per2



Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_TcI2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

k\_HwPosOutputLPFError\_HwDeg\_f32)=False



```
Test Case 1: Metrics Test
Specification
           Performance Metrics (With "None" Instrumentation and WithPS Environment) \,
           TS1.1 1463.00 Cycles
TS1.2 1942.00 Cycles
Description
           Vector Description:
```

Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	9556.42969
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	204.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75999999
AbsHwPos HwPosSource Cnt M u16	1348
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	442.940002
AbsHwPos RelHwPos HwDeg M f32	442.940002
AbsHwPos SrlComHwPosStatus Cnt M u16	26817
AbsHwPos SrlComHwPos HwDeg M f32	1700.22998
AbsHwPos TargetHwAuthority Uls M f32	1
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012
AbsHwPos VehCntrOffset HwDeg M f32	8178.93018
AbsHwPos VehCntrOfstLearn Cnt M lgc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorSta	•
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
KIC_INSC_AP_ABSHWI 03  KIC_INSC_AP_ABSHWI 03  KIC_INSC_AP_ABSHWI 03	0
k HwPosOutputLPFError HwDeg f32	674,299988
k I2CHwAuthority Uls f32	0.75
k MaxSensorlessAuthority Uls f32	0.839999974
k MaxVehCntrOffDiff HwDeg f32	822.799988
	0.75
MinSensorlessAuthority_UIs_f32     TurnerControl at the ority_UIs_f32	0.75
C_TurnsCntrAuthority_Uls_f32	0.75
k_UseTurnsCntr_Cnt_lgc	-
k_VehCntrOffValidLimit_HwDeg_f32	674.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.valu	
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.83999974
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_E	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosR	0 =
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_er	num target_AbsHwPos_Per2_ManufMode_Cnt_enum

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1700.22998	1700.22998 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001	2325.1001 ± 0.009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	-

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	3787.70996
AbsHwPos HandwheelAuthority Uls M f32	0.280000001
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1112.18005
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.280000001
AbsHwPos HwPosSource Cnt M u16	296
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID
AbsHwPos PrevHandwheelPosition HwDeq M f32	1207.09998
AbsHwPos RelHwPos HwDeg M f32	1207.09998
AbsHwPos SrlComHwPosStatus Cnt M u16	23721
AbsHwPos SrlComHwPos HwDeg M f32	1232.09998
AbsHwPos TargetHwAuthority UIs M f32	0.280000001
AbsHwPos TempHwPos HwDeg M f32	8009.91016
AbsHwPos VehCntrOffset HwDeg M f32	-1132.57996
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.270000011
k_HwPosOutputLPFError_HwDeg_f32	1179.19995
k_I2CHwAuthority_Uls_f32	0.270000011
k_MaxSensorlessAuthority_Uls_f32	0.340000004
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.270000011
k_TurnsCntrAuthority_Uls_f32	0.270000011
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	822.799988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012

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Name	Input Value			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347			
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	rf_( target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32$	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f$	32 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	2 target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	<b>✓</b>	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	<b>✓</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1232.09998	1232.09998 ± 0.1	<b>✓</b>	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>	
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	<b>✓</b>	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	•	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1232.09998	1232.09998 ± 0.1	•	
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

AbsHwPos\_Per2

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Test Case 2: Boundary Test

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AbsHwPos\_Per2



#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1467.00 Cycles TS2.2 2917.00 Cycles TS2.3 1877.00 Cycles TS2.4 1933.00 Cycles TS2.5 1838.00 Cycles TS2.6 1921.00 Cycles TS2.7 1341.00 Cycles TS2.8 1885.00 Cycles TS2.9 1360.00 Cycles TS2.9 1360.00 Cycles TS2.10 1862.00 Cycles TS2.11 1360.00 Cycles TS2.12 1417.00 Cycles TS2.13 1352.00 Cycles TS2.14 1868.00 Cycles TS2.15 1347.00 Cycles TS2.16 1895.00 Cycles TS2.17 1347.00 Cycles TS2.18 1902.00 Cycles TS2.19 1450.00 Cycles TS2.20 1904.00 Cycles TS2.21 1388.00 Cycles TS2.22 1428.00 Cycles TS2.23 1362.00 Cycles TS2.24 1428.00 Cycles TS2.24 1428.00 Cycles TS2.25 1341.00 Cycles TS2.26 1428.00 Cycles TS2.27 1908.00 Cycles TS2.28 1889.00 Cycles TS2.29 1844.00 Cycles TS2.30 1889.00 Cycles TS2.31 1361.00 Cycles TS2.32 1902.00 Cycles TS2.32 1902.00 Cycles
TS2.33 1373.00 Cycles
TS2.35 1428.00 Cycles
TS2.35 1428.00 Cycles
TS2.36 1861.00 Cycles
TS2.37 1844.00 Cycles
TS2.38 1904.00 Cycles
TS2.39 1824.00 Cycles
TS2.40 1904.00 Cycles
TS2.41 1844.00 Cycles
TS2.42 1861.00 Cycles
TS2.42 1861.00 Cycles
TS2.43 2347.00 Cycles
TS2.44 2812.00 Cycles
TS2.45 1373.00 Cycles
TS2.46 2763.00 Cycles
TS2.48 1907.00 Cycles
TS2.49 1373.00 Cycles
TS2.49 1373.00 Cycles
TS2.51 1352.00 Cycles
TS2.51 1470.00 Cycles
TS2.53 1352.00 Cycles TS2.33 1373.00 Cycles TS2.52 1417.00 Cycles TS2.53 1352.00 Cycles TS2.54 1868.00 Cycles TS2.55 1815.00 Cycles TS2.56 1840.00 Cycles TS2.57 1352.00 Cycles TS2.57 1352.00 Cycles TS2.59 1352.00 Cycles TS2.60 1840.00 Cycles TS2.61 1426.00 Cycles TS2.62 2799.00 Cycles TS2.63 2358.00 Cycles TS2.63 2358.00 Cycles TS2.64 1922.00 Cycles TS2.65 1943.00 Cycles TS2.66 1384.00 Cycles TS2.66 1386.00 Cycles TS2.68 1361.00 Cycles TS2.69 1904.00 Cycles TS2.70 1373.00 Cycles TS2.71 1904.00 Cycles TS2.72 1361.00 Cycles TS2.72 1361.00 Cycles TS2.73 1904.00 Cycles TS2.74 1904.00 Cycles TS2.75 1373.00 Cycles TS2.76 1904.00 Cycles TS2.77 1361.00 Cycles TS2.78 1889.00 Cycles TS2.79 1361.00 Cycles TS2.80 1904.00 Cycles TS2.81 1361.00 Cycles TS2.82 1889.00 Cycles TS2.83 1361.00 Cycles TS2.84 1904.00 Cycles TS2.85 1373.00 Cycles TS2.86 1904.00 Cycles TS2.87 1361.00 Cycles TS2.88 1904.00 Cycles TS2.89 1373.00 Cycles TS2.90 1904.00 Cycles TS2.90 1904.00 Cycles TS2.91 1368.00 Cycles TS2.92 1889.00 Cycles TS2.93 1338.00 Cycles TS2.94 1889.00 Cycles TS2.95 1303.00 Cycles TS2.96 1840.00 Cycles TS2.97 1359.00 Cycles TS2.98 1885.00 Cycles TS2.99 1352.00 Cycles TS2.99 1352.00 Cycles TS2.100 1840.00 Cycles TS2.101 2310.00 Cycles TS2.102 2740.00 Cycles TS2.103 2287.00 Cycles TS2.104 2753.00 Cycles TS2.105 2288.00 Cycles TS2.106 1838.00 Cycles TS2.107 1465.00 Cycles

TS2.108 1945.00 Cycles TS2.109 1815.00 Cycles

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AbsHwPos\_Per2



TS2.110 1898.00 Cycles
TS2.111 1386.00 Cycles
TS2.112 1874.00 Cycles
TS2.113 1862.00 Cycles
TS2.114 1926.00 Cycles
TS2.115 1379.00 Cycles
TS2.116 1839.00 Cycles
TS2.117 1451.00 Cycles
TS2.118 2812.00 Cycles
TS2.119 2358.00 Cycles
TS2.121 2306.00 Cycles
TS2.121 2306.00 Cycles
TS2.122 2358.00 Cycles
TS2.123 2792.00 Cycles



#### **Description** Vector Description:

TS2.1All Min TS2.2All Max TS2.3TurnsCntrValidity\_Cnt\_u08=>Min TS2.4TurnsCntrValidity\_Cnt\_u08=>Max TS2.5TurnsCntrValidity\_Cnt\_u08=>Pos TS2.6ManufMode\_Cnt\_enum=>Min TS2.7ManufMode\_Cnt\_enum=>Max TS2.8ManufMode\_Cnt\_enum=>Pos TS2.9DiagStatusHwPosReducedPerf\_Cnt\_lgc=>Min TS2.10DiagStatusHwPosReducedPerf\_Cnt\_lgc=>Max TS2.11l2CHwAbsPosValid\_Cnt\_lgc=>Min IS2.11/I2CHwAbsPosValid\_Cnt\_lgc=>Mln
TS2.12/CHwAbsPosValid\_Cnt\_lgc=>Max
TS2.13SensorlessAuthority\_Uls\_f32=>Min
TS2.14SensorlessAuthority\_Uls\_f32=>Max
TS2.15SensorlessAuthority\_Uls\_f32=>Max
TS2.15SensorlessHwPos\_HwDeg\_f32=>Min
TS2.16SensorlessHwPos\_HwDeg\_f32=>Min
TS2.18SensorlessHwPos\_HwDeg\_f32=>Zero TS2.19SensorlessHwPos\_HwDeg\_f32=>Pos TS2.20SensorlessHwPos\_HwDeg\_f32=>Neg TS2.21I2CHwAbsPos\_HwDeg\_f32=>Min TS2.22I2CHwAbsPos\_HwDeg\_f32=>Max TS2.23I2CHwAbsPos\_HwDeg\_f32=>Zero TS2.24I2CHwAbsPos\_HwDeg\_f32=>Pos TS2.25I2CHwAbsPos\_HwDeg\_f32=>Neg TS2.26k\_UseTurnsCntr\_Cnt\_lgc=>Min TS2.27k\_UseTurnsCntr\_Cnt\_lgc=>Max TS2.28k\_MinSensorlessAuthority\_Uls\_f32=>Min TS2.29k\_MinSensorlessAuthority\_Uls\_132=>Msx TS2.30k\_MinSensorlessAuthority\_Uls\_132=>Pos TS2.31k\_MaxSensorlessAuthority\_Uls\_132=>Min TS2.32k\_MaxSensorlessAuthority\_Uls\_132=>Max TS2.32k\_MaxSensorlessAuthority\_Uls\_132=>Pos TS2.33k\_MaxSensorlessAuthority\_Uls\_132=>Pos TS2.34k\_VehCntrOffValidLimit\_HwDeg\_132=>Min TS2.35k\_VehCntrOffValidLimit\_HwDeg\_132=>Min TS2.35k\_VehCntrOffVali TS2.35k\_VehCntrOffValidLimit\_HwDeg\_f32=>Max
TS2.36k\_VehCntrOffValidLimit\_HwDeg\_f32=>Pos
TS2.37Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32=>Min TS2.38Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_Hwbeg\_f32=>Max TS2.39Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_Hwbeg\_f32=>Zero TS2.40Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_Hwbeg\_f32=>Pos TS2.41Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32=>Neg TS2.42k\_MaxVehCntrOffDiff\_HwDeg\_f32=>Min TS2.43k\_MaxVehCntrOffDiff\_HwDeg\_f32=>Max TS2.44k\_MaxVehCntrOffDiff\_HwDeg\_f32=>Pos TS2.45k\_HwPosOutputLPFError\_HwDeg\_f32=>Min TS2.46k\_HwPosOutputLPFError\_HwDeg\_f32=>Max TS2.47k\_HwPosOutputLPFError\_HwDeg\_f32=>Pos TS2.48k\_HwPosAuthorityStep\_Uls\_f32=>Min TS2.49k\_HwPosAuthorityStep\_Uls\_f32=>Max TS2.50k\_HwPosAuthorityStep\_Uls\_f32=>Pos TS2.51k\_I2CHwAuthority\_UIs\_f32=>Min TS2.52k\_I2CHwAuthority\_UIs\_f32=>Max TS2.53k\_I2CHwAuthority\_UIs\_f32=>Pos TS2.54k\_TurnsCntrAuthority\_Uls\_f32=>Min TS2.55k\_TurnsCntrAuthority\_Uls\_f32=>Max TS2.56k\_TurnsCntrAuthority\_Uls\_f32=>Pos TS2.57Rte Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Min TS2.58Rte Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Max TS2.59Rte\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16=>Pos TS2.60AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Min TS2.61AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Max TS2.62AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Zero TS2.63AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Pos TS2.64AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Neg TS2.65AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32=>Min TS2.66AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32=>Max TS2.67AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32=>Pos TS2.68AbsHwPos\_HwPosSource\_Cnt\_M\_u16=>Min TS2.69AbsHwPos\_HwPosSource\_Cnt\_M\_u16=>Max TS2.70AbsHwPos\_HwPosSource\_Cnt\_M\_u16=>Pos
TS2.71AbsHwPos\_HwPosState\_Cnt\_M\_enum=>HWPOS\_STATE\_SENSORVALID
TS2.72AbsHwPos\_HwPosState\_Cnt\_M\_enum=>HWPOS\_STATE\_INPUTINVALID TS2.73AbsHwPos HwPosState Cnt M enum=>HWPOS STATE INIT TS2.74AbsHwPos HwPosState Cnt M enum=>HWPOS STATE SENSORINVALID TS2.75AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32=>Min TS2.76AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32=>Max TS2.77AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32=>Zero TS2.78AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32=>Pos TS2.79AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32=>Neg TS2.80AbsHwPos\_RelHwPos\_HwDeg\_M\_f32=>Min TS2.81AbsHwPos\_RelHwPos\_HwDeg\_M\_f32=>Max TS2.82AbsHwPos\_RelHwPos\_HwDeg\_M\_f32=>Zero TS2.83AbsHwPos\_RelHwPos\_HwDeg\_M\_f32=>Pos TS2.83AbsHwPos\_RelHwPos\_HwDeg\_M\_f32=>Pos TS2.84AbsHwPos\_RelHwPos\_HwDeg\_M\_f32=>Neg TS2.85AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32=>Min TS2.86AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32=>Max TS2.87AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32=>Zero TS2.88AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32=>Pos TS2.89AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32=>Neg TS2.90AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16=>Min TS2.91AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16=>Max TS2.92AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16=>Pos TS2.93AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32=>Min TS2.94AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32=>Max



```
TS2.95AbsHwPos_TargetHwAuthority_UIs_M_f32=>Pos
TS2.96AbsHwPos_TempHwPos_HwDeg_M_f32=>Min
TS2.97AbsHwPos_TempHwPos_HwDeg_M_f32=>Max
TS2.98AbsHwPos_TempHwPos_HwDeg_M_f32=>Zero
TS2.99AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Neg
TS2.101AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Min
TS2.102AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Min
TS2.102AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Pos
TS2.103AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Pos
TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Pos
TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.105AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc=>Min
TS2.107AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc=>Min
TS2.107AbsHwPos_VehCntrValid_Cnt_M_lgc=>Min
TS2.109AbsHwPos_VehCntrValid_Cnt_M_lgc=>Max
TS2.110AbsHwPos_HandwheelPositionLPF_M_str.SV=>Min
TS2.111AbsHwPos_HandwheelPositionLPF_M_str.SV=>Max
TS2.112AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.114AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.115AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.116AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Min
TS2.116AbsHwPos_HandwheelPositionLPF_M_str.K=>Pos
TS2.118Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_OK
TS2.119Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_INTEGRITY_FAILED
TS2.122Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_INTEGRITY_FAILED
TS2.123Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_INTEGRITY_FAILED
TS2.123Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_INTEGRITY_FAILED
```

Test Step 2.1 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0
AbsHwPos_HwPosSource_Cnt_M_u16	1
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k HwPosAuthorityStep Uls f32	0
k HwPosOutputLPFError HwDeg f32	0
	0
k MaxSensorlessAuthority Uls f32	0
k MaxVehCntrOffDiff HwDeg f32	0
k MinSensorlessAuthority Uls f32	0
k_TurnsCntrAuthority_Uls_f32	0
k_UseTurnsCntr_Cnt_Igc	0
	0
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900
	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValidity Cnt u08



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725	-11725 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725	-11725 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)	v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1
AbsHwPos_HwPosSource_Cnt_M_u16	65534
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725
AbsHwPos_ReIHwPos_HwDeg_M_f32	10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	1
AbsHwPos_TempHwPos_HwDeg_M_f32	11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	1
k_HwPosOutputLPFError_HwDeg_f32	1620
k_I2CHwAuthority_Uls_f32	1
k_MaxSensorlessAuthority_Uls_f32	1
k_MaxVehCntrOffDiff_HwDeg_f32	1600
k_MinSensorlessAuthority_Uls_f32	1
k_TurnsCntrAuthority_UIs_f32	1
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1600
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsAbsHwPosReducedPerf\_InstAp\_AbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsA$	( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc



Name	Input value
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntro

nput Value
arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08
arget_Pim_EOLVehCntrOffset

Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         10125         10125 ± 0.09           AbsHwPos_HandwheelAuthority_Uls_M_f32         0         0 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         11725         11725 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         65534         65534           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         11725         11725 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         65535         65535           AbsHwPos_SrlComHwPos_HwDeg_M_f32         0         0 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0         0 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         11725         11725 ± 0.1	Result
AbsHwPos_HandwheelAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       11725       11725 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       65534       65534         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       11725       11725 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004	\rightarrow \right
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       11725       11725 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       65534       65534         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       11725       11725 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004	\rightarrow \right
AbsHwPos_HwPosSource_Cnt_M_u16       65534       65534         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       11725       11725 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         11725         11725 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         65535         65535           AbsHwPos_SrlComHwPos_HwDeg_M_f32         0         0 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0         0 ± 0.004	<b>*</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       11725       11725 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004	
AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004	~
AbsHwPos_TargetHwAuthority_UIs_M_f32 $0 \pm 0.004$	<b>✓</b>
	~
AbsHwPos_TempHwPos_HwDeg_M_f32 11725 11725 11725 ± 0.1	~
	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32         65535         65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc 1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc 1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 504 504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) 0 0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 0 0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value 0 0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value 1600 1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value 65534 65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 65535 65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value         0         0 ± 0.1	~
$target\_Pim\_EOLVehCntrOffset\_HwDeg\_f32 \\ 65535 \\ 65535 \\ 65535$	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)	<b>→</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	128.470001
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	584.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	88
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1487
AbsHwPos_RelHwPos_HwDeg_M_f32	1487
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21849
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.100000001
k_HwPosOutputLPFError_HwDeg_f32	407
k_I2CHwAuthority_UIs_f32	0.100000001
k_MaxSensorlessAuthority_Uls_f32	0.100000001
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.100000001
k_TurnsCntrAuthority_UIs_f32	0.100000001
k_UseTurnsCntr_Cnt_lgc	1

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Name	Input Value		
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	100.099998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	90.3000031		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	123		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	s 1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	erf_( target_AbsHwPos_Per2_DiagStatus	sHwPosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_Handwhee	elAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_t	f32 target_AbsHwPos_Per2_Handwhee	elPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSo	urce_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbs	sPosValid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbs	sPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMod	de_Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_Sensorles	sAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	32 target_AbsHwPos_Per2_Sensorles	sHwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	6 target_AbsHwPos_Per2_SrlComHv	vPosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHv	vPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntr	Validity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1487	1487 ± 0.009	✓
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	806.663025	806.663025 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	806.663025	806.663025 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012	9026.62012 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5	1328.5 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	806.663025	806.663025 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>V</b>

65534

1600

90.3000031

65534

1600 ± 0.1

90.3000031 ± 0.00009

Test Step 2.4 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	269.209991	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	605.179993	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003	
AbsHwPos_HwPosSource_Cnt_M_u16	96	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1507.09998	
AbsHwPos_RelHwPos_HwDeg_M_f32	1507.09998	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21921	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value\\ target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

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AbsHwPos_Per2		Table 1	orcat
Name	Input Value		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.20000003		
k_HwPosOutputLPFError_HwDeg_f32	436.700012		
k_I2CHwAuthority_Uls_f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.109999999		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.200000003		
k_TurnsCntrAuthority_UIs_f32	0.20000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.109999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	120.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	247		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf		psReducedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_(		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1507.09998	1507.09998 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	524.650024	524.650024 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	524.650024	524.650024 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	8920.66016 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	1358.69995 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	
<del></del>			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>~</b>

524.650024

65534

65534

1600

120.5

524.650024 ± 0.1

65534

65534

1600 ± 0.1

120.5 ± 0.0009

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value



Test Step 2.5 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-409.950012		
AbsHwPos HandwheelAuthority Uls M f32	0.300000012		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	625.460022		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.300000012		
AbsHwPos HwPosSource Cnt M u16	104		
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	1527.19995		
AbsHwPos RelHwPos HwDeg M f32	1527.19995		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21993		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.30000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.300000012		
k_HwPosOutputLPFError_HwDeg_f32	466.399994		
k I2CHwAuthority Uls f32	0.30000012		
k_MaxSensorlessAuthority_Uls_f32	0.119999997		
k MaxVehCntrOffDiff HwDeg f32	169.399994		
k MinSensorlessAuthority Uls f32	0.300000012		
k TurnsCntrAuthority Uls f32	0.300000012		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	110		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	371		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf		PosReducedPerf Cnt Ido	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3:			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_	_ · · · · ·	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPos\	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Ci	<del></del>	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuth	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_		
talget_Nte_ilist_Ap_Abst WF0s.Abst WF0s_Fet2_5ftCoffit WF0s_ftWDeg_i52	target_Abstrive 05_Fet2_SitContrive 05_	_i iwbeg_i32	
target Dto Inst An AbeHwDos AbeHwDos Dor? TurnsCntrValidity Cnt u08	target AbeHwDoe Der2 TurneCntr\/alid	ty Cpt u08	
	target_AbsHwPos_Per2_TurnsCntrValid	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		Per
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name	target_Pim_EOLVehCntrOffset  Actual Value	Expected Value	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995	<b>Expected Value</b> 1527.19995 ± 0.009	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0	Expected Value 1527.19995 ± 0.009 0 ± 0.004	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105	Expected Value 1527.19995 ± 0.009 0 ± 0.004 3378.58105 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534	Expected Value 1527.19995 ± 0.009 0 ± 0.004 3378.58105 ± 0.1 65534	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uis_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uis_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uis_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_igc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504	Resi
Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1 0 1600	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004  1600 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_earn_Cnt_M_lgc  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value  target_AbsHwPos_Per2_HmPosSource_Cnt_u16.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1 0 1600 65534	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004  1600 ± 0.1  65534	Res
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  1527.19995 0 3378.58105 65534 HWPOS_STATE_SENSORVALID 3378.58105 65534 8814.7002 0 9802.53027 1388.90002 0 0 504 1 1 0 1600	Expected Value  1527.19995 ± 0.009  0 ± 0.004  3378.58105 ± 0.1  65534  HWPOS_STATE_SENSORVALID  3378.58105 ± 0.1  65534  8814.7002 ± 0.1  0 ± 0.004  9802.53027 ± 0.1  1388.90002 ± 0.009  0  504  1  1  0 ± 0.004  1600 ± 0.1	Res

1600

1600 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997	150.699997 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	550.690002		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.400000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	645.73999		
AbsHwPos HandwheelPositionLPF M str.K UIs f32	0.40000006		
AbsHwPos HwPosSource Cnt M u16	112		
AbsHwPos_HwPosState_Cnt_M_enum	5		
AbsHwPos PrevHandwheelPosition HwDeg M f32	157.300003		
AbsHwPos RelHwPos HwDeg M f32	157.300003		
AbsHwPos SrlComHwPosStatus Cnt M u16	22065		
AbsHwPos SrlComHwPos HwDeg M f32	-8708.74023		
AbsHwPos TargetHwAuthority UIs M f32	0.400000006		
AbsHwPos TempHwPos HwDeg M f32	3224.75		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1419.09998		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
HwPosAuthorityStep Uls f32	0.400000006		
HwPosOutputLPFError HwDeg f32	496.100006		
C_I2CHwAuthority_Uls_f32	0.40000006		
MaxSensorlessAuthority Uls f32	0.129999995		
	199.100006		
MinSensorlessAuthority Uls f32	0.40000006		
TurnsCntrAuthority Uls f32	0.400000006		
c_useTurnsCntr_Cnt_lgc	1		
VehCntrOffValidLimit HwDeg f32	139.699997		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	160.399994		
arget AbsHwPos Per2 ManufMode Cnt enum.value	0		
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.129999995		
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	160.399994		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	10		
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	180.899994		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	495		
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		teducedPerf Cnt lac	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatu	·	
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPosStatu target_AbsHwPos_Per2_SrlComHwPos_Hw		
arget_Rte_inst_Ap_AbshwPos.AbshwPos_Per2_SnCoffinwPos_nwbeg_is2 arget_Rte_Inst_Ap_AbshwPos.AbshwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity (		
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_rumsChitvalidity_Chit_u06 arget_Rte_inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset		JIIuo0	
	target_Pim_EOLVehCntrOffset	Form a stand Walter	
Name	Actual Value	Expected Value	Res
AbsHwPos AlianedHwPos HwDea M f32	157.300003	157.300003 ± 0.0009	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	157.300003	157.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1677.34399	1677.34399 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1677.34399	1677.34399 ± 0.1	<b>~</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~

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Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1419.09998	1419.09998 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.7 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	691.429993
AbsHwPos HandwheelAuthority Uls M f32	0.5
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-666.02002
AbsHwPos HandwheelPositionLPF M str.K UIs f32	0.5
AbsHwPos HwPosSource Cnt M u16	120
AbsHwPos HwPosState Cnt M enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	567.400024
AbsHwPos_RelHwPos_HwDeg_M_f32	567.400024
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027
AbsHwPos TargetHwAuthority Uls M f32	0.5
AbsHwPos TempHwPos HwDeg M f32	120.199997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k HwPosAuthorityStep UIs f32	0.5
k HwPosOutputLPFError HwDeg f32	525.799988
k I2CHwAuthority Uls f32	0.5
k MaxSensorlessAuthority Uls f32	0.140000001
k_MaxVehCntrOffDiff_HwDeg_f32	228.800003
k_MinSensorlessAuthority_Uls_f32	0.5
k_TurnsCntrAuthority_Uls_f32	0.5
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	169.399994
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.140000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	180.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	14
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	619
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_ManufMode\_Cnt\_enum$	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32

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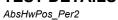




Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	567.400024	567.400024 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997	120.199997 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	120	120	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997	120.199997 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137	22137	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>~</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	120.199997	120.199997 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	120	120	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	22137	22137	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006	211.100006 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-832.169983
AbsHwPos HandwheelAuthority Uls M f32	0.60000024
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	686,299988
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.60000024
AbsHwPos HwPosSource Cnt M u16	128
AbsHwPos HwPosState Cnt M enum	7
AbsHwPos PrevHandwheelPosition HwDeg M f32	1587.5
AbsHwPos RelHwPos HwDeg M f32	1587.5
AbsHwPos SrlComHwPosStatus Cnt M u16	22209
AbsHwPos SrlComHwPos HwDeg M f32	8496.82031
AbsHwPos TargetHwAuthority Uls M f32	0.60000024
AbsHwPos TempHwPos HwDeq M f32	140.300003
AbsHwPos VehCntrOffset HwDeg M f32	-960.900024
AbsHwPos VehCntrOfstLearn Cnt M Igc	1
AbsHwPos VehCntrValid Cnt M Igc	1
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0.60000024
k HwPosOutputLPFError HwDeg f32	555.5
k I2CHwAuthority Uls f32	0.60000024
k MaxSensorlessAuthority Uls f32	0.150000006
k MaxVehCntrOffDiff HwDeq f32	258.5
k MinSensorlessAuthority Uls f32	0.600000024
k TurnsCntrAuthority Uls f32	0.60000024
k UseTurnsCntr Cnt Igc	1
k VehCntrOffValidLimit HwDeg f32	199.100006
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006
target AbsHwPos Per2 ManufMode Cnt enum.value	2
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.150000006
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	200.600006
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	20
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	241.300003
angot, mi_cot.ononeonoc.cotvononeonoc_nwbog_loz	2





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	743		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3$	32 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16000000000000000000000000000000000000$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1587.5	1587.5 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	358.699982	358.700012 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	358.699982	358.700012 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-960.900024	-960.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	358.699982	358.700012 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>V</b>

Test Step 2.9 (Repeat Count = 1)	✓ ·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	972.909973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	706.580017
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	136
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1000.29999
AbsHwPos_RelHwPos_HwDeg_M_f32	-1000.29999
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22281
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	585.200012
k_I2CHwAuthority_Uls_f32	0.69999988





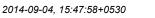
Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.159999996		
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.69999988		
k_TurnsCntrAuthority_UIs_f32	0.69999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.159999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	26		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	867		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	ıt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	324.253998	324.253998 ± 0.1	_

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Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.253998	324.253998 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.253998	324.253998 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	324.253998	324.253998 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5	271.5 ± 0.0009	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 2.10 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1113.65002	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-726.859985	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.800000012	
AbsHwPos_HwPosSource_Cnt_M_u16	144	
AbsHwPos_HwPosState_Cnt_M_enum	9	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-980.599976	
AbsHwPos_RelHwPos_HwDeg_M_f32	-980.599976	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22353	

AbsHwPos\_Per2





Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8284.88965		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.800000012	0.800000012	
AbsHwPos_TempHwPos_HwDeg_M_f32	180.5		
AbsHwPos VehCntrOffset HwDeg M f32	603.700012		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.800000012		
k_HwPosOutputLPFError_HwDeg_f32	614.900024		
<ci2chwauthority_uls_f32< td=""><td>0.800000012</td><td></td><td></td></ci2chwauthority_uls_f32<>	0.800000012		
MaxSensorlessAuthority Uls f32	0.170000002		
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994		
k_MinSensorlessAuthority_Uls_f32	0.800000012		
k_TurnsCntrAuthority_Uls_f32	0.800000012		
k UseTurnsCntr Cnt lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	258.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	240.800003		
target AbsHwPos Per2 ManufMode Cnt enum.value	1		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.170000002		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	240.800003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	32		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	301.700012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	991		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		ssReducedPerf Cnt Idc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cni		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SensorlessHwPostarget_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	y_Cnt_uoo	
		Function Value	Dagui
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	'
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-0.971984863	-0.972000003 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	1
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	,
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-0.971984863	-0.972000003 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	180.5	180.5 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	603.700012	603.700012 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	Rte Call AbsHwPos Per2 CP1 ChecknointReached	1	-	

-0.971984863

504

0

0

65534

65535

301.700012

0

0

0

0 ± 0.004

65534

65535

0 ± 0.1

-0.972000003 ± 0.1

301.700012 ± 0.0009

504

AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value



Test Step 2.11 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1254.39001		
AbsHwPos HandwheelAuthority Uls M f32	0.899999976		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	747.140015		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.899999976		
AbsHwPos_HwPosSource_Cnt_M_u16	152		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-960.900024		
AbsHwPos_RelHwPos_HwDeg_M_f32	-960.900024		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22425		
AbsHwPos SrlComHwPos HwDeg M f32	8178.93018		
AbsHwPos TargetHwAuthority Uls M f32	0.899999976		
AbsHwPos TempHwPos HwDeg M f32	200.600006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	633.900024		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehi	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.899999976		
k_HwPosOutputLPFError_HwDeg_f32	644.599976		
k_I2CHwAuthority_Uls_f32	0.89999976		
k_MaxSensorlessAuthority_Uls_f32	0.180000007		
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006		
k_MinSensorlessAuthority_Uls_f32	0.89999976		
k_TurnsCntrAuthority_Uls_f32	0.89999976		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	288.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.180000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	38		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	331.899994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1115		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	( target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositi	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	ılid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-960.900024	-960.900024 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	255.253998	255.253998 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	255.253998	255.253998 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8178.93018	8178.93018 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	200.600006	200.600006 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	633.900024	633.900024 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
		0 ± 0.004	
	0		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	-		
	255.253998 65534	255.253998 ± 0.1 65534	





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	331.899994	331.899994 ± 0.0009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	1395.13		
AbsHwPos HandwheelAuthority Uls M f32	0.109999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	767.419983		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999		
AbsHwPos HwPosSource Cnt M u16	160		
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_Provided Registron LivePos_M_622	HWPOS_STATE_INIT 1547.30005		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32			
AbsHwPos_RelHwPos_HwDeg_M_f32	1547.30005		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22497		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8072.97021		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	220.699997		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.100000001		
C_HwPosOutputLPFError_HwDeg_f32	674.299988		
c_I2CHwAuthority_Uls_f32	0.100000001		
<pre>&lt;_MaxSensorlessAuthority_Uls_f32</pre>	0.189999998		
c_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
_MinSensorlessAuthority_Uls_f32	0.100000001		
C_TurnsCntrAuthority_Uls_f32	0.100000001		
<_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	317.899994		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	281		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	44		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	362.100006		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1239		
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPer		ReducedPart Cnt Inc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthor		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
arget_Rte_inst_Ap_AbshwPos.AbshwPos_Pet2_HandwheeiPosition_hwbeg_ts arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Pet2_HwPosSource_Cnt_u16			
	target_AbsHwPos_Per2_HwPosSource_Cr	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_Valid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	713.913757	713.913818 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	713.913757	713.913818 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281	281 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.10000001	0.100000001 ± 0.004	<b>✓</b>





Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	281	281 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976	664.099976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	713.913757	713.913818 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	281	281 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos HandwheelAuthority Uls M f32	0.119999997
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos HwPosSource Cnt M u16	168
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos SrlComHwPos HwDeg M f32	7967.00977
AbsHwPos TargetHwAuthority Uls M f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k MaxSensorlessAuthority Uls f32	0.20000003
k MaxVehCntrOffDiff HwDeg f32	407
k MinSensorlessAuthority Uls f32	0.109999999
k TurnsCntrAuthority Uls f32	0.109999999
k UseTurnsCntr Cnt Igc	0
k VehCntrOffValidLimit HwDeg f32	347.600006
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target AbsHwPos Per2 ManufMode Cnt enum.value	1
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	301.100006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	392.299988
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1363
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	1
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	<b>✓</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	722.072021	722.072021 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>~</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	<b>✓</b>	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003	240.800003 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>~</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999791	0.00999999978 ± 0.004	•	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	722.072021	722.072021 ± 0.1	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	168	168	•	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	•	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.14 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1676.60999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995
AbsHwPos_HwPosSource_Cnt_M_u16	176
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.119999997
k_HwPosOutputLPFError_HwDeg_f32	733.700012
k_I2CHwAuthority_Uls_f32	0.119999997
k_MaxSensorlessAuthority_Uls_f32	0.209999993
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012
k_MinSensorlessAuthority_Uls_f32	0.119999997
k_TurnsCntrAuthority_Uls_f32	0.119999997
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	377.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487

AbsHwPos\_Per2



Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.15 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.319999993

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Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	407		
k_MinSensorlessAuthority_Uls_f32	0.109999999		
k_TurnsCntrAuthority_UIs_f32	0.109999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos HandwheelAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 ChecknointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Nome	Input Value	
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1958.08997	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	848.539978	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006	
AbsHwPos_HwPosSource_Cnt_M_u16	192	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	126.699997	
AbsHwPos_RelHwPos_HwDeg_M_f32	126.699997	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22785	
AbsHwPos SrlComHwPos HwDeg M f32	269.209991	

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Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.140000001		
k HwPosOutputLPFError HwDeg f32	793.099976		
k_I2CHwAuthority_Uls_f32	0.140000001		
k_MaxSensorlessAuthority_Uls_f32	0.230000004		
k_MaxVehCntrOffDiff_HwDeg_f32	496.100006		
k_MinSensorlessAuthority_Uls_f32	0.140000001		
k_TurnsCntrAuthority_UIs_f32	0.140000001		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	436.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1735		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f ( target AbsHwPos Per2 DiagStatusHwP	osReducedPerf Cnt lgc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3/			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	/	
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	126.699997	126.699997 ± 0.0009	Resu
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	301.100006	301.100006 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	192	192	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	301.100006	301.100006 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	
AbsHwPos TargetHwAuthority Uls M f32	0.150000006	0.150000006 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	784.900024	784.900024 ± 0.0009	
AbsHwPos_VenChtrOfstLearn Cnt M lgc	1	1	
AbsHwPos_venChtroistLearn_Cht_in_igc  AbsHwPos_VehChtrValid_Cht_M_lgc	0	0	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cft_1_efturn)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Nic_cail_np_nusi iwr us_ivkii biagivigi_celiv i Colalus(Falalii_Ciil_ I _uuo)	0	0	
Pto Call An AbeHupoe NytrDiagMar CotNTCCtatus/Ctatus Cat T animal	U		
	0.150000006		1 '
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.150000006	0.150000006 ± 0.004	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	301.100006	301.100006 ± 0.1	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	301.100006 192	301.100006 ± 0.1 192	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	301.100006	301.100006 ± 0.1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2098.83008		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-868.820007		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.159999996		
AbsHwPos_HwPosSource_Cnt_M_u16	5200		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	139.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	139.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22857		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-409.950012		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.159999996		
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	815.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.150000006		
k_HwPosOutputLPFError_HwDeg_f32	822.799988		
k_I2CHwAuthority_Uls_f32	0.150000006		
k_MaxSensorlessAuthority_Uls_f32	0.23999995		
k_MaxVehCntrOffDiff_HwDeg_f32	525.799988		
k MinSensorlessAuthority Uls f32	0.150000006		
k TurnsCntrAuthority Uls f32	0.150000006		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	466.399994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.239999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	74		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	513.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1859		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (		osPeducedParf Cnt Igo	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_HwPosSource_0 target_AbsHwPos_Per2_I2CHwAbsPosVer	_	
	target AbsHwPos Per2 I2CHwAbsPos I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	<del>-</del>	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAutho		
	target AbsHwPos Per2 SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	V		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_uu8	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1=	12 .
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	139.100006	139.100006 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-473.808807	-473.808807 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-473.808807	-473.808807 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1600	1600 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.23999995	0.239999995 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	1600	1600 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	815.099976	815.099976 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.23999995	0.239999995 ± 0.004	•
	-473.808807	-473.808807 ± 0.1	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value			
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	-   -
		3 21845	





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	513.099976	513.099976 ± 0.0009	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	889.099976		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.170000002		
AbsHwPos HwPosSource Cnt M u16	208		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORINVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	151.5		
AbsHwPos RelHwPos HwDeg M f32	151.5		
AbsHwPos SrlComHwPosStatus Cnt M u16	22929		
AbsHwPos SrlComHwPos HwDeg M f32	550.690002		
AbsHwPos TargetHwAuthority Uls M f32	0.170000002		
AbsHwPos TempHwPos HwDeq M f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCr	trOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
K HwPosAuthorityStep Uls f32	0.159999996		
K HwPosOutputLPFError HwDeg f32	852.5		
<_I2CHwAuthority_Uls_f32	0.159999996		
MaxSensorlessAuthority Uls f32	0.25		
maxVehCntrOffDiff_HwDeg_f32	555.5		
MinSensorlessAuthority Uls f32	0.159999996		
TurnsCntrAuthority Uls f32	0.159999996		
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	1		
VehCntrOffValidLimit HwDeg f32	496.100006		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	502.100006		
arget AbsHwPos Per2 ManufMode Cnt enum.value	0		
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.25		
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	0		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	543.299988		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1983		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPer		ReducedPerf Cnt lac	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthorit		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3		,	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthorit		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f3/			
arget_Re_inst_Ap_AbsHwPos.AbsHwPos Per2_SrlComHwPosStatus Cnt_u16	target AbsHwPos Per2 SrlComHwPosStati	· · · · ·	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValidity		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	J.I400	
		Funcated Value	D.
Name	Actual Value	Expected Value	Res
AbsHwPos AlignedHwPos HwDeg M f32	151.5	151.5 ± 0.0009	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	0	0 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~

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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

me	Input Value
sHwPos_AlignedCumMtrPos_HwDeg_M_f32	2380.31006
sHwPos HandwheelAuthority Uls M f32	0.180000007
sHwPos_HandwheelPositionLPF M str.SV Uls f32	909.380005
sHwPos_HandwheelPositionLPF_M_str.Sv_ois_i32	0.18000007
sHwPos HwPosSource Cnt M u16	216
sHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID
sHwPos PrevHandwheelPosition HwDeg M f32	163.899994
shwPos_RelHwPos_HwDeg_M_f32	163.899994
sHwPos_SrlComHwPosStatus_Cnt_M_u16	23001
sHwPos SrlComHwPos HwDeg M f32	691.429993
sHwPos TargetHwAuthority Uls M f32	0.180000007
:	6602.50977
sHwPos_TempHwPos_HwDeg_M_f32	
sHwPos_VehCntrOffset_HwDeg_M_f32	875.5
sHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
sHwPos_VehCntrValid_Cnt_M_lgc	-
e_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
e_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos 0.170000002
HwPosAuthorityStep_Uls_f32	882.200012
HwPosOutputLPFError_HwDeg_f32	
2CHwAuthority_Uls_f32	0.170000002
MaxSensorlessAuthority_Uls_f32	0.25999999
MaxVehCntrOffDiff_HwDeg_f32	585.200012
MinSensorlessAuthority_Uls_f32	0.170000002
FurnsCntrAuthority_Uls_f32	0.170000002
JseTurnsCntr_Cnt_lgc	0
/ehCntrOffValidLimit_HwDeg_f32	525.799988
get_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
get_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
get_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012
get_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
get_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25999999
get_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	421.700012
get_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	86
get_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	573.5
get_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2107
get_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_t	
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	1
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	163.899994	163.899994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.180000007	0.180000007 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1934.14343	1934.14343 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	216	216	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1934.14343	1934.14343 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	691.429993	691.429993 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.180000007	0.180000007 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	875.5	875.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.180000007	0.180000007 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	216	216	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	691.429993	691.429993 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_Uls_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_Uls_f32	0.159999996
k_TurnsCntrAuthority_Uls_f32	0.159999996
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	496.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-500.600006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-500.600006	-500.600006 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-500.600006	-500.600006 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-500.600006	-500.600006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Pte Call AheHwPos Per? CP1 ChecknointReached	1	Pte Call AhsHwPos Per? CP1 ChecknointReached	1	-

Test Step 2.21 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2661.79004
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003
AbsHwPos_HwPosSource_Cnt_M_u16	232
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-941.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-941.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23145
AbsHwPos_SrlComHwPos_HwDeg_M_f32	972.909973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	941.599976
k_I2CHwAuthority_Uls_f32	0.189999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001

AbsHwPos\_Per2



Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976		
k_MinSensorlessAuthority_Uls_f32	0.189999998		
k_TurnsCntrAuthority_UIs_f32	0.189999998		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	585.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	98		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2355		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf 0	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-900	-900 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-900	-900 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-900	-900 ± 0.1	<b>✓</b>	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-900	-900 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024	935.900024 ± 0.0009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.189999998	0.189999998 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-900	-900 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-900	-900 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024	633.900024 ± 0.0009	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.22 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2802.53003	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.209999993	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	970.219971	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.209999993	
AbsHwPos_HwPosSource_Cnt_M_u16	240	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-921.5	
AbsHwPos_RelHwPos_HwDeg_M_f32	-921.5	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23217	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1113.65002	

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Name	Input Value		
AbsHwPos TargetHwAuthority Uls M f32	0.209999993		
AbsHwPos TempHwPos HwDeq M f32	7024.72998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	966.099976		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	hCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	971.299988		
k I2CHwAuthority Uls f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.289999992		
k MaxVehCntrOffDiff HwDeg f32	674.299988		
k MinSensorlessAuthority Uls f32	0.200000003		
k_TurnsCntrAuthority_UIs_f32	0.200000003		
k_UseTurnsCntr_Cnt_lgc	0		
k VehCntrOffValidLimit HwDeg f32	614.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	900		
target AbsHwPos Per2 ManufMode Cnt enum.value	1		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.289999992		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	482		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	104		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	664.099976		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2479		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u		PosReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target AbsHwPos Per2 SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
		Francis d Value	Danul
Name Ababba Para Alfarradiba Para ba Para M. 600	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-921.5	-921.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	955.473755	955.473816 ± 0.1	,
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	955.473755	955.473816 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	900	900 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	900	900 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	966.099976	966.099976 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.20000003	0.200000003 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	955.473755	955.473816 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	900	900 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	664.099976	664.099976 ± 0.0009	<b>■</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_



Test Step 2.23 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2943.27002		
AbsHwPos HandwheelAuthority Uls M f32	0.219999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-990.5		
AbsHwPos_HandwheelPositionLPF_M_str.K_UIs_f32	0.219999999		
AbsHwPos HwPosSource Cnt M u16	248		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	324.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	324.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23289		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1254.39001		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.21999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	996.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.20999993		
k HwPosOutputLPFError HwDeg f32	1001		
k_I2CHwAuthority_Uls_f32	0.209999993		
k_MaxSensorlessAuthority_Uls_f32	0.300000012		
k_MaxVehCntrOffDiff_HwDeg_f32	704		
k_MinSensorlessAuthority_Uls_f32	0.209999993		
k_TurnsCntrAuthority_UIs_f32	0.20999993		
k_UseTurnsCntr_Cnt_Igc	0		
k VehCntrOffValidLimit HwDeg f32	644.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.30000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	110		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	694.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2603		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		PosReducedPerf Cnt Inc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority UIs f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target AbsHwPos Per2 SensorlessAut	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target AbsHwPos Per2 SensorlessHwl	7	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
	target_7 taer titl ee_t etetteetii titt ee_		
target Rte Inst An AbsHwPos AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	ity_Cnt_u08	Rosu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name	target_Pim_EOLVehCntrOffset  Actual Value	ity_Cnt_u08  Expected Value	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  324.5	Expected Value  324.5 ± 0.0009	Resu
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value 324.5 0.2099999993	Expected Value  324.5 ± 0.0009  0.2099999993 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_Pim_EOLVehCntrOffset  Actual Value 324.5 0.2099999993 0	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value 324.5 0.2099999993 0 2	Expected Value  324.5 ± 0.0009 0.209999993 ± 0.004 0 ± 0.1 2	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum	target_Pim_EOLVehCntrOffset  Actual Value 324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value 324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID 0	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.2099999993 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0 0	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.209999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0 0 504	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0 0 504	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*  *none*	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.209999993 0 996.299988 0 0 504 1	Expected Value  324.5 ± 0.0009  0.209999993 ± 0.004  0 ± 0.1  2  HWPOS_STATE_SENSORVALID  0 ± 0.1  21845  0 ± 0.1  0.209999993 ± 0.004  0 ± 0.1  996.299988 ± 0.0009  0  *none*  *none*  *none*	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 996.299988 0 0 504 1 1 0.2099999993	Expected Value  324.5 ± 0.0009 0.209999993 ± 0.004 0 ± 0.1 2 HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.209999993 ± 0.004 0 ± 0.1 996.299988 ± 0.0009 0 *none* *none* *none* *none* 0.209999993 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 0996.299988 0 0 504 1 1 0.2099999993 0	Expected Value  324.5 ± 0.0009 0.209999993 ± 0.004 0 ± 0.1 2 HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.209999993 ± 0.004 0 ± 0.1 996.299988 ± 0.0009 0  *none* *none* *none* *none* 0.209999993 ± 0.004 0 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 0996.299988 0 0 504 1 1 0.2099999993 0 2	Expected Value  324.5 ± 0.0009 0.209999993 ± 0.004 0 ± 0.1 2 HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.209999993 ± 0.004 0 ± 0.1 996.299988 ± 0.0009 0  *none* *none* *none* *none* 0.209999993 ± 0.004 0 ± 0.1 2	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset  Name  AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_Pim_EOLVehCntrOffset  Actual Value  324.5 0.2099999993 0 2 HWPOS_STATE_SENSORVALID 0 21845 0 0.2099999993 0 0996.299988 0 0 504 1 1 0.2099999993 0	Expected Value  324.5 ± 0.0009 0.209999993 ± 0.004 0 ± 0.1 2 HWPOS_STATE_SENSORVALID 0 ± 0.1 21845 0 ± 0.1 0.209999993 ± 0.004 0 ± 0.1 996.299988 ± 0.0009 0  *none* *none* *none* *none* 0.209999993 ± 0.004 0 ± 0.1	Resul

0

0 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

2014-09-04, 15:47:58+0530





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	694.299988	694.299988 ± 0.0009	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.24 (Repeat Count = 1)			<b>~</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-3084.01001		
AbsHwPos HandwheelAuthority Uls M f32	0.230000004		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	1010.78003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.230000004		
AbsHwPos_HwPosSource_Cnt_M_u16	256		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	471.600006		
AbsHwPos_RelHwPos_HwDeg_M_f32	471.600006		
AbsHwPos SrlComHwPosStatus Cnt M u16	23361		
AbsHwPos SrlComHwPos HwDeg M f32	1395.13		
AbsHwPos TargetHwAuthority Uls M f32	0.230000004		
AbsHwPos_TempHwPos_HwDeg_M_f32	7306.20996 1026.5		
AbsHwPos_VehCntrOfftset_HwDeg_M_f32			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	IrOnset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.219999999		
k_HwPosOutputLPFError_HwDeg_f32	1030.69995		
k_I2CHwAuthority_Uls_f32	0.219999999		
k_MaxSensorlessAuthority_Uls_f32	0.310000002		
k_MaxVehCntrOffDiff_HwDeg_f32	733.700012		
k_MinSensorlessAuthority_Uls_f32	0.219999999		
k_TurnsCntrAuthority_Uls_f32	0.219999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	674.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.310000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	522.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	116		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	724.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2727		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Other and the property of the property $	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos AlignedHwPos HwDeg M f32	471.600006	471.600006 ± 0.0009	

0.219999999

100.300003

100.300003

100.300003

0.219999999

100.300003

21845

HWPOS\_STATE\_SENSORVALID

2

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32

 $AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32$ 

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

0.219999999 ± 0.004

HWPOS\_STATE\_SENSORVALID

100.300003 ± 0.1

100.300003 ± 0.1

100.300003 ± 0.1

100.300003 ± 0.1

0.219999999 ± 0.004

2





Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1026.5	1026.5 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.219999999	0.219999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	100.300003	100.300003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	100.300003	100.300003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.25 (Repeat Count = 1)		
Name	Input Value	
bsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3224.75	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1031.06006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.23999995	
AbsHwPos HwPosSource Cnt M u16	264	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-488.100006	
AbsHwPos RelHwPos HwDeg M f32	-488.100006	
AbsHwPos SrlComHwPosStatus Cnt M u16	23433	
AbsHwPos SrlComHwPos HwDeg M f32	1535.87	
ubsHwPos TargetHwAuthority Uls M f32	0.239999995	
bsHwPos_TempHwPos_HwDeg_M_f32	-7446.9502	
bsHwPos VehCntrOffset HwDeg M f32	1056.69995	
bsHwPos VehCntrOfstLearn Cnt M Igc	0	
bsHwPos VehCntrValid Cnt M Igc	0	
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	
tte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos	
HwPosAuthorityStep Uls f32	0.230000004	
_HwPosOutputLPFError_HwDeg_f32	1060.40002	
I2CHwAuthority Uls f32	0.23000004	
MaxSensorlessAuthority Uls f32	0.319999993	
MaxVehCntrOffDiff HwDeg f32	763.400024	
	0.230000004	
_MinSensorlessAuthority_UIs_f32		
_TurnsCntrAuthority_Uls_f32	0.230000004	
_UseTurnsCntr_Cnt_Igc	704	
_VehCntrOffValidLimit_HwDeg_f32	0	
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1	
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-350.200012	
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1	
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.31999993	
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	542.299988	
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	122	
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	754.700012	
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2851	
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16	
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-488.100006	-488.100006 ± 0.0009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.230000004	0.230000004 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.230000004	0.230000004 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1056.69995	1056.69995 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.230000004	0.230000004 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-350.200012	-350.200012 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-350.200012	-350.200012 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	754.700012	754.700012 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3365.48999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1051.33997
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.25
AbsHwPos_HwPosSource_Cnt_M_u16	272
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	765.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	765.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23505
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25
AbsHwPos_TempHwPos_HwDeg_M_f32	7587.68994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1086.90002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.23999995
_HwPosOutputLPFError_HwDeg_f32	1090.09998
c_I2CHwAuthority_Uls_f32	0.23999995
_MaxSensorlessAuthority_Uls_f32	0.330000013
c_MaxVehCntrOffDiff_HwDeg_f32	793.099976
_MinSensorlessAuthority_Uls_f32	0.23999995
c_TurnsCntrAuthority_Uls_f32	0.23999995
<pre>C_UseTurnsCntr_Cnt_lgc</pre>	0
<pre></pre>	733.700012
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-330.5
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.330000013
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	562.400024
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	784.900024
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2975
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	rf_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_fa	32 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32$	target_AbsHwPos_Per2_SensorlessAutl	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	2 target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	765.799988	765.799988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	705.880005	705.880005 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	705.880005	705.880005 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-330.5	-330.5 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-330.5	-330.5 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1086.90002	1086.90002 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.239999995	0.239999995 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	705.880005	705.880005 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

21845

-330.5

65535

21845

-330.5 ± 0.1

65535 ± 0.09

Test Step 2.27 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_UIs_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_Uls_f32	0.159999996





ADSHWPOS_Per2		( WAC	1000	
Name	Input Value			
k_TurnsCntrAuthority_Uls_f32	0.159999996			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	496.100006			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPo	osReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	prity_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit	ion_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Resu	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	•	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1		
AbsHwPos TargetHwAuthority Uls M f32	0.25	0.25 ± 0.004		

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988	543.299988 ± 0.0009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.28 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3646.96997	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.270000011	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1091.90002	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.270000011	
AbsHwPos_HwPosSource_Cnt_M_u16	2288	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1060	
AbsHwPos_RelHwPos_HwDeg_M_f32	1060	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23649	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1958.08997	

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AbsHwPos Per2 Input Value AbsHwPos TargetHwAuthority Uls M f32 0.270000011 -7869.16992 AbsHwPos\_TempHwPos\_HwDeg\_M\_f32 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32 9238.54004 AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc AbsHwPos VehCntrValid Cnt M Igc Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus(ErrorStatus) target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus Rte Inst Ap AbsHwPos target\_Rte\_Inst\_Ap\_AbsHwPos  $k\_HwPosAuthorityStep\_Uls\_f32$ 0.25999999 k\_HwPosOutputLPFError\_HwDeg\_f32 1149.5 0 25999999 k\_I2CHwAuthority\_Uls\_f32 k\_MaxSensorlessAuthority\_Uls\_f32 k\_MaxVehCntrOffDiff\_HwDeg\_f32 852.5 k\_MinSensorlessAuthority\_Uls\_f32 0.25999999 k\_TurnsCntrAuthority\_Uls\_f32 0 25999999 k\_UseTurnsCntr\_Cnt\_lgc k\_VehCntrOffValidLimit\_HwDeg\_f32 793.099976 target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_lgc.value Λ target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value 1 target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value -291.100006 target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum.value target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32.value 0.349999994 target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32.value 602.599976 target\_AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08.value 100 target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32 845.299988 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16 3223  $target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_(target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_Igc$ target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32 target AbsHwPos Per2 HandwheelPosition HwDeg f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16$ target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32 target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum target AbsHwPos Per2 ManufMode Cnt enum target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32 target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32 target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$ target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32$  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08$ target AbsHwPos Per2 TurnsCntrValidity Cnt u08  $target\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset$ target\_Pim\_EOLVehCntrOffset **Actual Value Expected Value** Result AbsHwPos\_AlignedHwPos\_HwDeg\_M f32 1060  $1060 \pm 0.009$ AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32 0.25999999 0.25999999 ± 0.004

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-712.636963	-712.636902 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-712.636963	-712.636902 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5591.57031	-5591.56982 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25999999	0.25999999 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-5591.57031	-5591.56982 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	9238.54004	9238.54004 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25999999	0.25999999 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-712.636963	-712.636902 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	845.299988	845.299988 ± 0.0009	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001		
AbsHwPos_HwPosSource_Cnt_M_u16	296		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc		obCotrOffoot CotErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)  Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	ehCntrOffset_GetErrorStatus_ErrorStatus	
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError HwDeg f32	1179.19995		
k_I2CHwAuthority Uls f32	0.270000011		
k_lachwauthority_ois_is2 k_MaxSensorlessAuthority_Uls_f32	1		
k_MaxVehCntrOffDiff HwDeq f32	882.200012		
k MinSensorlessAuthority Uls f32	0.270000011		
k TurnsCntrAuthority Uls f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer		PosReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAu		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f3:			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	sStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValid	dity Cnt u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	·	
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	
AbsHwPos HandwheelAuthority Uls M f32	0.360000014	0.360000014 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORVALID	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	622.700012	622.700012 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	•
		3	٠,
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	3 21845	21845	

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Name	Actual value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5	875.5 ± 0.0009	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.30 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	-1676.60999		
AbsHwPos HandwheelAuthority Uls M f32	0.129999995		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995		
AbsHwPos_HwPosSource_Cnt_M_u16	176		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-901.799988		
AbsHwPos_ReiHwPos_HwDeg_M_f32	-901.799988		
AbsHwPos SrlComHwPosStatus Cnt M u16	22641		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498		
AbsHwPos TargetHwAuthority Uls M f32	0.129999995		
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	rOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	- Onoce_Octerrorounds_Errorounds	
k_HwPosAuthorityStep_Uls_f32	0.119999997		
k HwPosOutputLPFError HwDeg f32	733.700012		
k I2CHwAuthority Uls f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.20999993		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_UIs_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (		educedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	··· <u>-</u>	
		Expected Value	Desult
Name Abel hyDec Aligned HyDec HyDec M 622	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	•

0.119999997

-1015.08685

-1015.08685

-2401.10986

0.119999997

21845

HWPOS\_STATE\_SENSORVALID

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum
AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

 $AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32$ 

0.119999997 ± 0.004

HWPOS\_STATE\_SENSORVALID

-1015.08691 ± 0.1

-1015.08691 ± 0.1

-2401.11011 ± 0.1

0.119999997 ± 0.004

21845

AbsHwPos\_Per2

2014-09-04, 15:47:58+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5	422.5 ± 0.0009	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Input Value
4069.18994
0.300000012
1152.73999
0.300000012
4312
HWPOS STATE SENSORINVALID
1501.30005
1501.30005
23865
2380.31006
0.300000012
8920.66016
8920.66016
0
0
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
target_Rte_Inst_Ap_AbsHwPos
0.100000001
1238.59998
0.100000001
0.379999995
941.599976
0
0.100000001
0
882.200012
0
0
-232
1
0.379999995
662.900024
158
935.900024
3595
5
( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc
target AbsHwPos Per2 HandwheelAuthority Uls f32
1
target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
target_AbsHwPos_Per2_ManufMode_Cnt_enum
target AbsHwPos Per2 SensorlessAuthority Uls f32
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target AbsHwPos Per2 SrlComHwPosStatus Cnt u16

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Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1501.30005	1501.30005 ± 0.009	<b>✓</b>	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.379999995	0.379999995 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	662.900024	662.900024 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	662.900024	662.900024 ± 0.1	•	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	662.900024	662.900024 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.379999995	0.379999995 ± 0.004	•	
AbsHwPos_TempHwPos_HwDeg_M_f32	662.900024	662.900024 ± 0.1	•	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8920.66016	8920.66016 ± 0.009	•	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.379999995	0.379999995 ± 0.004	•	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	662.900024	662.900024 ± 0.1	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	•	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	662.900024	662.900024 ± 0.1	•	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	935.900024	935.900024 ± 0.0009	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.32 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	4209.93018
AbsHwPos HandwheelAuthority Uls M f32	0.310000002
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	1173.02002
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.310000002
AbsHwPos_HwPosSource_Cnt_M_u16	320
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-448.700012
AbsHwPos_RelHwPos_HwDeg_M_f32	-448.700012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23937
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.310000002
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.200000003
k_HwPosOutputLPFError_HwDeg_f32	1268.30005
k_I2CHwAuthority_Uls_f32	0.200000003
k_MaxSensorlessAuthority_Uls_f32	0.389999986
k_MaxVehCntrOffDiff_HwDeg_f32	1060.40002
k_MinSensorlessAuthority_Uls_f32	1
k_TurnsCntrAuthority_Uls_f32	0.200000003
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	911.900024
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-212.300003
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.389999986
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	683
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	966.099976
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3719
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 





Name	Input Value			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerror and all the properties of the$	f_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	nority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	2 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDegf32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-448.700012	-448.700012 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999	0.109999999 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3541.94092	3541.94092 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	320	320	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3541.94092	3541.94092 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002	8814.7002 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002	8814.7002 ± 0.009	<b>✓</b>	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.109999999	0.109999999 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	320	320	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

65535

65535 ± 0.09

Test Step 2.33 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-4350.66992
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.319999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1193.30005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.319999993
AbsHwPos_HwPosSource_Cnt_M_u16	328
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-429
AbsHwPos_RelHwPos_HwDeg_M_f32	-429
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24009
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.319999993
AbsHwPos_TempHwPos_HwDeg_M_f32	-8708.74023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1208.73999
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.310000002
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_Uls_f32	0.310000002
k_MaxSensorlessAuthority_Uls_f32	0.40000006
k_MaxVehCntrOffDiff_HwDeg_f32	1090.09998





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Name	Input Value		
k MinSensorlessAuthority Uls f32	0.310000002		
k_TurnsCntrAuthority_UIs_f32	0.310000002		
k_UseTurnsCntr_Cnt_lgc	0	0	
k_VehCntrOffValidLimit_HwDeg_f32	941.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-192.600006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.40000006		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	170		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	996.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3843		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-429	-429 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	703.099976	703.099976 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>→</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	703.099976	703.099976 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	•

Abshweds_hweds30dice_chi_w_d10	J	J	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1208.73999	-1208.73999 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.40000006	0.40000006 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	703.099976	703.099976 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	703.099976	703.099976 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	996.299988	996.299988 ± 0.0009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.34 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4491.41016	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.330000013	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1213.57996	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.330000013	
AbsHwPos_HwPosSource_Cnt_M_u16	336	
AbsHwPos_HwPosState_Cnt_M_enum	7	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.300003	
AbsHwPos_RelHwPos_HwDeg_M_f32	147.300003	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24081	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2802.53003	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.330000013	

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AbsHwPos Per2

AbsHwPos_Per2		MACO	icab
Name	Input Value		
AbsHwPos_TempHwPos_HwDeg_M_f32	8602.78027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8602.78027		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.319999993		
k_HwPosOutputLPFError_HwDeg_f32	1327.69995		
k_I2CHwAuthority_Uls_f32	0.319999993		
k_MaxSensorlessAuthority_Uls_f32	0.409999996		
k_MaxVehCntrOffDiff_HwDeg_f32	1119.80005		
k_MinSensorlessAuthority_Uls_f32	0.319999993		
k_TurnsCntrAuthority_UIs_f32	0.319999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	0		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-172.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.409999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	723.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	176		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1026.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3967		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	147.300003	147.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3652.01611	3652.01611 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3652.01611	3652.01611 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8602.78027	8602.78027 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000203	0.00999999978 ± 0.004	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

1600

65534

65535

1026.5

0

1600 ± 0.1

1026.5 ± 0.009

65534

65535

0 ± 0.1

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

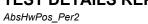


Test Step 2.35 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4632.1499		
AbsHwPos HandwheelAuthority Uls M f32	0.34000004		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1233.85999		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.340000004		
AbsHwPos HwPosSource Cnt M u16	344		
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	156.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	156.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24153		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.340000004		
AbsHwPos_TempHwPos_HwDeg_M_f32	8496.82031		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8496.82031		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.330000013		
k_HwPosOutputLPFError_HwDeg_f32	1357.40002		
k_I2CHwAuthority_Uls_f32	0.330000013		
k_MaxSensorlessAuthority_Uls_f32	0.419999987		
k_MaxVehCntrOffDiff_HwDeg_f32	1149.5		
k_MinSensorlessAuthority_Uls_f32	0.330000013		
k_TurnsCntrAuthority_UIs_f32	0.330000013		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.419999987		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	743.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	182		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1056.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4091		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_e	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	ority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosit	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	/_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Namo	Actual Value	Expected Value	
Name		· ·	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	156.899994	156.899994 ± 0.0009	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	156.899994 0.00999999046	156.899994 ± 0.0009 0.00999999978 ± 0.004	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	156.899994 0.00999999046 3703.2666	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	156.899994 0.009999999046 3703.2666 65534	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.10 8496.82031 ± 0.009 1 0 *none* *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_EnwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504 0	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_BrwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504 0 0 0.009999999046	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.10 8496.82031 ± 0.009 1 0 *none* *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504 0 0 0.009999999046 1600	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.10 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004 1600 ± 0.1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value  target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	156.89994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 1 0 504 0 0 0.009999999046 1600 65534	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.1 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004 1600 ± 0.1 65534	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	156.899994 0.00999999046 3703.2666 65534 HWPOS_STATE_SENSORINVALID 3703.2666 65534 2943.27002 0 8496.82031 8496.82031 1 0 504 0 0 0.009999999046 1600	156.899994 ± 0.0009 0.00999999978 ± 0.004 3703.26636 ± 0.1 65534 HWPOS_STATE_SENSORINVALID 3703.26636 ± 0.1 65534 2943.27002 ± 0.1 0 ± 0.004 8496.82031 ± 0.10 8496.82031 ± 0.009 1 0 *none* *none* *none* 0.00999999978 ± 0.004 1600 ± 0.1	Resul

1600

1600 ± 0.1

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.36 (Repeat Count = 1) Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	-4772.89014		
	0.349999994		
AbsHwPos_HandwheelAuthority_Uls_M_f32	1254.14001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HandwheelPositionLPF_M_str.K_UIs_f32	0.349999994		
	352		
AbsHwPos_HwPosCource_Cnt_M_u16	9		
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_Provilendive all Position   HwPos_M_622			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	166.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	166.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24225		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.349999994		
AbsHwPos_TempHwPos_HwDeg_M_f32	-8390.86035		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1390.85999		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.340000004		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_Uls_f32	0.340000004		
k_MaxSensorlessAuthority_Uls_f32	0.430000007		
k_MaxVehCntrOffDiff_HwDeg_f32	1179.19995		
k_MinSensorlessAuthority_Uls_f32	0.340000004		
k_TurnsCntrAuthority_UIs_f32	0.340000004		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1030.69995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.430000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	763.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	188		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1086.90002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4215		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	4		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerformance AbsHwPosReducedPerformance AbsHwPosReducedPerforman$	f_( target_AbsHwPos_Per2_DiagSta	atusHwPosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_Handwl	heelAuthority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_fraction = 100000000000000000000000000000000000$	target_AbsHwPos_Per2_Handwl	heelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPos	Source_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwA	AbsPosValid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwA	AbsPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufM	Mode_Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_Sensorl	lessAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3.	target_AbsHwPos_Per2_Sensorl	lessHwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlCom	HwPosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlCom	HwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsC	ntrValidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	166.5	166.5 ± 0.0009	11030
	0.0000000000000000000000000000000000000	0.0000000000000000000000000000000000000	

Abshwros_Alighedhwros_hwbeg_ivi_loz	100.3	100.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2121.60986	-2121.61011 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2121.60986	-2121.61011 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~





Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1390.85999	-1390.85999 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Fest Step 2.37 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	4913.62988
	0.360000014
AbsHwPos_HandwheelAuthority_Uls_M_f32	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1274.42004
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.360000014
AbsHwPos_HwPosSource_Cnt_M_u16	360
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	176.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	176.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24297
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014
bsHwPos_TempHwPos_HwDeg_M_f32	8284.88965
bsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.100000001
_HwPosOutputLPFError_HwDeg_f32	1416.80005
_I2CHwAuthority_Uls_f32	0.100000001
_MaxSensorlessAuthority_Uls_f32	0.439999998
:_MaxVehCntrOffDiff_HwDeg_f32	1208.90002
_MinSensorlessAuthority_Uls_f32	0.10000001
_TurnsCntrAuthority_Uls_f32	0.100000001
_UseTurnsCntr_Cnt_lgc	1
VehCntrOffValidLimit HwDeg f32	1060.40002
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.43999998
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	783.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	194
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-1600
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	4339
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatu	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedP	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Fet2_HandwheelAbsition_HwDeg	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	2 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	176.100006	176.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	783.5	783.5 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	783.5	783.5 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.439999998	0.439999998 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	783.5	783.5 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	783.5	783.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.38 (Repeat Count = 1)	· ·
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5054.37012
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.370000005
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1294.69995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.370000005
AbsHwPos_HwPosSource_Cnt_M_u16	368
AbsHwPos_HwPosState_Cnt_M_enum	11
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	185.699997
AbsHwPos_RelHwPos_HwDeg_M_f32	185.699997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24369
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3365.48999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.370000005
AbsHwPos_TempHwPos_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.300000012
k_HwPosOutputLPFError_HwDeg_f32	1446.5
k_I2CHwAuthority_Uls_f32	0.300000012
k_MaxSensorlessAuthority_Uls_f32	0.449999988
k_MaxVehCntrOffDiff_HwDeg_f32	1238.59998
k_MinSensorlessAuthority_Uls_f32	0.300000012
k_TurnsCntrAuthority_UIs_f32	0.300000012
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1090.09998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2

AbsHwPos\_Per2





Name	Input Value		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.449999988		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	803.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	200		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4463		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPer	f(target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	32 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3.	2 target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	185.699997	185.699997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0699999928	0.0700000003 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3841.86523	3841.86499 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3841.86523	3841.86499 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	8178.93018	8178.93018 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0699999928	0.0700000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5195.10986
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.379999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1314.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.379999995
AbsHwPos_HwPosSource_Cnt_M_u16	376
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	195.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	195.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24441
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3506.22998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.379999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-8072.97021
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.370000005



AbsHwPos\_Per2

Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	1476.19995		
k_I2CHwAuthority_Uls_f32	0.370000005		
k_MaxSensorlessAuthority_Uls_f32	0.460000008		
k_MaxVehCntrOffDiff_HwDeg_f32	1268.30005		
k_MinSensorlessAuthority_Uls_f32	0.370000005		
k_TurnsCntrAuthority_Uls_f32	0.370000005		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1119.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.460000008		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	823.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	206		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4587		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	195.300003	195.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	823.700012	823.700012 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	823.700012	823.700012 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000008	0.460000008 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	823.700012	823.700012 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	823.700012	823.700012 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.40 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-5335.8501	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.389999986	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1487	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.389999986	





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Name	Input Value		
AbsHwPos_HwPosSource_Cnt_M_u16	6384		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	204.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	204.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24513		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3646.96997		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.389999986		
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977 7967.00977		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCr	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target Rte Inst Ap AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.379999995		
k_HwPosOutputLPFError_HwDeg_f32	1505.90002		
k I2CHwAuthority Uls f32	0.379999995		
k_MaxSensorlessAuthority_Uls_f32	0.469999999		
k_MaxVehCntrOffDiff_HwDeg_f32	1298		
k_MinSensorlessAuthority_Uls_f32	0.379999995		
k_TurnsCntrAuthority_Uls_f32	0.379999995		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1149.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	562.400024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.469999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	843.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	212		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1000.59998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4711		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2	Dadward Dark Out Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelAuthori target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	204.899994	204.899994 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4014.20361	4014.20386 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4014.20361	4014.20386 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977	7967.00977 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	Ĭ
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046 1600	0.00999999978 ± 0.004	Ž
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	1600 ± 0.1 65534	J
target_AbsHwPos_Per2_srlComHwPosStatus_Cnt_u16.value	65535	65535	-
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0±0.1	
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1000.59998	1000.59998 ± 0.009	-
5 1			





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.41 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5476.58984		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1507.09998		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40000006		
AbsHwPos_HwPosSource_Cnt_M_u16	392		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	214.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	214.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24585		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3787.70996		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	7861.0498		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	nCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.38999986		
C_HwPosOutputLPFError_HwDeg_f32	1535.59998		
c_I2CHwAuthority_Uls_f32	0.389999986		
<_MaxSensorlessAuthority_Uls_f32	0.479999989		
c_MaxVehCntrOffDiff_HwDeg_f32	1327.69995		
_MinSensorlessAuthority_Uls_f32	0.389999986		
C_TurnsCntrAuthority_Uls_f32	0.389999986		
c_UseTurnsCntr_Cnt_lgc	1		
<_VehCntrOffValidLimit_HwDeg_f32	1179.19995		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	582.5		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.479999989		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	863.900024		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	218		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-500.899994		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4835		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_( target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3.	2 target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	/alid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cn		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuth	ority Uls f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	_ 0_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
NosHwPos_AlignedHwPos_HwDeg_M_f32	214.5	214.5 ± 0.0009	i i i
AbsHwPos HandwheelAuthority Uls M f32	0.479999989	0.479999989 ± 0.004	
kbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	863.900024	863.900024 ± 0.1	
.bsHwPos_HwPosSource_Cnt_M_u16	3 HWPOS_STATE_SENSORINVALID	3	
hallupaa Hupaastata Cat M anima	LEVYPUS STATE SENSORINVALID	HWPOS_STATE_SENSORINVALID	
		062 000024 + 0.4	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	863.900024	863.900024 ± 0.1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	863.900024 21845	21845	
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	863.900024		

863.900024

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

863.900024 ± 0.1

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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	863.900024	863.900024 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	863.900024	863.900024 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.42 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1617.32996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40999996
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1527.19995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.409999996
AbsHwPos_HwPosSource_Cnt_M_u16	400
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	224.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	224.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24657
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3928.44995
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.409999996
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1455.08997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
HwPosAuthorityStep Uls f32	0.40000006
:_HwPosOutputLPFError_HwDeg_f32	1565.30005
	0.40000006
MaxSensorlessAuthority Uls f32	0.49000001
MaxVehCntrOffDiff HwDeg f32	0
MinSensorlessAuthority Uls f32	0.40000006
TurnsCntrAuthority Uls f32	0.40000006
UseTurnsCntr Cnt lgc	1
C VehCntrOffValidLimit HwDeg f32	1208.90002
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.49000001
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	884
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1268.09998
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	4959
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDog f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16

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AbsHwPos\_Per2

Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	224.100006	224.100006 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2278.53882	-2278.53882 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984	-7755.08984 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	733.329956	733.330017 ± 0.0009	<b>✓</b>	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	733.329956	733.330017 ± 0.0009	<b>✓</b>	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.43 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-558.070007
AbsHwPos HandwheelAuthority Uls M f32	0.419999987
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	157.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.419999987
AbsHwPos_HwPosSource_Cnt_M_u16	408
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-389.600006
AbsHwPos_RelHwPos_HwDeg_M_f32	-389.600006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24729
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4069.18994
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.419999987
AbsHwPos_TempHwPos_HwDeg_M_f32	269.209991
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.409999996
k_HwPosOutputLPFError_HwDeg_f32	1595
k_I2CHwAuthority_Uls_f32	0.409999996
k_MaxSensorlessAuthority_Uls_f32	0.5
k_MaxVehCntrOffDiff_HwDeg_f32	1600
k_MinSensorlessAuthority_Uls_f32	0.409999996
k_TurnsCntrAuthority_UIs_f32	0.409999996
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1500.34998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	622.700012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.5
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	904.099976





Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	230		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1298.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5083		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-389.600006	-389.600006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.419999987	0.419999987 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	269.209991	269.209991 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	408	408	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	269.209991	269.209991 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24729	24729	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4069.18994	4069.18994 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.419999987	0.419999987 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1462.16992	-1462.17004 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.419999987	0.419999987 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	269.209991	269.209991 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	408	408	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	24729	24729	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
	4400 40000	4400 47004 : 0.000	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

-1462.16992

-1462.17004 ± 0.009

Test Step 2.44 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	898.809998
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.430000007
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	567.400024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.430000007
AbsHwPos_HwPosSource_Cnt_M_u16	416
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	243.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	243.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24801
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4209.93018
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.430000007
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.419999987
k_HwPosOutputLPFError_HwDeg_f32	50.5999985

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 





Name	Input Value			
k_I2CHwAuthority_Uls_f32	0.419999987	0.419999987		
k_MaxSensorlessAuthority_Uls_f32	0.50999999			
k_MaxVehCntrOffDiff_HwDeg_f32	1416.80005			
k_MinSensorlessAuthority_Uls_f32	0.419999987			
k_TurnsCntrAuthority_UIs_f32	0.419999987			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	1268.30005			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.50999999			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	924.200012			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1328.5			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5207			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	243.300003	243.300003 ± 0.0009	~	
AbsHwPos HandwheelAuthority Uls M f32	0.0100000203	0.00999999978 + 0.004	<b>✓</b>	

3	- 3		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	243.300003	243.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	147.139496	147.139496 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.139496	147.139496 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012	-409.950012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-25.3900146	-25.3899994 ± 0.00009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000203	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	147.139496	147.139496 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1328.5	1328.5 ± 0.009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.45 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6039.5498	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.439999998	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1587.5	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.439999998	
AbsHwPos_HwPosSource_Cnt_M_u16	424	
AbsHwPos_HwPosState_Cnt_M_enum	5	





Name				
Abstance   Reviewer   Abstance			•	
Absthorps SICOURHAPOSIABURA (D.E.)   438565892				
Abath-Part S				
Abashir/Pos_TargethwoLinkong_M_152   Abashir/Pos_VerCintChills_am_Crit_M_152   Abashir/Pos_VerCintChills_am_Crit_M_152   Abashir/Pos_VerCintChills_am_Crit_M_152   Abashir/Pos_VerCintChills_am_Crit_M_152   Abashir/Pos_VerCintChills_am_Crit_M_152   Abashir/Pos_VerCintChills_am_Crit_M_152   Abashir/Pos_VerCintChill_am_Crit_M_152   BigCall_A_A_Abashir/Pos_Crit_Chill_GetErroriStatus(ErroriStatus)   StepCall_A_A_Abashir/Pos_Usc_122   Abashir/Pos_VerCintChill_am_Crit_M_152   BigCall_A_A_Abashir/Pos_Usc_122   Abashir/Pos_Usc_122   Abashir/Pos_U				
AbachivPox   Ambridox   Ambrido				
Abahlwings   Aba				
Asahawhos_VenCrutvalic_Crut_Migc   Net_Alp_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Ap_AbathwPos_Ent_Miss_Coll_Ap_AbathwPos_Ent_Ap_Ab				
Re_Catt Ap_AbathWoon_ECU   VehCntrOffset_GelErnoStatus(ErnoStatus)   Insept_Re_Inst_Ap_AbathWoon_ECU   VehCntrOffset_GelErnoStatus_ErnoStatus   Insept_Re_Inst_Ap_AbathWoon_ECU   VehCntrOffset_GelErnoStatus_ErnoStatus   Insept_Re_Inst_Ap_AbathWoon_ECU   VehCntrOffset_GelErnoStatus_ErnoStatus   VehCntrOffset_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_GelErnoStatus_Group_G			0	AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc
Re_Inst_AA_Abel+WPos			0	AbsHwPos_VehCntrValid_Cnt_M_lgc
ManaGeolamber   Manageolamb		trOffset_GetErrorStatus_ErrorStatus	target_Rte_Call_Ap_AbsHwPos_EOLVehC	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)
K_HANG-SerosCupeLLP FERROR   Whose   15			target_Rte_Inst_Ap_AbsHwPos	Rte_Inst_Ap_AbsHwPos
N_INASSESSIONED   N_INASSES				, ,= =
MaxVerbintorOffile  Harbog 132				
MASS				
Machine   Mac				
V_USETURNCHT_CRUE_QC   0				
K_VehCntrOffValidLmilL HwDg_ri32   1298				
Larget_AbsHWPos_Per2_DagSlatusHwPosPeducedPerf_Cnt_lgc value				
target_AbsHwPos_Per2_DiagSlatusHwPosReducedPerf_Cnt_Igc.value 10				
target_AbsHwPos_Per2_UzCHwAbsPos_HwDeg_132 value				
target_AbsHMPos_Per2_ManufMode_Cnt_enum_value			0	
target_AbsHwPos_Per2_SensorlessAuthority_Uis_f32.value   0.519999981			662.900024	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_132.value   242   242   243   243   244			0	target_AbsHwPos_Per2_ManufMode_Cnt_enum.value
target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value target_Pim_EOLVehCntriOffset_EOLVehCntriOffset_HwDeg_f32 target_Pim_EOLVehCntriOffset_EOLHwPosTimPerformed_Cnt_u16 target_Rte_Call_Ap_AbsHwPos_EOLVehCntriOffset_GetErrorStatus_ErrorStatus target_Rte_Call_Ap_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_gtc target_Rte_Inst_Ap_AbsHwPos_Per2_Landwheelundrivity_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_Landwheelundrivity_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_Landwheelundrivity_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_Landwheelindrivity_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LiCHwAbsPos_Valid_Cnt_log_c target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LiCHwAbsPos_Valid_Cnt_log_c target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LiCHwAbsPos_Valid_Cnt_log_c target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LiCHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Us_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPosStatus_Cnt_uf6 target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u6 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_SirComHwPosHwDeg_f32 target_AbsHwPos_Per2_S			0.519999981	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value
target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_DeltwDs_132   target_Pim_EOLVehCntrOffset_EOLTehPostTrimPerformed_Cnt_u16   target_Ric_all_Ap_AbsHwPos_EOLVehCntrOffset_GelErrorStatus_ErrorStatus   target_Ric_inst_Ap_AbsHwPos_AbsHwPos_Per2_DiagStatusHwPosReducedPer1_Crt_target_Ric_inst_Ap_AbsHwPos_AbsHwPos_Per2_DiagStatusHwPosReducedPer1_Crt_target_Ric_inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPusition_HwDeg_132   target_AbsHwPos_Per2_HandwheelPusition_HwDeg_132   target_AbsHwPos_Per2_HandwheelPusition_HwDeg_132   target_AbsHwPos_Per2_HandwheelPusition_HwDeg_132   target_AbsHwPos_Per2_HandwheelPusition_HwDeg_132   target_AbsHwPos_Per2_HandwheelPusition_HwDeg_132   target_AbsHwPos_Per2_HwDes_Per2_DiagStatusHwPos_Per2_D				target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value
target_Pim_EOLVehCntrOffset_EOLHwPostTrimPerformed_Cnt_u16         5331           target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus         1           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Pez_DaigStatusHwPosReducedPerf_Call_gct         target_AbsHwPos_Pez_DaigStatusHwPosReducedPerf_Call_gct           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Pez_HandwheelPosition_HwDeg_132         target_Rte_Inst_Ap_AbsHwPosAbsHwPos_Per2_HandwheelPosition_HwDeg_132           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LindFoxSource_Cnt_u16         target_Rte_Inst_Ap_AbsHwPosAbsHwPos_Per2_InctWhAbsPosValid_Cnt_Igc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_InctWhAbsPosValid_Cnt_Igc         target_AbsHwPos_Per2_IndtWhAbsPosValid_Cnt_Igc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_132         target_Rte_Inst_Ap_AbsHwPosAbsHwPos_Per2_SensoriessAuthority_Uls_132           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_132         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_132           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SencoriessAuthority_Uls_132         target_AbsHwPos_Per2_SensoriessAuthority_Uls_132           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SirComHwPosStatus_Cnt_ull         target_AbsHwPos_Per2_SensoriessAuthority_Uls_132           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SirComHwPos_HwDeg_122         target_AbsHwPos_Per2_SirComHwPos_HwDeg_123           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnScntrValidity_Cnt_ull         target_AbsHwPos_Per2_SirComHwPos_HwDeg_132 <td< td=""><td></td><td></td><td></td><td></td></td<>				
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Ctarget_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewAbsPosValid_Cnt_Index target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewAbsPos_Index_				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos.Per2_DiagStatusHwPosReducedPerf_Citarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_Per2_HwDesSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensoriessAuthority_Uis_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_Per2_Inst_AbsHwPos_Per2_SifComHwPos_SifComHwPos_SifComHwPos_Per2_SifComHwPos_Per2_SifComHwPos_Per2_				
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16         target_AbsHwPos_Per2_HwPosSource_Cnt_u16         target_AbsHwPos_Per2_HwPosSource_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IncHwAbsPosValid_Cnt_Igc         target_AbsHwPos_Per2_HwPosSource_Cnt_u16         target_AbsHwPos_Per2_HwPosSource_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IncHwAbsPosValid_Cnt_Igc         target_AbsHwPos_Per2_IncHwAbsPosValid_Cnt_Igc         target_AbsHwPos_Per2_IncHwAbsPosValid_Cnt_Igc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IncHwAbsPos_HwDeg_f32         target_AbsHwPos_Per2_IncHwAbsPos_HwDeg_f32         target_AbsHwPos_Per2_ManufMode_Cnt_enum         target_AbsHwPos_Per2_ManufMode_Cnt_enum           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_Per2_SensoriessHwPos_Per2_SensoriessHwPos_Per2_SensoriessHwPos_Per2_SensoriessHwPos_Per2		PaduaadDarf Cat Iga		· ·
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc         target_Rte_Inst_Ap_AbsHwPos_Per2_VaruhosPos_VaruhosPos_Varu				
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ECHwAbsPos_HwDeg_f32         target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_face_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_face_face_face_face_face_face_face_face				
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc         target_AbsHwPos_Per2_I2CHwAbsPos_HvDeg_f32         target_AbsHwPos_Per2_I2CHwAbsPos_PhvDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HvDeg_f32         target_AbsHwPos_Per2_I2CHwAbsPos_PhvDeg_f32         target_AbsHwPos_Per2_I2CHwAbsPos_PhvDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SriComHwPos_PtwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SriComHwPos_HvDeg_f32         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Inst_Ap_AbsHwPos_HvDeg_f32         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           Name         Actual Value         Expected Value           AbsHwPos_HandwheelAuthority_Uls_M_f32         4252.899994         252.899994 ± 0.0009           AbsHwPos_HwPosSource_Cnt_M_enum         HWPOs_Strate_InvDutinvALID         HWPOS_STATE_INVDUTINVALID           AbsHwPos_SicComHwPos_Status_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_SicComHwPos_HwDeg_M_f32         4350.66992				
target_Rte_inst_Ap_AbsHwPos_Per2_ManufMode_Cnt_enum         target_AbsHwPos_Per2_ManufMode_Cnt_enum         target_AbsHwPos_Per2_ManufMode_Cnt_enum           target_Rte_inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_Rte_inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_SencomHwPos_HwDeg_f32         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_inst_Ap_AbsHwPos_Pins_EDUVehCntrOffset         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_inst_Ap_AbsHwPos_Pins_Deg_Mf32         Actual Value         Expected Value           AbsHwPos_HandwheelPauthority_Uls_Mf32         0.43999999999999999999999999999999999999				
target_Rte_inst_Ap_AbsHwPos_Per2_SensoriessAuthority_Uls_f32         target_Rte_inst_Ap_AbsHwPos_Per2_SensoriessAuthority_Uls_f32         target_Rte_inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_fa2         424         424         424         424         424         424         424         424         424		Deg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_Inst_Ap_AbsHwPos_Pim_EOLVehCntrOffset         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_M_f32         252.899994         252.899994         252.899994         252.899994 ± 0.0099           AbsHwPos_HwDes_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1         1131.30359 ± 0.1           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1         24873         24873           AbsHwPos_SrlCom		num	target_AbsHwPos_Per2_ManufMode_Cnt_	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset         target_Pim_EOLVehCntrOffset           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         252.899994         252.899994 ± 0.0009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_HwPosSource_Cnt_M_u16         424         424           AbsHwPos_HwPosSource_Cnt_M_u16         424         424           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_SriComHwPosStatus_Cnt_M_u16         24873         24873           AbsHwPos_SriComHwPos_HwDeg_M_f32         4350.66992         4350.66992 ± 0.1           AbsHwPos_TermpHwPos_HwDeg_M_f32         0.439999998         0.439999998 ± 0.0004           AbsHwPos_TermpHwPos_HwDeg_M_f32         4350.66992         4350.66992 ± 0.1           AbsH		y_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset         target_Pim_EOLVehCntrOffset           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         252.899994         252.899994 ± 0.0009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         424         424           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         24873         24873           AbsHwPos_SrlComHwPos_HwDeg_M_f32         4350.66992         4350.66992 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         550.690002         550.690002 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1437.21997         -1437.21997 ± 0.009           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1437.21997         -1437.21997 ± 0.009		_HwDegf32	target_AbsHwPos_Per2_SensorlessHwPos	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos.Per2_TurnsCntrValidity_Cnt_u08         target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         252.899994         252.899994 ± 0.0009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_uf6         424         424           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_uf6         24873         24873           AbsHwPos_SrlComHwPos_HwDeg_M_f32         4350.66992         4350.66992 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         -1437.21997         -1437.21997 ± 0.009           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1437.21997         -1437.21997 ± 0.009           AbsHwPos_VehCntrOfstt_earn_Cnt_M_lgc         0         0           AbsHwPos_VehCntrValid_Cnt_M_lgc		us_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset         target_Pim_EOLVehCntrOffset           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         252.899994         252.899994 ± 0.0009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         424         424           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         24873         24873           AbsHwPos_SrlComHwPos_HwDeg_M_f32         -4350.66992         -4350.66992 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         550.690002         550.690002 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1437.21997         -1437.21997 ± 0.009           AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc         0         0           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504				
Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         252.899994         252.899994 ± 0.0009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.439999998         0.439999998 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         424         424           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         1131.30359         1131.30359 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         24873         24873           AbsHwPos_FlwDeg_M_f32         -4350.66992         -4350.66992 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.439999998         0.43999998 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         550.690002         550.690002 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1437.21997         -1437.21997 ± 0.009           AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc         0         0           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         *none*		Cnt_u08		
AbsHwPos_AlignedHwPos_HwDeg_M_f32       252.899994       252.899994 ± 0.0009         AbsHwPos_HandwheelAuthority_Uls_M_f32       0.439999998       0.439999998 ± 0.004         AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       424       424         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*		1=		
AbsHwPos_HandwheelAuthority_Uls_M_f32       0.439999998       0.439999998 ± 0.004         AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       424       424         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*	Result	•		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       424       424         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_UIs_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*	· ·			
AbsHwPos_HwPosSource_Cnt_M_u16       424       424         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*				
AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_INPUTINVALID       HWPOS_STATE_INPUTINVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_FrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*				
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       1131.30359       1131.30359 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*				
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       24873       24873         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*				
AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.439999998       0.43999998 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*				
AbsHwPos_TempHwPos_HwDeg_M_f32       550.690002       550.690002 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*	•			
AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1437.21997       -1437.21997 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       0       0         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       *none*	-	0.439999998 ± 0.004	0.43999998	AbsHwPos_TargetHwAuthority_Uls_M_f32
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc         0         0           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         *none*	•	550.690002 ± 0.1	550.690002	AbsHwPos_TempHwPos_HwDeg_M_f32
AbsHwPos_VehCntrValid_Cnt_M_lgc 0 0  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 504 *none*	•	-1437.21997 ± 0.009	-1437.21997	AbsHwPos_VehCntrOffset_HwDeg_M_f32
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 504 *none*	•			
	~			
Rife Call An Anshwens NxtrDiagMgr SetNTCStatus(Param Cnt T u08) 11 *none*	•			
	•	*none*	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  1 *none*  1 430000008 + 0.004				
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value 0.439999998 0.439999998 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.439999998 ± 0.004 0.43999999999999999999999999999999999999	Ĭ			
target_AbsHwPos_Per2_HandwheelPostition_HwDeg_rsz.value 1131.30359 1131.30359 1131.30359 ± 0.1  target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value 424 424				
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 24873 24873				
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value				
target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32	•			

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.46 (Repeat Count = 1)			V	
Name	Input Value			
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6180.29004			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.44999988			
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-1000.29999			
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.449999988			
AbsHwPos_HwPosSource_Cnt_M_u16	432			
AbsHwPos_HwPosState_Cnt_M_enum	6 262.5			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_RelHwPos_HwDeg_M_f32	262.5			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24945			
AbsHwPos SrlComHwPos HwDeg M f32	4491.41016			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.449999988			
AbsHwPos_TempHwPos_HwDeg_M_f32	691.429993			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwP	os_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPo	os		
k_HwPosAuthorityStep_Uls_f32	0.43999998			
k_HwPosOutputLPFError_HwDeg_f32	1620			
k_I2CHwAuthority_Uls_f32	0.439999998			
k_MaxSensorlessAuthority_Uls_f32	0.529999971			
k_MaxVehCntrOffDiff_HwDeg_f32	1476.19995			
k_MinSensorlessAuthority_UIs_f32	0.439999998			
k_TurnsCntrAuthority_UIs_f32	0.439999998			
k_UseTurnsCntr_Cnt_lgc k_VehCntrOffValidLimit_HwDeg_f32	1327.69995			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1327.09993			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	683			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.529999971			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	964.400024			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	248			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1388.90002			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5455			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorSt	atus 2			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduce	dPerf_( target_AbsHwPos_Per2_Diag	StatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_		· – –		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDe				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwF			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_I				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f3		_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_Man			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_t target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDet		7		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SerisoriessHwPos_HwDet target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f3	-   •			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u0i				
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffse			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	262.5	262.5 ± 0.0009	/ /	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	691.429993	691.429993 ± 0.1	•	
AbsHwPos HwPosSource Cnt M u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVA	LID HWPOS_STATE_INPUTINVALID	•	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	691.429993	691.429993 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	691.429993	691.429993 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977	7331.25977 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		0	<b>V</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~	
target AbsHwPos Per2 HandwheelPosition HwDeg f32 value	691 429993	691 429993 + 0 1		

691.429993

65534

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

691.429993 ± 0.1

65534

2014-09-04, 15:47:58+0530



AbsHwPos\_Per2

Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1388.90002	1388.90002 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.47 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	-6321.02979		
AbsHwPos HandwheelAuthority Uls M f32	0.460000008		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-980.599976		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.460000008		
AbsHwPos_HwPosSource_Cnt_M_u16	6440		
AbsHwPos HwPosState Cnt M enum	7		
AbsHwPos PrevHandwheelPosition HwDeg M f32	272.100006		
AbsHwPos RelHwPos HwDeg M f32	272.100006		
AbsHwPos SrlComHwPosStatus Cnt M u16	25017		
AbsHwPos SrlComHwPos HwDeg M f32	4632.1499		
AbsHwPos TargetHwAuthority Uls M f32	0.460000008		
AbsHwPos_TempHwPos_HwDeg_M_f32	-832.169983		
AbsHwPos VehCntrOffset HwDeg_M_132	7225.2998		
AbsHwPos VehCntrOfstLearn Cnt M lqc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
		strOffeet CatEgrarCtatus EgrarCtatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	ntronset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_UIs_f32	0.44999988		
k_HwPosOutputLPFError_HwDeg_f32	139.699997		
k_I2CHwAuthority_Uls_f32	0.44999988		
k_MaxSensorlessAuthority_Uls_f32	0.540000021		
k_MaxVehCntrOffDiff_HwDeg_f32	1505.90002		
k_MinSensorlessAuthority_Uls_f32	0.44999988		
k_TurnsCntrAuthority_UIs_f32	0.449999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1357.40002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.540000021		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	984.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	254		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1419.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5579		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AL II D. AE. III D. II D. M.00	070 400000	070 400000 + 0 0000	

272.100006

0.460000008

-912.322144

-912.322144

HWPOS\_STATE\_INPUTINVALID

6440

 $AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32$ 

AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

 $AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32$ 

272.100006 ± 0.0009

0.460000008 ± 0.004

HWPOS\_STATE\_INPUTINVALID

-912.322205 ± 0.1

-912.322205 ± 0.1

6440

AbsHwPos\_Per2



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25017	25017	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4632.1499	4632.1499 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.460000008	0.460000008 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-832.169983	-832.169983 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998	7225.2998 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000008	0.460000008 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-912.322144	-912.322205 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	6440	6440	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	25017	25017	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1419.09998	1419.09998 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32         6461.77002           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.469999999           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         -960.900024           AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32         0.469999999           AbsHwPos_HwPosSource_Cnt_M_unf         448           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INIT           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         281.700012           AbsHwPos_RelHwPos_HwDeg_M_f32         281.700012           AbsHwPos_SifComHwPosStatus_Cnt_M_u16         25089           AbsHwPos_SifComHwPos_HwDeg_M_f32         4772.89014           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.469999999           AbsHwPos_TempHwPos_HwDeg_M_f32         0.469999999           AbsHwPos_VehCntrOffset_HwDeg_M_f32         1119.32996           AbsHwPos_VehCntrOffstet_HwDeg_M_f32         -1119.32996           AbsHwPos_VehCntrOffstet_GetErrorStatus(ErrorStatus)         1           Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)         target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus           Rt_Inst_Ap_AbsHwPos         target_Rte_Call_Ap_AbsHwPos           k_HwPosAuthority_Uls_f32         0.460000008           k_HwPosAuthority_Uls_f32         0.4600000008           k_HwAxSensorfessAuthority_Uls_f32	
AbsHwPos_HandwheelAuthority_Uls_M_f32         0.469999999           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         -960.900024           AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32         0.469999999           AbsHwPos_HwPosSource_Cnt_M_u16         448           AbsHwPos_HwPosSource_Cnt_M_u16         448           AbsHwPos_HwPosState_Cnt_M_enum         HWPOs_STATE_INIT           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         281.700012           AbsHwPos_RelHwPos_HwDeg_M_f32         281.700012           AbsHwPos_SifComHwPosStatus_Cnt_M_u16         25089           AbsHwPos_STcomHwPosHwDeg_M_f32         4772.89014           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.46999999           AbsHwPos_TempHwPos_HwDeg_M_f32         972.909973           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1119.32996           AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc         1           AbsHwPos_VehCntrOffset_GetErrorStatus(ErrorStatus)         1arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus           Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         1arget_Rte_Inst_Ap_AbsHwPos           k_HwPosAuthoritystep_Uls_f32         0           k_HwPosAuthority_Uls_f32         0.460000008           k_HwPosCoUtputLPFError_HwDeg_f32         0.460000008           k_HwAsXenoritesAuthority_Uls_f32         0.550000012	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         -960.900024           AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32         0.469999999           AbsHwPos_HwPosSource_Cnt_M_u16         448           AbsHwPos_HvPosState_Cnt_M_enum         HWPOS_STATE_INIT           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         281.700012           AbsHwPos_RelHwPos_HwDeg_M_f32         281.700012           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         25089           AbsHwPos_SrlComHwPos_HwDeg_M_f32         -4772.89014           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.469999999           AbsHwPos_TempHwPos_HwDeg_M_f32         972.909973           AbsHwPos_VehCntrOffste_HwDeg_M_f32         -1119.32996           AbsHwPos_VehCntrOffste_am_Cnt_M_lgc         1           AbsHwPos_VehCntrOffste_am_Cnt_M_lgc         1           AbsHwPos_VehCntrOffste_GetErrorStatus(ErrorStatus)         target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus           Rte_call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         1           RtwPosAuthorityStep_Uls_f32         0           k_HwPosOutputLPFError_HwDeg_f32         169.399994           k_LSCHwAuthority_Uls_f32         0.460000008           k_MaxSensorlessAuthority_Uls_f32         0.550000012           k_MaxSensorlessAuthority_Uls_f32         0.550000012	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32         0.469999999           AbsHwPos_HwPosSource_Cnt_M_u16         448           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INIT           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         281.700012           AbsHwPos_RelHwPos_HwDeg_M_f32         281.700012           AbsHwPos_StrIComHwPosStatus_Cnt_M_u16         25089           AbsHwPos_StrIComHwPos_HwDeg_M_f32         4772.89014           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.469999999           AbsHwPos_TempHwPos_HwDeg_M_f32         972.909973           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1119.32996           AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         1           Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)         target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus           Rte_Inst_Ap_AbsHwPos         target_Rte_Inst_Ap_AbsHwPos           k_HwPosOutputLPFError_HwDeg_f32         169.399994           k_I2CHwAuthority_Uls_f32         0.460000008           k_MaxSensorlessAuthority_Uls_f32         0.550000012           k_MaxVehCntrOffDiff_HwDeg_f32         1535.59998	
AbsHwPos_HwPosSource_Cnt_M_u16         448           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INIT           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         281.700012           AbsHwPos_RelHwPos_HwDeg_M_f32         281.700012           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         25089           AbsHwPos_TclComHwPos_HwDeg_M_f32         4772.89014           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.469999999           AbsHwPos_TempHwPos_HwDeg_M_f32         972.909973           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1119.32996           AbsHwPos_VehCntrOffst_Learn_Cnt_M_lgc         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         1           Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)         target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus           Rte_Inst_Ap_AbsHwPos         target_Rte_Inst_Ap_AbsHwPos           k_HwPosOutput_PFError_HwDeg_f32         169.399994           k_I2CHwAuthority_Uls_f32         0.460000008           k_MaxSensorlessAuthority_Uls_f32         0.550000012           k_MaxVehCntrOffDiff_HwDeg_f32         1535.59998	
AbsHwPos HwPosState_Cnt_M_enum         HWPOS_STATE_INIT           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         281.700012           AbsHwPos_RelHwPos_HwDeg_M_f32         281.700012           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         25089           AbsHwPos_TargetHwAuthority_Uls_M_f32         4772.89014           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.469999999           AbsHwPos_TempHwPos_HwDeg_M_f32         972.909973           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1119.32996           AbsHwPos_VehCntrOffset_Cat_m_Cnt_M_lgc         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         1           Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)         target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus           Rte_Inst_Ap_AbsHwPos         EOLVehCntrOffSet_GetErrorStatus           k_HwPosAuthorityStep_Uls_f32         0           k_HwPosOutputLPFError_HwDeg_f32         169.399994           k_I2CHwAuthority_Uls_f32         0.46000008           k_MaxSensorlessAuthority_Uls_f32         0.550000012           k_MaxVehCntrOffDiff_HwDeg_f32         1535.59998	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       281.700012         AbsHwPos_RelHwPos_HwDeg_M_f32       281.700012         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       25089         AbsHwPos_SrlComHwPos_HwDeg_M_f32       4772.89014         AbsHwPos_TargetHwAuthority_UIs_M_f32       0.469999999         AbsHwPos_TempHwPos_HwDeg_M_f32       972.909973         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrOfstLearn_Cnt_M_igc       1         AbsHwPos_VehCntrValid_Cnt_M_igc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_UIs_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_UIs_f32       0.460000008         k_MaxSensorlessAuthority_UIs_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_RelHwPos_HwDeg_M_f32       281.700012         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       25089         AbsHwPos_SrlComHwPos_HwDeg_M_f32       4772.89014         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.469999999         AbsHwPos_TempHwPos_HwDeg_M_f32       972.909973         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       25089         AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4772.89014         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.469999999         AbsHwPos_TempHwPos_HwDeg_M_f32       972.909973         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_SrlComHwPos_HwDeg_M_f32       -4772.89014         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.469999999         AbsHwPos_TempHwPos_HwDeg_M_f32       972.909973         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrValid_Cnt_M_lgc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.55000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_TargetHwAuthority_Uls_M_f32       0.469999999         AbsHwPos_TempHwPos_HwDeg_M_f32       972.909973         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.55000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_TempHwPos_HwDeg_M_f32       972.909973         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1119.32996         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       1         Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)       target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus ErrorStatus         Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.55000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
AbsHwPos_VehCntrOfstLearn_Ont_M_lgc         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         1           Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)         target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus           Rte_Inst_Ap_AbsHwPos         target_Rte_Inst_Ap_AbsHwPos           k_HwPosAuthorityStep_Uls_f32         0           k_HwPosOutputLPFError_HwDeg_f32         169.399994           k_I2CHwAuthority_Uls_f32         0.460000008           k_MaxSensorlessAuthority_Uls_f32         0.550000012           k_MaxVehCntrOffDiff_HwDeg_f32         1535.59998	
AbsHwPos_VehCntrValid_Cnt_M_lgc 1  Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) 1  target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus 1  target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus 1  target_Rte_Call_Ap_AbsHwPos 2  target_Rte_Inst_Ap_AbsHwPos 3  k_HwPosAuthorityStep_Uls_f32 0  k_HwPosOutputLPFError_HwDeg_f32 169.399994  k_I2CHwAuthority_Uls_f32 0.46000008  k_MaxSensorlessAuthority_Uls_f32 0.55000012  k_MaxVehCntrOffDiff_HwDeg_f32 1535.59998	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)  Rte_Inst_Ap_AbsHwPos  k_HwPosAuthorityStep_Uls_f32  k_HwPosOutputLPFError_HwDeg_f32  k_2CHwAuthority_Uls_f32  k_MaxSensorlessAuthority_Uls_f32  k_MaxVehCntrOfffiff_HwDeg_f32  target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus  target_Rte_Call_Ap_AbsHwPos  target_Rte_Call_Ap_AbsHwPos  0  0  169.399994  169.399994  169.39900008  170.550000012  170.550000012	
Rte_Inst_Ap_AbsHwPos       target_Rte_Inst_Ap_AbsHwPos         k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
k_HwPosAuthorityStep_Uls_f32       0         k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_12CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.55000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
k_HwPosOutputLPFError_HwDeg_f32       169.399994         k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
k_I2CHwAuthority_Uls_f32       0.460000008         k_MaxSensorlessAuthority_Uls_f32       0.550000012         k_MaxVehCntrOffDiff_HwDeg_f32       1535.59998	
k_MaxSensorlessAuthority_Uls_f32         0.550000012           k_MaxVehCntrOffDiff_HwDeg_f32         1535.59998	
k_MaxVehCntrOffDiff_HwDeg_f32 1535.59998	
k_TurnsCntrAuthority_Uls_f32 0.460000008	
k_UseTurnsCntr_Cnt_lgc 1	
k_VehCntrOffValidLimit_HwDeg_f32 1387.09998	
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value 1	
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value 1	
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value 723.200012	
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value 0	
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value 0.550000012	
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value 1004.59998	
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value 12	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 -980.599976	
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 5703	
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus 4	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_clarget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_HwPosSource_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target_AbsHwPos_Per2_ManufMode_Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	281.700012	281.700012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.469999999	0.469999999 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-52.0092773	-52.0093002 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-52.0092773	-52.0093002 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	972.909973	972.909973 ± 0.1	<b>~</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996	-1119.32996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.469999999	0.469999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-52.0092773	-52.0093002 ± 0.1	<b>~</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.49 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6602.50977
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1547.30005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.479999989
AbsHwPos_HwPosSource_Cnt_M_u16	456
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-350.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-350.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25161
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4913.62988
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989
AbsHwPos_TempHwPos_HwDeg_M_f32	1113.65002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	1
k_HwPosOutputLPFError_HwDeg_f32	199.100006
k_I2CHwAuthority_Uls_f32	0.469999999
k_MaxSensorlessAuthority_Uls_f32	0.560000002
k_MaxVehCntrOffDiff_HwDeg_f32	1565.30005
k_MinSensorlessAuthority_Uls_f32	0.469999999
k_TurnsCntrAuthority_UIs_f32	0.469999999
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1416.80005
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	743.299988
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.560000002





Name	Input Value		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1024.69995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	16		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-960.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5827		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_( target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1339.14807	1339.14795 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	456	456	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1339.14807	1339.14795 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25161	25161	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4913.62988	4913.62988 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	1113.65002	1113.65002 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988	7013.37988 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1339.14807	1339.14795 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	456	456	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	25161	25161	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
toward Direct FOLVah Ontroffe at FOLVah Ontroffe at 114 Day 500	000 000004	000 000004 + 0 0000	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

-960.900024

-960.900024 ± 0.0009

Test Step 2.50 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-6743.25
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.49000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1567.40002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.49000001
AbsHwPos_HwPosSource_Cnt_M_u16	464
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	300.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	300.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25233
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5054.37012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.49000001
AbsHwPos_TempHwPos_HwDeg_M_f32	-1254.39001
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.479999989
k_HwPosOutputLPFError_HwDeg_f32	228.800003
k_I2CHwAuthority_Uls_f32	0.479999989

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 



AbsHwPos\_Per2

Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.569999993		
k_MaxVehCntrOffDiff_HwDeg_f32	1595		
k_MinSensorlessAuthority_Uls_f32	0.479999989		
k_TurnsCntrAuthority_Uls_f32	0.479999989		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1446.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	763.400024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.569999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1044.80005		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-941.200012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5951		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_el	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	300.899994	300.899994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	184.7229	184.7229 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	184.7229	184.7229 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-1254.39001	-1254.39001 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992	6907.41992 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000203	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	184.7229	184.7229 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.51 (Repeat Count = 1)		V
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6883.99023	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5	
AbsHwPos_HwPosSource_Cnt_M_u16	472	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	

AbsHwPos\_Per2





Name	Inner A Welling		
Name  Abaltupos Providendubas/Position Hurbor M #22	Input Value 310.5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_RelHwPos_HwDeg_M_f32	310.5		
	25305		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	5195.10986		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5		
AbsHwPos TempHwPos HwDeg M f32	1395.13		
AbsHwPos VehCntrOffset HwDeg M f32	269.209991		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target Rte Inst Ap AbsHwPos		
k HwPosAuthorityStep Uls f32	0.49000001		
k_HwPosOutputLPFError_HwDeg_f32	258.5		
k_I2CHwAuthority_Uls_f32	0		
k_MaxSensorlessAuthority_Uls_f32	0.579999983		
k MaxVehCntrOffDiff HwDeg f32	50.5999985		
k_MinSensorlessAuthority_Uls_f32	0.49000001		
k_TurnsCntrAuthority_UIs_f32	0.49000001		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1476.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	783.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.579999983		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1064.90002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	24		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-921.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6075		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Diag$	_( target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	310.5	310.5 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-59.1499634	-59.1500015 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-59.1499634	-59.1500015 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	269.209991	269.209991 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-59.1499634	-59.1500015 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
	1700 5	702 5 1 0 4	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg_f32	783.5 -921.5	783.5 ± 0.1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.52 (Repeat Count = 1)			•
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7024.72998		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.50999999	0.50999999	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	114.300003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.50999999		
AbsHwPos_HwPosSource_Cnt_M_u16	480		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	320.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	320.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25377		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5335.8501		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.50999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.5		
C_HwPosOutputLPFError_HwDeg_f32	288.200012		
<_I2CHwAuthority_Uls_f32	1		
C_MaxSensorlessAuthority_Uls_f32	0.589999974		
C_MaxVehCntrOffDiff_HwDeg_f32	80.3000031		
_MinSensorlessAuthority_Uls_f32	0.5		
<_TurnsCntrAuthority_Uls_f32	0.5		
<_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	1505.90002		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.589999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1085		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	28		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-901.799988		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	6199		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		PosReducedPerf Cnt Iqc	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAut		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPos		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode C		
rarget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAut	_	
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	,	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	y_0/!\_uv	
		Fyma atod Value	Dagui
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	320.100006	320.100006 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	465.843018	465.842987 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	465.843018	465.842987 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	•
AbsHwPos_TargetHwAuthority_UIs_M_f32	1	1 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012	-409.950012 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
	E04	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504		
	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	*none*	

465.843018

2

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

2

465.842987 ± 0.1

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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	803.599976	803.599976 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-901.799988	-901.799988 ± 0.0009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.53 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7165.47021		
AbsHwPos HandwheelAuthority Uls M f32	0.519999981		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	126.699997		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.519999981		
AbsHwPos_HwPosSource_Cnt_M_u16	488		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-507.799988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-507.799988		
AbsHwPos SrlComHwPosStatus Cnt M u16	25449		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5476.58984		
AbsHwPos TargetHwAuthority Uls M f32	0.519999981		
AbsHwPos TempHwPos HwDeg M f32	-1676.60999		
AbsHwPos VehCntrOffset HwDeg M f32	550.690002		
AbsHwPos VehCntrOfstLearn Cnt M lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCnt	rrOffeet GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_lnst_Ap_AbsHwPos target_Rte_lnst_Ap_AbsHwPos	IOII361_GEILITOIGIAIUS_EITOIGIAIUS	
k HwPosAuthorityStep Uls f32	0.50999999		
k HwPosOutputLPFError HwDeg f32	317.899994		
k I2CHwAuthority Uls f32	0.47999989		
	0.600000024		
k_MaxSensorlessAuthority_Uls_f32 k_MaxVehCntrOffDiff_HwDeg_f32	110		
k_MinSensorlessAuthority_Uls_f32	0.50999999		
k_TurnsCntrAuthority_Uls_f32 k UseTurnsCntr Cnt lgc	0.50999999		
k VehCntrOffValidLimit HwDeg f32	1535.59998		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	823.700012		
target AbsHwPos Per2 ManufMode Cnt enum.value	2		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.600000024		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1105.09998		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	32		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-882.099976		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	6323		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		educedParf Cnt Ido	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwE		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	<del>-</del>	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	-507.799988	-507.799988 ± 0.0009	- Count
. 1.5.5 55_,g631 IWI 50_1 IWD69_IW_102		33 33300 ± 0.0000	

0.479999989

HWPOS\_STATE\_SENSORVALID

489.139954

489.139954

21845

 $AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32$ 

 $AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32$ 

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16 AbsHwPos\_HwPosState\_Cnt\_M\_enum 0.479999989 ± 0.004

HWPOS\_STATE\_SENSORVALID

489.140015 ± 0.1

489.140015 ± 0.1

21845

AbsHwPos\_Per2



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	550.690002	550.690002 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	489.139954	489.140015 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	823.700012	823.700012 ± 0.1	~
target Pim FOI VehCntrOffset FOI VehCntrOffset HwDeg f32	-882 099976	-882 099976 + 0 0009	_

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-7306,20996
AbsHwPos HandwheelAuthority Uls M f32	0.529999971
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	139.100006
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.529999971
AbsHwPos HwPosSource Cnt M u16	9496
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INIT
AbsHwPos PrevHandwheelPosition HwDeg M f32	389.200012
AbsHwPos RelHwPos HwDeg M f32	389.200012
AbsHwPos SrlComHwPosStatus Cnt M u16	25521
AbsHwPos SrlComHwPos HwDeg M f32	5617.33008
AbsHwPos_TargetHwAuthority Uls M f32	0.529999971
AbsHwPos TempHwPos HwDeg M f32	1817.34998
AbsHwPos_TemphwFos_TwDeg_M_32	691.429993
	1
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos 0.519999981
C_HwPosAuthorityStep_Uls_f32	1111111
x_HwPosOutputLPFError_HwDeg_f32	347.600006
c_I2CHwAuthority_Uls_f32	0.519999981
x_MaxSensorlessAuthority_Uls_f32	0.610000014
x_MaxVehCntrOffDiff_HwDeg_f32	139.699997
x_MinSensorlessAuthority_Uls_f32	0.519999981
x_TurnsCntrAuthority_Uls_f32	0
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	1
x_VehCntrOffValidLimit_HwDeg_f32	1565.30005
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.610000014
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1125.19995
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-862.400024
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6447
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatu	4
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedP	erf_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_	f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f	32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u1	16 target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



AbsHwPos\_Per2

Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	389.200012	389.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4173.37207	-4173.37207 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4173.37207	-4173.37207 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993	691.429993 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 2.55 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	7446.9502
AbsHwPos HandwheelAuthority Uls M f32	0.540000021
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	151.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.540000021
AbsHwPos_HwPosSource_Cnt_M_u16	504
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-468.399994
AbsHwPos_RelHwPos_HwDeg_M_f32	-468.399994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25593
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5758.06982
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.540000021
AbsHwPos_TempHwPos_HwDeg_M_f32	1958.08997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-832.169983
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.529999971
k_HwPosOutputLPFError_HwDeg_f32	377.299988
k_I2CHwAuthority_UIs_f32	0.529999971
k_MaxSensorlessAuthority_Uls_f32	0.620000005
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994
k_MinSensorlessAuthority_Uls_f32	0.529999971
k_TurnsCntrAuthority_Uls_f32	1
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1595
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	863.900024
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.620000005
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1145.30005





Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-842.700012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6571		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsAbsHwPosReducedPerf\_Inst\_AbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsA$	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-468.399994	-468.399994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4540.41504	4540.41504 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4540.41504	4540.41504 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8279.12012	8279.12012 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	8279.12012	8279.12012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-832.169983	-832.169983 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	<b>~</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
torget Dire FOLVah Catroffeet FOLVah Catroffeet Lluchar (22)	040 700040	040 700040 + 0 0000	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>V</b>	

-842.700012

-842.700012 ± 0.0009

Test Step 2.56 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7587.68994
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.550000012
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	163.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.550000012
AbsHwPos_HwPosSource_Cnt_M_u16	512
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	784.099976
AbsHwPos_RelHwPos_HwDeg_M_f32	7844.1001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25665
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5898.81006
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.550000012
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2521.05005
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.540000021
k_HwPosOutputLPFError_HwDeg_f32	50.5999985
k_I2CHwAuthority_Uls_f32	0.540000021

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 





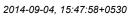
Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.629999995		
k_MaxVehCntrOffDiff_HwDeg_f32	199.100006		
k_MinSensorlessAuthority_Uls_f32	0.540000021		
k_TurnsCntrAuthority_UIs_f32	0.479999989		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	884		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.629999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1165.40002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-823		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6695		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7844.1001	7844.1001 ± 0.009	~
AbsHwPos HandwheelAuthority Ills M f32	0.479999989	0.479999989 + 0.004	<b>V</b>

target_itte_inst_Ap_Absrive os.Fiiii_EOE verionitionset	target_Filli_LOLVEIIOHilOliset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7844.1001	7844.1001 ± 0.009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2860.40674	2860.40698 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2860.40674	2860.40698 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5066.63965	5066.64014 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5066.63965	5066.64014 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2521.05005	2521.05005 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.57 (Repeat Count = 1)		<b>~</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7728.43018	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.560000002	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	176.300003	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.560000002	
AbsHwPos_HwPosSource_Cnt_M_u16	520	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	

AbsHwPos\_Per2





Name	Input Value		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	966.200012		
AbsHwPos_RelHwPos_HwDeg_M_f32	9665.2002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25737		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6039.5498		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.560000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1261.79004		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.550000012		
k_HwPosOutputLPFError_HwDeg_f32	80.3000031		
k_I2CHwAuthority_UIs_f32	0.550000012		
k_MaxSensorlessAuthority_UIs_f32	0.639999986		
k_MaxVehCntrOffDiff_HwDeg_f32	228.800003 0.550000012		
k_MinSensorlessAuthority_UIs_f32 k_TurnsCntrAuthority_UIs_f32	0.550000012		
k_UseTurnsCntr_Cnt_lgc	0.556666612		
k VehCntrOffValidLimit HwDeq f32	80.3000031		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998		
target AbsHwPos Per2 ManufMode Cnt enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.639999986		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1185.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	48		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-803.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		ReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthorit		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	·		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	9665.2002	9665.2002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.639999986	0.639999986 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	741.452026	741.452026 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	741.452026	741.452026 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1185.5	1185.5 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.63999986	0.63999986 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	1185.5	1185.5 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1261.79004	-1261.79004 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>V</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>V</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.639999986	0.639999986 ± 0.004	<b>V</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	741.452026	741.452026 ± 0.1	<b>V</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target AbsHwPos Per2 SrlComHwPosStatus Cnt u16.value	3 21845	3 21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1185.5	1185.5 ± 0.1	
target_Pim_FOI_VehCntrOffset_FOI_VehCntrOffset_HwDeg_f32	-803 299988	-803 299988 + 0 0009	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

-803.299988

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

-803.299988 ± 0.0009



Test Step 2.58 (Repeat Count = 1)	Innut Wales		
Name AboliviDea Alfonso (Court Mar Dea Llow Dea M. (60)	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7869.16992		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.569999993		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-941.200012 0.569999993		
AbsHwPos_HuRosSource_Cnt_M_u16	528		
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos HwPosState Cnt M enum	5		
AbsHwPos_revHandwheelPosition_HwDeg_M_f32	-409.299988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-409.299988		
AbsHwPos_srlComHwPosStatus_Cnt_M_u16	25809		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos TargetHwAuthority Uls M f32	0.569999993		
AbsHwPos TempHwPos HwDeg M f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003		
AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.560000002		
k HwPosOutputLPFError HwDeg f32	110		
k_I2CHwAuthority_Uls_f32	0.560000002		
k_MaxSensorlessAuthority_Uls_f32	0.649999976		
k_MaxVehCntrOffDiff_HwDeg_f32	258.5		
k_MinSensorlessAuthority_Uls_f32	0.560000002		
k_TurnsCntrAuthority_UIs_f32	0.560000002		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	110		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.649999976		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1205.59998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	52		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-783.599976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	-409.299988	-409.299988 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	952.06073	952.06073 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	952.06073	952.06073 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006	2380.31006 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003	2802.53003 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbsHwPos VehCntrValid Cnt M Igc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	952.06073	952.06073 ± 0.1	

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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-783.599976	-783.599976 ± 0.0009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.59 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	8009.91016		
AbsHwPos HandwheelAuthority Uls M f32	0.579999983		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-921.5		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.579999983		
AbsHwPos_HwPosSource_Cnt_M_u16	536		
AbsHwPos HwPosState Cnt M enum	6		
AbsHwPos PrevHandwheelPosition HwDeg M f32	213		
AbsHwPos RelHwPos HwDeg M f32	213		
AbsHwPos SrlComHwPosStatus Cnt M u16	25881		
AbsHwPos SrlComHwPos HwDeg M f32	-6321.02979		
AbsHwPos TargetHwAuthority Uls M f32	0.579999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos VehCntrOffset HwDeg_M_132	2943.27002		
AbsHwPos VehCntrOfstLearn Cnt M lqc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
		trOffeet CetErrerStatus ErrerStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	tronset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_UIs_f32	0.569999993		
k_HwPosOutputLPFError_HwDeg_f32	139.699997		
k_I2CHwAuthority_Uls_f32	0.569999993		
k_MaxSensorlessAuthority_Uls_f32	0.660000026		
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.56999993		
k_TurnsCntrAuthority_UIs_f32	0.569999993		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	139.699997		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.660000026		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1225.69995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	56		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-763.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7067		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	· <b>-</b> -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	·-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AL II D. AE. III D. II D. M.00	040	040 + 0 0000	

213

536

0.579999983

1075.17896

1075.17896

HWPOS\_STATE\_INPUTINVALID

213 ± 0.0009

536

0.579999983 ± 0.004

HWPOS\_STATE\_INPUTINVALID

1075.17896 ± 0.1

1075.17896 ± 0.1

AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32 AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25881	25881	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979	-6321.02979 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.579999983	0.579999983 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002	2943.27002 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.579999983	0.579999983 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1075.17896	1075.17896 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	536	536	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	25881	25881	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-763.900024	-763.900024 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.60 (Repeat Count = 1)           Name         Input Value           AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32         -10125           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.589999974           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         324.5	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 -10125 AbsHwPos_HandwheelAuthority_Uls_M_f32 0.589999974	
AbsHwPos_HandwheelAuthority_Uls_M_f32 0.589999974	
- '	
7.867 W 05_10.10 W 05_10.10 V 05_05_05_05_05_05_05_05_05_05_05_05_05_0	
AbsHwPos HandwheelPositionLPF M str.K Uls f32 0.589999974	
AbsHwPos HwPosSource Cnt M u16 544	
AbsHwPos HwPosState Cnt M enum 7	
AbsHwPos PrevHandwheelPosition HwDeg M f32 -369.899994	
AbsHwPos ReiHwPos HwDeg M f32 -369.899994	
AbsHwPos SrlComHwPosStatus Cnt M u16 25953	
AbsHwPos SrlComHwPos HwDeg M f32 6461.77002	
AbsHwPos TargetHwAuthority Uls M f32 0.58999974	
AbsHwPos TempHwPos HwDeg M f32 -2661.79004	
AbsHwPos VehCntrOffset HwDeg M f32 -1084.01001	
AbsHwPos VehCntrOfstLearn Cnt M lqc 1	
AbsHwPos VehCntrValid Cnt M Igc 1	
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus) target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos target Rte Inst Ap AbsHwPos	
k HwPosAuthorityStep Uls f32 0.579999983	
k_HwPosOutputLPFError_HwDeg_f32 169.399994	
k I2CHwAuthority Uls f32 0.57999983	
k MaxSensorlessAuthority Uls f32 0.670000017	
k MaxVehCntrOffDiff HwDeg f32 317.899994	
k MinSensorlessAuthority Uls f32 0.57999983	
k_TurnsCntrAuthority_UIs_f32 0.579999983	
k_UseTurnsCntr_Cnt_lgc 1	
k_VehCntrOffValidLimit_HwDeg_f32 169.399994	
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value 1	
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value 1	
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value 160.399994	
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value 0	
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value 0.670000017	
target AbsHwPos Per2 SensorlessHwPos HwDeg_f32.value 1245.80005	
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value 60	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 -744.200012	
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 7191	
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus_4	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_HwPosSource_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target_AbsHwPos_Per2_ManufMode_Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1437.41101	-1437.41113 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001	-1084.01001 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.61 (Repeat Count = 1)	<u> </u>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.600000024
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024
AbsHwPos_HwPosSource_Cnt_M_u16	7552
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26025
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.589999974
k_HwPosOutputLPFError_HwDeg_f32	199.100006
k_I2CHwAuthority_Uls_f32	0.589999974
k_MaxSensorlessAuthority_Uls_f32	0.680000007
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006
k_MinSensorlessAuthority_Uls_f32	0.589999974
k_TurnsCntrAuthority_UIs_f32	0.589999974
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	199.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.680000007

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Name	Input Value		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1265.90002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	64		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-724.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7315		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	s 5		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPersUnited StatusHwPosReducedPersUnited StatusHwPosReducedPersUnit$	erf_( target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32$	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_Results = 1.000 + $	f32 target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc$	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32$	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_fa	32 target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u1000000000000000000000000000000000000$	16 target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75	3224.75 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
torget AbelliuDes Dary CriComliuDes LiuDes 622 value	4000	4000 + 0.4	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

1600 ± 0.1

65535 ± 0.09

1600

65535

Test Step 2.62 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.610000014
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-488.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.610000014
AbsHwPos_HwPosSource_Cnt_M_u16	560
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	140.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	140.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26097
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.610000014
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3365.48999
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.600000024

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

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AbsHwPos\_Per2

Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	228.800003		
k_I2CHwAuthority_UIs_f32	0.600000024		
k_MaxSensorlessAuthority_Uls_f32	0.689999998		
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
k_MinSensorlessAuthority_Uls_f32	0.600000024		
k_TurnsCntrAuthority_UIs_f32	0.600000024		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.689999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1286		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1872.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7439		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	2 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1605.03577	1605.03564 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1605.03577	1605.03564 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3365.48999	3365.48999 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.63 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8572.87012	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.620000005	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	765.799988	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.620000005	

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AbsHwPos\_Per2

Absnwros_reiz			00
Name	Input Value		
AbsHwPos_HwPosSource_Cnt_M_u16	568		
AbsHwPos_HwPosState_Cnt_M_enum	10		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	160.399994		
AbsHwPos_RelHwPos_HwDeg_M_f32	160.399994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26169		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.620000005		
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	-3084.01001 -1506.22998		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target Rte Inst Ap AbsHwPos	ar onoce_ociemorotatao_errorotatao	
k_HwPosAuthorityStep_Uls_f32	0.610000014		
k HwPosOutputLPFError HwDeg f32	258.5		
k I2CHwAuthority Uls f32	0.610000014		
k_MaxSensorlessAuthority_Uls_f32	0.69999988		
k_MaxVehCntrOffDiff_HwDeg_f32	407		
k_MinSensorlessAuthority_Uls_f32	0.610000014		
k_TurnsCntrAuthority_Uls_f32	0.610000014		
k UseTurnsCntr Cnt Igc	0		
k_VehCntrOffValidLimit_HwDeg_f32	258.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.69999988		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1306.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	72		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1902.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7563		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.620000005	0.620000005 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1621.08228	-1621.08215 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	568	568	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1621.08228	-1621.08215 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26169	26169	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023	6883.99023 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.620000005	0.620000005 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1506.22998	-1506.22998 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.620000005	0.620000005 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	568	568	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26169	26169	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	-
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.64 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	-8713.61035		
AbsHwPos HandwheelAuthority Uls M f32	0.629999995		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	912.900024		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.629999995		
AbsHwPos HwPosSource Cnt M u16	576		
AbsHwPos_HwPosState_Cnt_M_enum	11		
AbsHwPos PrevHandwheelPosition HwDeg M f32	180.5		
AbsHwPos RelHwPos HwDeg M f32	180.5		
AbsHwPos SrlComHwPosStatus Cnt M u16	26241		
AbsHwPos SrlComHwPos HwDeg M f32	7024.72998		
AbsHwPos TargetHwAuthority Uls M f32	0.629999995		
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3646.96997		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
		hCatrOffact CatErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	IICIIIIOIISEL_GELEITOI Status_EITOI Status	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.620000005		
k_HwPosOutputLPFError_HwDeg_f32	288.200012		
k_I2CHwAuthority_Uls_f32	0.620000005		
k_MaxSensorlessAuthority_UIs_f32	0.709999979		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.620000005		
k_TurnsCntrAuthority_Uls_f32	0.620000005		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	288.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.709999979		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1326.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	76		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1932.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7687		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Other and the property of the property $	target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	nority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutl	nority_Uls_f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwF	Pos HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	- <del>,                                   </del>	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<i>7</i> = =	
Name	Actual Value	Expected Value	Resul
AbsHwPos AlignedHwPos HwDeg M f32	180.5	180.5 ± 0.0009	1.0301
. 155 55_, signout int 50_1 in 50g_in_152	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos HandwheelAuthority Ills M f22		2369.36548 ± 0.1	
AbsHwPos_HandwheelPositionI PE_M_str_SV_Llls_f32	2360 36572	∠JUJ.JUJ4U ⊥ U. I	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2369.36572	GEE24	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	65534 HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID 2369.36572	HWPOS_STATE_INPUTINVALID 2369.36548 ± 0.1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	65534 HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•

3224.75

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

0 ± 0.004

3224.75 ± 0.1

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AbsHwPos\_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3646.96997	3646.96997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1932.5	1932.5 ± 0.009	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9558.0498 -
AbsHwPos_HandwheelAuthority_Uls_M_f32	0
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-429
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.68999998
AbsHwPos_HwPosSource_Cnt_M_u16	624
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	301.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26313
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1889.09998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.689999998
AbsHwPos_TempHwPos_HwDeg_M_f32	4069.18994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4491.41016
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
HwPosAuthorityStep Uls f32	0.680000007
HwPosOutputLPFError HwDeg f32	466.399994
C I2CHwAuthority Uls f32	0.680000007
 :_MaxSensorlessAuthority_Uls_f32	0.769999981
MaxVehCntrOffDiff HwDeg f32	614.900024
MinSensorlessAuthority Uls f32	0.68000007
TurnsCntrAuthority Uls f32	0.680000007
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	1
VehCntrOffValidLimit HwDeg f32	466.399994
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	461.899994
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.769999981
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1446.80005
	100
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	1
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2113.69995
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8431
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorSta	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduced	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f	0 = = =
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDe	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_I	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f3	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg	j_f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_	u16 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f3:	2 target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2674.75098	2674.75122 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2674.75098	2674.75122 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4069.18994	4069.18994 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4491.41016	4491.41016 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2113.69995	2113.69995 ± 0.009	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.66 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	9698.79004
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	147.300003
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.69999988
AbsHwPos HwPosSource Cnt M u16	632
AbsHwPos HwPosState Cnt M enum	5
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	321.26001
AbsHwPos_RelHwPos_HwDeg_M_f32	3421.26001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26385
AbsHwPos_SrlComHwPos_HwDeg_M_f32	909.380005
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	4209.93018
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4632.1499
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.68999998
k_HwPosOutputLPFError_HwDeg_f32	496.100006
k_I2CHwAuthority_Uls_f32	0.68999998
k_MaxSensorlessAuthority_Uls_f32	0.779999971
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976
k_MinSensorlessAuthority_Uls_f32	0.68999998
k_TurnsCntrAuthority_UIs_f32	0.68999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	496.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.779999971
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1466.90002
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	104
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2143.8999





Name	Input Value			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8555			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	rget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	itus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	3421.26001	3421.26001 ± 0.009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988	0.699999988 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2991.14111	2991.14111 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	632	632	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2991.14111	2991.14111 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26385	26385	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988	0.69999988 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	4209.93018	4209.93018 ± 0.1	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4632.1499	4632.1499 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.69999988	0.69999988 ± 0.004	•	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	632	632	•	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26385	26385	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	909.380005	909.380005 ± 0.1	~	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

2143.8999

2143.8999 ± 0.009

Test Step 2.67 (Repeat Count = 1)	<b>→</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9839.53027
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.709999979
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	156.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.709999979
AbsHwPos_HwPosSource_Cnt_M_u16	640
AbsHwPos_HwPosState_Cnt_M_enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	341.540009
AbsHwPos_RelHwPos_HwDeg_M_f32	341.540009
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26457
AbsHwPos_SrlComHwPos_HwDeg_M_f32	929.659973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.709999979
AbsHwPos_TempHwPos_HwDeg_M_f32	-4350.66992
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1572.89001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	525.799988
k_I2CHwAuthority_UIs_f32	0.69999988
k_MaxSensorlessAuthority_Uls_f32	0.790000021
k_MaxVehCntrOffDiff_HwDeg_f32	674.299988
k_MinSensorlessAuthority_Uls_f32	0.69999988

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 





7.1567.W		• • • • • • • • • • • • • • • • • • • •	
Name	Input Value		
k_TurnsCntrAuthority_Uls_f32	0.69999988		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	525.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.790000021		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1487		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	108		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2174.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8679		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	_( target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Ci	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	id_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	341.540009	341.540009 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-3043.47461	-3043.47461 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-3043.47461	-3043.47461 ± 0.1	-
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	·

AbsHwPos_HandwheelAuthority_Uls_M_f32	AbsHwPos_AlignedHwPos_HwDeg_M_f32	341.540009	341.540009 ± 0.0009	~
AbsHwPos_HwPosSource_Cnt_M_enum         65534         65534           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         -3043.47461         -3043.47461 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_g16         65535         65535           AbsHwPos_CntOmHwPos_HwDeg_M_f32         0         0 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0         0 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         -1572.89001         -1572.89001 ± 0.009           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1572.89001         -1572.89001 ± 0.009           AbsHwPos_VehCntrOffst_earn_Cnt_M_gc         1         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value         -1600         -1600 ± 0.1           target_AbsHwPos_Per2_HmPosSource_Cnt_u16.value         65534         65535           target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value         65535         65535           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value         0         0 ± 0.1 </td <td>AbsHwPos_HandwheelAuthority_Uls_M_f32</td> <td>0.00999999046</td> <td>0.0099999978 ± 0.004</td> <td>~</td>	AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.0099999978 ± 0.004	~
AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_INPUTINVALID         HWPOS_STATE_INPUTINVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         -3043.47461         -3043.47461 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         65535         65535           AbsHwPos_SrlComHwPos_HwDeg_M_f32         0         0 ± 0.1           AbsHwPos_TargetHwAuthority_UIs_M_f32         0         0 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         -4350.66992         -4350.66992 ± 0.1           AbsHwPos_VehCntrOffstLearn_Cnt_M_Igc         1         1           AbsHwPos_VehCntrValid_Cnt_M_Igc         0         0           AbsHwPos_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NtrDiagMgr_SetNTCStatus(Cnt_T_u08)         1         1           Rte_Call_Ap_AbsHwPos_NtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value         0.00999999046         0.0099999978 ± 0.004           target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value         65534         65534           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value         65535         65535           target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value         0         0 ± 0.1	AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-3043.47461	-3043.47461 ± 0.1	~
AbsHwPos PrevHandwheelPosition_HwDeg_M_f32       -3043.47461       -3043.47461 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1572.89001       -1572.89001 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         xtarget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         xtarget_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       65535       65535         AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.10         AbsHwPos_TargetHwAuthority_UIs_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1572.89001       -1572.89001 ± 0.009         AbsHwPos_VehCntrVofistLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value       0.00999999046       0.0099999978 ± 0.004         *target_AbsHwPos_Per2_HuPosSource_Cnt_u16.value       65534       65534         *target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         *target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0       0	AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32       0       0 ± 0.10         AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1572.89001       -1572.89001 ± 0.009         AbsHwPos_VehCntrVofistLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         vtarget_AbsHwPos_Per2_HuvPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-3043.47461	-3043.47461 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32       0       0 ± 0.004       ✓         AbsHwPos_TempHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1       ✓         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1572.89001       -1572.89001 ± 0.009       ✓         AbsHwPos_VehCntrVofistLearn_Cnt_M_lgc       1       1       ✓         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1       1       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1       1       ✓       ✓         Rte_Call_Ap_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004       ✓       ✓         target_AbsHwPos_Per2_HuvPosSource_Cnt_u16.value       65534       65534       ✓       ✓         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       ✓	AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_TempHwPos_HwDeg_M_f32       -4350.66992       -4350.66992 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1572.89001       -1572.89001 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       0         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1572.89001       -1572.89001 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       0         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_VehCntrOfstLearn_ont_M_lgc       1       1       ✓         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1       1       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1       1       ✓         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004       ✓         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1       ✓         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534       ✓         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       ✓       ✓         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1       ✓	AbsHwPos_TempHwPos_HwDeg_M_f32	-4350.66992	-4350.66992 ± 0.1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1       1       ✓         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1       1       ✓         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004       ✓         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1       ✓         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534       ✓         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       ✓       0       0 ± 0.1       ✓	AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1572.89001	-1572.89001 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.009999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_ont_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value       0.00999999046       0.0099999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value       0.00999999046       0.00999999978 ± 0.004         target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value       -1600       -1600 ± 0.1         target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value       65534       65534         target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value       65535       65535         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value       0       0 ± 0.1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value -1600 -1600 ± 0.1   target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value 65534 65534   target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 65535 65535   target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0 0 ± 0.1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value 65534 65534   target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 65535 65535  target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0 0 ± 0.1	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value 65535 65535  target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0 0 ± 0.1	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value 0 0 ± 0.1 ✓	target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
0	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 65535 ± 0.09   ✓	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
	target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.68 (Repeat Count = 1)		
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9980.26953	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.720000029	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	166.5	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.720000029	
AbsHwPos_HwPosSource_Cnt_M_u16	1	
AbsHwPos_HwPosState_Cnt_M_enum	7	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	361.820007	
AbsHwPos_RelHwPos_HwDeg_M_f32	361.820007	
AbsHwPos SrlComHwPosStatus Cnt M u16	26529	

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Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	949.940002		
AbsHwPos TargetHwAuthority Uls M f32	0.720000029		
AbsHwPos_TempHwPos_HwDeg_M_f32	4491.41016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4913.62988		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.709999979		
k HwPosOutputLPFError HwDeg f32	555.5		
k_I2CHwAuthority_Uls_f32	0.709999979		
k_MaxSensorlessAuthority_Uls_f32	0.800000012		
k MaxVehCntrOffDiff HwDeg f32	704		
k_MinSensorlessAuthority_Uls_f32	0.709999979		
k_TurnsCntrAuthority_UIs_f32	0.709999979		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	555.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012		
target AbsHwPos Per2 ManufMode Cnt enum.value	1		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.80000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1507.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	112		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2204.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8803		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u		ReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<u></u>	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	361.820007	361.820007 ± 0.0009	Result
	0.720000029		
AbsHwPos_HandwheelAuthority_Uls_M_f32		0.720000029 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3280.43555	3280.4353 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	1	1	
	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3280.43555	3280.4353 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwPos_M f22	26529	26529	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Llls_M_f32	949.940002 0.720000029	949.940002 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	4491.41016	0.720000029 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCatrOffeet_HwDeg_M_f32	4913.62988	4491.41016 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32		4913.62988 ± 0.009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc			-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	Ž
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0.720000029	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.720000029	0.720000029 ± 0.004	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

1600

26529

949.940002

2204.30005

1600 ± 0.1

949.940002 ± 0.1

2204.30005 ± 0.009

1

26529

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value



Test Step 2.69 (Repeat Count = 1)			·
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9874.30957		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.730000019		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	176.100006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.730000019		
AbsHwPos_HwPosSource_Cnt_M_u16	65534		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	382.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	382.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26601		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	970.219971		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.730000019		
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	4632.1499 5054.37012		
	1		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	hCntrOffeet GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	Honitonset_GetEnorGtatus_EnorGtatus	
k_HwPosAuthorityStep_Uls_f32	0.720000029		
k_HwPosOutputLPFError_HwDeg_f32	585.200012		
k_I2CHwAuthority_Uls_f32	0.720000029		
k MaxSensorlessAuthority Uls f32	0.810000002		
k MaxVehCntrOffDiff HwDeq f32	733.700012		
k MinSensorlessAuthority Uls f32	0.720000029		
k TurnsCntrAuthority Uls f32	0.720000029		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	585.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988		
target AbsHwPos Per2 ManufMode Cnt enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.810000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1527.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	116		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2234.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8927		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		PosReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cr		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuth	nority Uls f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosS		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidi		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	924 244	
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	382.100006	382.100006 ± 0.0009	Resul
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	3429.0166	3429.0166 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3429.0166	3429.0166 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos SrlComHwPos HwDeg M f32	0	0 ± 0.1	
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	4632.1499	4632.1499 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	5054.37012	5054.37012 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
50_+0.10.10.40.40_0.11_190	504	504	
Rte Call An AbsHwPos NxtrDiagMar SetNTCStatus(NTC Cot T enum)		301	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		1	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1 1 0.00999999046	1 0.00999999978 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	

65535

0

65535

0 ± 0.1

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

AbsHwPos\_Per2



Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9768.34961		
AbsHwPos HandwheelAuthority Uls M f32	0.74000001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	185.699997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.74000001		
AbsHwPos HwPosSource Cnt M u16	1024		
	9		
AbsHwPos_HwPosState_Cnt_M_enum			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	402.380005		
AbsHwPos_RelHwPos_HwDeg_M_f32	402.380005		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26673		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-990.5		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.74000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	-4772.89014		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5195.10986		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.730000019		
k_HwPosOutputLPFError_HwDeg_f32	614.900024		
x_I2CHwAuthority_Uls_f32	0.730000019		
<_MaxSensorlessAuthority_Uls_f32	0.819999993		
c_MaxVehCntrOffDiff_HwDeg_f32	763.400024		
C_MinSensorlessAuthority_Uls_f32	0.730000019		
<_TurnsCntrAuthority_Uls_f32	0.730000019		
<_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	614.900024		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-330.5		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.819999993		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1547.30005		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	120		
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2264.69995		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9051		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0		ReducedPerf Cnt lac	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target AbsHwPos Per2 HandwheelPosition		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessAuthoritarget_AbsHwPos_Per2_SensorlessHwPos		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	CIII_UU8	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resi
AbsHwPos_AlignedHwPos_HwDeg_M_f32	402.380005	402.380005 ± 0.0009	
Abel hu Dee Hendricheel Authority I He M 522	0.74000004	0.74000004 + 0.004	

Name	Actual value	Expected value	IXESUIL
AbsHwPos_AlignedHwPos_HwDeg_M_f32	402.380005	402.380005 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.74000001	0.74000001 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-3483.65698	-3483.65649 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1024	1024	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-3483.65698	-3483.65649 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26673	26673	~





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.74000001	0.74000001 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-4772.89014	-4772.89014 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5195.10986	5195.10986 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.74000001	0.74000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1024	1024	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26673	26673	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-990.5	-990.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2264.69995	2264.69995 ± 0.009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>~</b>

Гest Step 2.71 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9662.38965
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	195.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75
AbsHwPos_HwPosSource_Cnt_M_u16	1236
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	422.660004
AbsHwPos_ReIHwPos_HwDeg_M_f32	422.660004
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26745
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003
bsHwPos_TargetHwAuthority_Uls_M_f32	0.75
.bsHwPos_TempHwPos_HwDeg_M_f32	4913.62988
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1.
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.74000001
_HwPosOutputLPFError_HwDeg_f32	644.599976
I2CHwAuthority_Uls_f32	0.74000001
_MaxSensorlessAuthority_Uls_f32	0.829999983
: MaxVehCntrOffDiff HwDeg f32	793.099976
MinSensorlessAuthority Uls f32	0.74000001
TurnsCntrAuthority Uls f32	0.74000001
UseTurnsCntr Cnt Igc	1
VehCntrOffValidLimit HwDeg f32	644.599976
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-310.799988
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.829999983
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1567.40002
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	124
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2294.8999
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9175
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	422.660004	422.660004 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75	0.75 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3734.04761	3734.04761 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1236	1236	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3734.04761	3734.04761 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003	1010.78003 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75	0.75 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4913.62988	4913.62988 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965	8284.88965 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75	0.75 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1236	1236	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1010.78003	1010.78003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2294.8999	2294.8999 ± 0.009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.72 (Repeat Count = 1)	💌
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9556.42969
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	204.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75999999
AbsHwPos_HwPosSource_Cnt_M_u16	1348
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	442.940002
AbsHwPos_RelHwPos_HwDeg_M_f32	442.940002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.75
k_HwPosOutputLPFError_HwDeg_f32	674.299988
k_I2CHwAuthority_Uls_f32	0.75
k_MaxSensorlessAuthority_Uls_f32	0.839999974
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988
k_MinSensorlessAuthority_Uls_f32	0.75
k_TurnsCntrAuthority_Uls_f32	0.75
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	674.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value\\ target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

Input Value



Name



target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwD	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	:nt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75999999	0.75999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>~</b>

1348

26817

123.400002

2325.1001

1348

26817

123.400002 ± 0.1

2325.1001 ± 0.009

Test Step 2.73 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9450.46973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	214.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.769999981
AbsHwPos_HwPosSource_Cnt_M_u16	1460
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	463.220001
AbsHwPos_RelHwPos_HwDeg_M_f32	463.220001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26889
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.769999981
AbsHwPos_TempHwPos_HwDeg_M_f32	5195.10986
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.75999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_UIs_f32	0.75999999
k_MaxSensorlessAuthority_Uls_f32	0.850000024
k_MaxVehCntrOffDiff_HwDeg_f32	852.5





AbsHwPos_Per2		MAC	Silab
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.75999999		
k_TurnsCntrAuthority_UIs_f32	0.75999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	704		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	132		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuth	nority Uls f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuth	pority Uls f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos HwDeg f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosS		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos	HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	-	
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	463.220001	463.220001 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4049.56958	4049.56958 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID	HWPOS STATE SENSORINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	4049.56958	4049.56958 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65534	65534	
AbsHwPos SrlComHwPos HwDeg M f32	-248.300003	-248.300003 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	5195.10986	5195.10986 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	-1072.96997	-1072.96997 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	٠,
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	504	
Di O II A AL II D. AL			

Test Step Call Trace				✓.
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

0.00999999046

1600

65534

65534

65535

-248.300003

0.00999999978 ± 0.004

-248.300003 ± 0.1

65535 ± 0.09

1600 ± 0.1

65534

65534

Test Step 2.74 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9662.38965	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	195.300003	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75	
AbsHwPos_HwPosSource_Cnt_M_u16	1236	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	422.660004	
AbsHwPos_RelHwPos_HwDeg_M_f32	422.660004	

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)
Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

AbsHwPos\_Per2

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ADSTWPOS_Per2		I GIZO	TOOL
Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26745		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75		
AbsHwPos_TempHwPos_HwDeg_M_f32	4913.62988		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.74000001		
k_HwPosOutputLPFError_HwDeg_f32	644.599976		
k_I2CHwAuthority_Uls_f32	0.74000001		
k_MaxSensorlessAuthority_Uls_f32	0.829999983		
k_MaxVehCntrOffDiff_HwDeg_f32	793.099976		
k_MinSensorlessAuthority_Uls_f32	0.74000001		
k TurnsCntrAuthority Uls f32	0.74000001		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	644.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-310.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.829999983		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1567.40002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	124		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2294.8999		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9175		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		ReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosStati		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	on <u>_</u> uoo	
Name	Actual Value	Expected Value	Pocul
	422.660004	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32		422.660004 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.829999983	0.829999983 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1224.375	1224.375 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbshwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1224.375	1224.375 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.829999983	0.829999983 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	-

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

0

0

0

0

0.829999983

1224.375

1567.40002

2294.8999

21845

504

0

0

0

0

3

21845

0.829999983 ± 0.004

1224.375 ± 0.1

1567.40002 ± 0.1

2294.8999 ± 0.009

504

 $AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc$ 

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value$ 

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

 $AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc$ 



Test Step 2.75 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9344.5		
AbsHwPos HandwheelAuthority Uls M f32	0.779999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	224.100006		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.779999971		
AbsHwPos_HwPosSource_Cnt_M_u16	1572		
AbsHwPos HwPosState Cnt M enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725		
AbsHwPos_RelHwPos_HwDeg_M_f32	-600		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26961		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13		
AbsHwPos TargetHwAuthority Uls M f32	0.779999971		
AbsHwPos TempHwPos HwDeg M f32	-5335.8501		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	<del>_</del>	
k_HwPosAuthorityStep_Uls_f32	0.769999981		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.769999981		
k_MaxSensorlessAuthority_Uls_f32	0.860000014		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.76999981		
k_TurnsCntrAuthority_UIs_f32	0.769999981		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	733.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	136		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2385.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	thority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-600	-600 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971	0.779999971 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4112.66113	-4112.66113 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4112.66113	-4112.66113 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26961	26961	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.779999971	0.779999971 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	-5335.8501	-5335.8501 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977	7967.00977 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.779999971	0.779999971 ± 0.004	
		1	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26961	26961	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1395.13	1395.13 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2385.5	2385.5 ± 0.009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.76 (Repeat Count = 1) Name	Input Value		
	9238.54004		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	0.790000021		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-389.600006		
	0.790000021		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1684		
AbsHwPos_HwPosSource_Cnt_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725		
AbsHwPos_RelHwPos_HwDeg_M_f32	6008		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27033		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1535.87		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.79000021		
AbsHwPos_TempHwPos_HwDeg_M_f32	5476.58984		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.779999971		
C_HwPosOutputLPFError_HwDeg_f32	763.400024		
c_I2CHwAuthority_Uls_f32	0.779999971		
x_MaxSensorlessAuthority_Uls_f32	0.870000005		
:_MaxVehCntrOffDiff_HwDeg_f32	911.900024		
_MinSensorlessAuthority_Uls_f32	0.779999971		
c_TurnsCntrAuthority_Uls_f32	0.779999971		
_UseTurnsCntr_Cnt_lgc	1		
c_VehCntrOffValidLimit_HwDeg_f32	763.400024		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-232		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	ReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDog f32	target AbsHwPos Per2 SensorlessHwPos	, <b>-</b> -	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPosStatu	· · · · ·	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target AbsHwPos Per2 SrlComHwPos Hwl		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	· <del>-</del>	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	5450	
		Function Value	
Name	Actual Value	Expected Value	Resi
AbsHwPos_AlignedHwPos_HwDeg_M_f32  AbsHwPos_HandwheelAuthority_LUs_M_f32	0.010000501	6008 ± 0.009	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	6008	6008 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4244.68994	4244.68994 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4244.68994	4244.68994 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~





Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5476.58984	5476.58984 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498	7861.0498 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

est Step 2.77 (Repeat Count = 1)	
ame	Input Value
osHwPos_AlignedCumMtrPos_HwDeg_M_f32	-9132.58008
osHwPos_HandwheelAuthority_Uls_M_f32	0.800000012
osHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	243.300003
osHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.800000012
osHwPos_HwPosSource_Cnt_M_u16	1796
osHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
psHwPos_PrevHandwheelPosition_HwDeg_M_f32	0
psHwPos_RelHwPos_HwDeg_M_f32	7150
osHwPos_SrlComHwPosStatus_Cnt_M_u16	27105
osHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999
osHwPos_TargetHwAuthority_Uls_M_f32	0.800000012
osHwPos_TempHwPos_HwDeg_M_f32	5617.33008
osHwPos_VehCntrOffset_HwDeg_M_f32	-1455.08997
osHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
osHwPos_VehCntrValid_Cnt_M_lgc	0
te_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
te_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.790000021
HwPosOutputLPFError HwDeg f32	793.099976
I2CHwAuthority Uls f32	0.790000021
MaxSensorlessAuthority Uls f32	0.879999995
MaxVehCntrOffDiff HwDeg f32	941.599976
MinSensorlessAuthority Uls f32	0.790000021
TurnsCntrAuthority Uls f32	0.790000021
UseTurnsCntr Cnt lgc	0
VehCntrOffValidLimit HwDeg f32	793.099976
rget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
rget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
rget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-212.300003
rget AbsHwPos Per2 ManufMode Cnt enum.value	0
rget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.879999995
rget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-941.200012
rget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	144
rget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2445.8999
rget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9795
rget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	1
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
rget_Rte_inst_Ap_AbshwPos.AbshwPos_Per2_manuimode_Cnt_enum rget_Rte_inst_Ap_AbshwPos.AbshwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority Uls f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7150	7150 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012	0.800000012 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4542.52393	4542.52393 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	1796	1796	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4542.52393	4542.52393 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999	-1676.60999 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.800000012	0.800000012 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5617.33008	5617.33008 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1455.08997	-1455.08997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.800000012	0.800000012 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1796	1796	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2445.8999	2445.8999 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.78 (Repeat Count = 1)	<b>√</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9026.62012
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.810000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	252.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.810000002
AbsHwPos_HwPosSource_Cnt_M_u16	1908
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	544.340027
AbsHwPos_RelHwPos_HwDeg_M_f32	544.340027
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27177
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1817.34998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.810000002
AbsHwPos_TempHwPos_HwDeg_M_f32	-5758.06982
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.800000012
k_HwPosOutputLPFError_HwDeg_f32	822.799988
k_I2CHwAuthority_Uls_f32	0.800000012
k_MaxSensorlessAuthority_Uls_f32	0.88999986
k_MaxVehCntrOffDiff_HwDeg_f32	971.299988
k_MinSensorlessAuthority_Uls_f32	0.800000012
k_TurnsCntrAuthority_Uls_f32	0.800000012
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	822.799988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-192.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.889999986
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-921.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	148

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2476.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9919		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	arget_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	544.340027	544.340027 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4045 00505		
	-4615.98535	-4615.98584 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	-4615.98584 ± 0.1 65534	<b>*</b>
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum			\rightarrow \frac{1}{2}
	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	65534 HWPOS_STATE_INPUTINVALID	65534 HWPOS_STATE_INPUTINVALID	~
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID -4615.98535	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1	~
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535 0	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535 0 ± 0.1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535 0	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535 0 ± 0.1 0 ± 0.004	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535 0 0 -5758.06982	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -5758.06982 ± 0.1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535 0 0 -5758.06982 7649.14014	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -5758.06982 ± 0.1 7649.14014 ± 0.009	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535 0 0 -5758.06982 7649.14014	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -5758.06982 ± 0.1 7649.14014 ± 0.009	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	65534 HWPOS_STATE_INPUTINVALID -4615.98535 65535 0 0 -5758.06982 7649.14014 1	65534 HWPOS_STATE_INPUTINVALID -4615.98584 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -5758.06982 ± 0.1 7649.14014 ± 0.009 1	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

0.00999999046

-1600

65534

65535

2476.1001

0.00999999978 ± 0.004

-1600 ± 0.1

2476.1001 ± 0.009

65534

65535

0 ± 0.1

Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	8920.66016
AbsHwPos HandwheelAuthority Uls M f32	0.81999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	262.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.81999993
AbsHwPos_HwPosSource_Cnt_M_u16	2020
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-564.619995
AbsHwPos_RelHwPos_HwDeg_M_f32	-564.619995
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27249
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1958.08997
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.81999993
AbsHwPos_TempHwPos_HwDeg_M_f32	5898.81006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.810000002
_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_Uls_f32	0.810000002

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AbsHwPos\_Per2

Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.89999976		
k MaxVehCntrOffDiff HwDeg f32	1001		
k MinSensorlessAuthority Uls f32	0.810000002		
k TurnsCntrAuthority Uls f32	0.810000002		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	852.5		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-172.899994		
target AbsHwPos Per2 ManufMode Cnt enum.value	2		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.89999976		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-901.799988		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	152		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2506.30005		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	10043		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-564.619995	-564.619995 ± 0.0009	~

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-564.619995	-564.619995 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.89999976	0.899999976 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988	-901.799988 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.89999976	0.899999976 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018	7543.18018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.899999976	0.899999976 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-901.799988	-901.799988 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-901.799988	-901.799988 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2506.30005	2506.30005 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.80 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8814.7002	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.829999983	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	272.100006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.829999983	
AbsHwPos_HwPosSource_Cnt_M_u16	2132	
AbsHwPos_HwPosState_Cnt_M_enum	5	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	584.900024	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27321	

AbsHwPos\_Per2

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Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.829999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.819999993		
k_HwPosOutputLPFError_HwDeg_f32	882.200012		
k_I2CHwAuthority_Uls_f32	0.819999993		
k_MaxSensorlessAuthority_Uls_f32	0.910000026		
k_MaxVehCntrOffDiff_HwDeg_f32	1030.69995		
k_MinSensorlessAuthority_Uls_f32	0.819999993		
k_TurnsCntrAuthority_Uls_f32	0.819999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	882.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.910000026		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-882.099976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	156		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2536.5 10167		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		PoducodPorf Cot Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<del>-</del>	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	-10125	-10125 ± 0.09	- 100 LIII
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>v</b>
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	5059.0835	5059.0835 ± 0.1	_
AbsHwPos HwPosSource Cnt M u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5059.0835	5059.0835 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498	6039.5498 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997	-1437.21997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

65535

0 ± 0.1

65535 ± 0.09

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 



Test Step 2.81 (Repeat Count = 1)	Invest Wales		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8708.74023		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.839999974		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	281.700012 0.839999974		
AbsHwPos_HwPosSource_Cnt_M_u16	2244		
AbsHwPos HwPosState Cnt M enum	6		
AbsHwPos PrevHandwheelPosition HwDeg M f32	605.179993		
AbsHwPos RelHwPos HwDeg M f32	10125		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27393		
AbsHwPos SrlComHwPos HwDeg M f32	-2239.57007		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.839999974		
AbsHwPos TempHwPos HwDeg M f32	6180.29004		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<pre>C_HwPosAuthorityStep_Uls_f32</pre>	0.829999983		
<pre>c_HwPosOutputLPFError_HwDeg_f32</pre>	911.900024		
c_I2CHwAuthority_Uls_f32	0.829999983		
x_MaxSensorlessAuthority_Uls_f32	0.920000017		
c_MaxVehCntrOffDiff_HwDeg_f32	1060.40002		
C_MinSensorlessAuthority_Uls_f32	0.829999983		
c_TurnsCntrAuthority_Uls_f32	0.829999983		
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	0		
	911.900024		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.920000017		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-862.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2566.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10291		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	nority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl	Pos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resi
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.09	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.839999974	0.839999974 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5236.51563	5236.51563 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	2244	2244	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
NbSHwPos PrevHandwheelPosition HwDeg M f32	5236.51563	5236.51563 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27393	27393	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2239.57007	-2239.57007 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.839999974	0.839999974 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	6180.29004	6180.29004 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977	7331.25977 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.839999974	0.839999974 ± 0.004	

2244

2244

AbsHwPos\_Per2

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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27393	27393	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2566.69995	2566.69995 ± 0.009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	8602.78027		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.850000024		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-350.200012		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.850000024		
AbsHwPos HwPosSource Cnt M u16	2356		
AbsHwPos_HwPosState_Cnt_M_enum	7		
AbsHwPos PrevHandwheelPosition HwDeg M f32	625.460022		
AbsHwPos_RelHwPos_HwDeg_M_f32	0		
AbsHwPos_riwPos_riwDeg_M_132  AbsHwPos_SrlComHwPosStatus Cnt M u16	27465		
	2380.31006		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0.850000024		
kbsHwPos_TargetHwAuthority_Uls_M_f32 kbsHwPos TempHwPos HwDeg M f32	-6321.02979		
_ ,	7225.2998		
\bsHwPos_VehCntrOffset_HwDeg_M_f32	1		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntrOnset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.839999974		
_HwPosOutputLPFError_HwDeg_f32	941.599976		
_I2CHwAuthority_Uls_f32	0.839999974		
_MaxSensorlessAuthority_Uls_f32	0.930000007		
_MaxVehCntrOffDiff_HwDeg_f32	1090.09998		
_MinSensorlessAuthority_Uls_f32	0.839999974		
_TurnsCntrAuthority_Uls_f32	0.839999974		
_UseTurnsCntr_Cnt_lgc	1		
:_VehCntrOffValidLimit_HwDeg_f32	941.599976		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.930000007		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-842.700012		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	164		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2596.8999		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	10415		
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPosI	ReducedPerf Cnt Igc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPosValid		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt e		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthorit		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos	,	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStat	<del></del>	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	CIIL_UU0	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1=	
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0	0 ± 0.000009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	

-5425.40527

-5425.40527

HWPOS\_STATE\_INPUTINVALID

65534

65535

0

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

 $AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32$ 

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

-5425.40527 ± 0.1

-5425.40527 ± 0.1

HWPOS\_STATE\_INPUTINVALID

65534

65535

0 ± 0.1





Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979	-6321.02979 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998	7225.2998 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.0099999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

est Step 2.83 (Repeat Count = 1)	
ame	Input Value
bsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8496.82031
bsHwPos_HandwheelAuthority_Uls_M_f32	0.860000014
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	300.899994
bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.860000014
bsHwPos_HwPosSource_Cnt_M_u16	2468
bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	645.73999
bsHwPos_RelHwPos_HwDeg_M_f32	645.73999
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	27537
bsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005
bsHwPos_TargetHwAuthority_Uls_M_f32	0.860000014
bsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
bsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
bsHwPos_VehCntrValid_Cnt_M_lgc	0
te_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
te_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.850000024
_HwPosOutputLPFError_HwDeg_f32	1060.40002
_I2CHwAuthority_Uls_f32	0.850000024
_MaxSensorlessAuthority_Uls_f32	0.93999998
_MaxVehCntrOffDiff_HwDeg_f32	1119.80005
_MinSensorlessAuthority_Uls_f32	0.850000024
_TurnsCntrAuthority_Uls_f32	0.850000024
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	971.299988
rget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
rget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
rget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012
rget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
rget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.93999998
rget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-823
rget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	168
rget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-488.100006
rrget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10539
irget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1
rrget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPert	f_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
riget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
rrget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
urget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
urget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
urget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
urget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
irget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	645.73999	645.73999 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5599.24854	5599.24805 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5599.24854	5599.24805 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	<b>~</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	<b>~</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996	-1119.32996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>~</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>~</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-488.100006	-488.100006 ± 0.0009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.84 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8390.86035
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.870000005
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	310.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.870000005
AbsHwPos_HwPosSource_Cnt_M_u16	2580
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-666.02002
AbsHwPos_RelHwPos_HwDeg_M_f32	-666.02002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27609
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.870000005
AbsHwPos_TempHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.860000014
k_HwPosOutputLPFError_HwDeg_f32	1090.09998
k_I2CHwAuthority_UIs_f32	0.860000014
k_MaxSensorlessAuthority_Uls_f32	0.94999988
k_MaxVehCntrOffDiff_HwDeg_f32	1149.5
k_MinSensorlessAuthority_Uls_f32	0.860000014
k_TurnsCntrAuthority_UIs_f32	0.860000014
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1001
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.949999988
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-803.299988
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	172

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-468.399994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10663		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Output Perf\_Output $	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Cymanted Value	D 14
	Actual value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-666.02002	-666.02002 ± 0.0009	Result
		· ·	Kesuit
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-666.02002	-666.02002 ± 0.0009	~
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	-666.02002 0.00999999046	-666.02002 ± 0.0009 0.00999999978 ± 0.004	~
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-666.02002 0.00999999046 5784.54834	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1	<i>y</i>
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	-666.02002 0.00999999046 5784.54834 65534	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534	· · ·
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	-666.02002 0.009999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1	*
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1	*
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1 65535 0 ± 0.1	*
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535 0	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1 65535 0 ± 0.1 0 ± 0.004	*
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535 0 0 6602.50977	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1 65535 0 ± 0.1 0 ± 0.004 6602.50977 ± 0.1	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535 0 0 6602.50977 7013.37988	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1 65535 0 ± 0.1 0 ± 0.004 6602.50977 ± 0.1 7013.37988 ± 0.009	· · · · · · · · · · · · · · · · · · ·
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535 0 0 6602.50977 7013.37988	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1 65535 0 ± 0.1 0 ± 0.004 6602.50977 ± 0.1 7013.37988 ± 0.009	***************************************
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	-666.02002 0.00999999046 5784.54834 65534 HWPOS_STATE_INPUTINVALID 5784.54834 65535 0 0 6602.50977 7013.37988 1	-666.02002 ± 0.0009 0.00999999978 ± 0.004 5784.54883 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 5784.54883 ± 0.1 65535 0 ± 0.1 0 ± 0.004 6602.50977 ± 0.1 7013.37988 ± 0.009 1 0	***************************************

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

-468.399994

0.00999999046

1600

65534

65535

0.00999999978 ± 0.004

-468.399994 ± 0.0009

1600 ± 0.1

65534

65535

0 ± 0.1

Test Step 2.85 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8284.88965
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.87999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	320.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.87999995
AbsHwPos_HwPosSource_Cnt_M_u16	2692
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	686.299988
AbsHwPos_RelHwPos_HwDeg_M_f32	686.299988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27681
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.87999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.870000005
k_HwPosOutputLPFError_HwDeg_f32	1119.80005
k_I2CHwAuthority_Uls_f32	0.870000005

AbsHwPos\_Per2





Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.959999979		
k_MaxVehCntrOffDiff_HwDeg_f32	1179.19995		
k_MinSensorlessAuthority_Uls_f32	0.870000005		
k_TurnsCntrAuthority_UIs_f32	0.870000005		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1030.69995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	562.400024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.959999979		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-783.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	176		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-448.700012		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10787		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Other and the property of the property $	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	686.299988	686.299988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.879999995	0.879999995 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-5895.64795	-5895.64795 ± 0.1	~
AballiuDos HiuDos Source Cot M 1116	2602	2602	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	686.299988	686.299988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.879999995	0.879999995 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-5895.64795	-5895.64795 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2692	2692	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-5895.64795	-5895.64795 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27681	27681	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.879999995	0.879999995 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-6743.25	-6743.25 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992	6907.41992 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.879999995	0.879999995 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2692	2692	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27681	27681	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-448.700012	-448.700012 ± 0.0009	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Name	Innut Value	
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	8178.93018	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.889999986	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-507.799988	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.889999986	
AbsHwPos_HwPosSource_Cnt_M_u16	2804	
AbsHwPos_HwPosState_Cnt_M_enum	11	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	706.580017	
AbsHwPos_RelHwPos_HwDeg_M_f32	706.580017	
AbsHwPos SrlComHwPosStatus Cnt M u16	27753	

AbsHwPos\_Per2





Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.889999986		
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6801.45996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.879999995		
k_HwPosOutputLPFError_HwDeg_f32	1149.5		
k_I2CHwAuthority_Uls_f32	0.879999995		
k_MaxSensorlessAuthority_Uls_f32	0.970000029 1208.90002		
k_MaxVehCntrOffDiff_HwDeg_f32	0.879999995		
k_MinSensorlessAuthority_Uls_f32 k_TurnsCntrAuthority_Uls_f32	0.879999995		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1060.40002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	582.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.970000029		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-763.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	180		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-429		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10911		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_( target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	706.580017	706.580017 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6070.89307	6070.89307 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	6070.89307	6070.89307 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TorgetHwAuthority_Ula_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeq_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos VehCntrOffset HwDeg_M_f32	6883.99023 6801.45996	6883.99023 ± 0.1 6801.45996 ± 0.009	
AbshwPos_venCntrOriset_HwDeg_M_132 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	6801.45996 ± 0.009	
AbsHwPos_venCntrOtstLearn_Cnt_M_igc  AbsHwPos_VehCntrValid_Cnt_M_igc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cit_T_enditi)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	
target AbsHwPos Per2 HandwheelPosition HwDeg f32.value	1600	1600 ± 0.1	
target AbsHwPos Per2 HwPosSource Cnt u16.value	65534	65534	
target AbsHwPos Per2 SrlComHwPosStatus Cnt u16.value	65535	65535	
g 05_1 0.1_01.001 055tata0_011_a10.1ata0			
target AbsHwPos Per2 SrlComHwPos HwDeg f32.value	0	$0 \pm 0.1$	

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-			
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~			
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•			



Test Step 2.87 (Repeat Count = 1)	In the Walter		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-8072.97021		
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.89999976		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	389.200012 0.899999976		
AbsHwPos_HwPosSource_Cnt_M_u16	2916		
AbsHwPos HwPosState Cnt M enum	12		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-726.859985		
AbsHwPos RelHwPos HwDeg M f32	-726.859985		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27825		
AbsHwPos SrlComHwPos HwDeg M f32	0		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.899999976		
AbsHwPos TempHwPos HwDeg M f32	7024.72998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6695.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<pre>K_HwPosAuthorityStep_Uls_f32</pre>	0.889999986		
<pre></pre>	1179.19995		
c_I2CHwAuthority_Uls_f32	0.889999986		
C_MaxSensorlessAuthority_Uls_f32	0.980000019		
x_MaxVehCntrOffDiff_HwDeg_f32	1238.59998		
C_MinSensorlessAuthority_Uls_f32	0.889999986		
c_TurnsCntrAuthority_Uls_f32	0.889999986		
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	0		
<pre>c_VehCntrOffValidLimit_HwDeg_f32</pre>	1090.09998		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.980000019		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-744.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	184		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-409.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11035		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	nority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl	Pos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-726.859985	-726.859985 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976	0.899999976 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6361.17676	6361.17676 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	2916	2916	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	6361.17676	6361.17676 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	27825	27825	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.899999976	0.899999976 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	7024.72998	7024.72998 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6695.5	6695.5 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	0	0	
AbsHwPos VehCntrValid Cnt M Igc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
	1	"none"	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0.899999976	*none* 0.899999976 + 0.004	
	1 0.899999976 1600	0.899999976 ± 0.004 1600 ± 0.1	

2916

2916

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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27825	27825	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-409.299988	-409.299988 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.88 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	7967.00977		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.910000026		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-468.399994		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.910000026		
AbsHwPos_HwPosSource_Cnt_M_u16	3028		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	747.140015		
AbsHwPos_RelHwPos_HwDeg_M_f32	747.140015		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27897		
AbsHwPos SrlComHwPos HwDeg M f32	2943.27002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.910000026		
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1589.54004		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
		trOffeet CetErrorStatue ErrorStatue	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	u Onset_GetEHOIStatuS_EHOIStatuS	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.89999976		
k_HwPosOutputLPFError_HwDeg_f32	1208.90002		
k_I2CHwAuthority_Uls_f32	0.89999976		
k_MaxSensorlessAuthority_UIs_f32	0.9900001		
k_MaxVehCntrOffDiff_HwDeg_f32	1268.30005		
k_MinSensorlessAuthority_Uls_f32	0.89999976		
k_TurnsCntrAuthority_UIs_f32	0.89999976		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1119.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.99000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-724.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	188		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-389.600006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11159		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_el	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	747.140015	747.140015 ± 0.0009	-

0.0100000501

HWPOS\_STATE\_INPUTINVALID

6478.42188

6478.42188

65534

65535

0

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16 AbsHwPos\_HwPosState\_Cnt\_M\_enum

 $AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32$ 

0.00999999978 ± 0.004

HWPOS\_STATE\_INPUTINVALID

6478.42188 ± 0.1

6478.42188 ± 0.1

65534

65535

0 ± 0.1





Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021	7165.47021 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1589.54004	-1589.54004 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

est Step 2.89 (Repeat Count = 1)	
ame	Input Value
bsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7861.0498
bsHwPos_HandwheelAuthority_Uls_M_f32	0.920000017
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	784.099976
bsHwPos HandwheelPositionLPF M str.K Uls f32	0.920000017
bsHwPos HwPosSource Cnt M u16	3140
bsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	767.419983
bsHwPos_RelHwPos_HwDeg_M_f32	767.419983
bsHwPos SrlComHwPosStatus Cnt M u16	27969
bsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001
bsHwPos TargetHwAuthority Uls M f32	0.920000017
bsHwPos TempHwPos HwDeg M f32	-7306.20996
bsHwPos VehCntrOffset HwDeg M f32	6483,58008
bsHwPos VehCntrOfstLearn Cnt M Igc	0
bsHwPos_VehCntrValid_Cnt_M_lgc	0
te Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
te Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
HwPosAuthorityStep Uls f32	0.910000026
HwPosOutputLPFError HwDeg f32	1238.59998
I2CHwAuthority Uls f32	0.910000026
MaxSensorlessAuthority_Uls_f32	0.100000001
MaxVehCntrOffDiff HwDeg f32	1298
MinSensorlessAuthority Uls f32	0.910000026
TurnsCntrAuthority Uls f32	0.910000026
UseTurnsCntr Cnt Igc	0
VehCntrOffValidLimit HwDeg f32	1149.5
rget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
riget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
lrget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988
riget AbsHwPos Per2 ManufMode Cnt enum.value	0
rget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.100000001
liget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-704.799988
riget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	192
riget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-369.899994
riget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	11283
riget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	1
riget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
irget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
	,
urget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
Irget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
lirget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
lirget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
rrget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum rrget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32_	target_AbsHwPos_Per2_ManufMode_Cnt_enum target AbsHwPos Per2 SensorlessAuthority Uls f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	767.419983	767.419983 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.920000017	0.920000017 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-6658.98535	-6658.98535 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3140	3140	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-6658.98535	-6658.98535 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27969	27969	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.920000017	0.920000017 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7306.20996	-7306.20996 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6483.58008	6483.58008 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.920000017	0.920000017 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3140	3140	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27969	27969	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-369.899994	-369.899994 ± 0.0009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.90 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7755.08984
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.930000007
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	966.200012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.930000007
AbsHwPos_HwPosSource_Cnt_M_u16	3252
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	787.700012
AbsHwPos_RelHwPos_HwDeg_M_f32	787.700012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.930000007
AbsHwPos_TempHwPos_HwDeg_M_f32	5828.25977
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6377.62012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	0.920000017
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	1268.30005
<_I2CHwAuthority_Uls_f32	0.920000017
<_MaxSensorlessAuthority_Uls_f32	0.109999999
<_MaxVehCntrOffDiff_HwDeg_f32	1327.69995
<_MinSensorlessAuthority_Uls_f32	0.920000017
<_TurnsCntrAuthority_Uls_f32	0.920000017
<_UseTurnsCntr_Cnt_lgc	1
<_VehCntrOffValidLimit_HwDeg_f32	1179.19995
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	662.900024
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.109999999
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-685.099976
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	196

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

AbsHwPos\_Per2



Name	Input Value				
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-350.200012				
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11407				
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	2				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos	/alid_Cnt_lgc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutl	nority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwf	Pos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset				
Name	Actual Value	Expected Value	Result		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	787.700012	787.700012 ± 0.0009	-		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5487.91553	5487.91602 ± 0.1	~		
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	-		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5487.91553	5487.91602 ± 0.1	<b>~</b>		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	-		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~		
AbsHwPos_TempHwPos_HwDeg_M_f32	5828.25977	5828.25977 ± 0.1	~		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6377.62012	6377.62012 ± 0.009	•		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>		
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

-350.200012

65534

65535

65534

65535

0 ± 0.1

-350.200012 ± 0.0009

Test Step 2.91 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7649.14014
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.93999998
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-409.299988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.93999998
AbsHwPos_HwPosSource_Cnt_M_u16	3364
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-807.97998
AbsHwPos_RelHwPos_HwDeg_M_f32	-807.97998
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
AbsHwPos_SrlComHwPos_HwDeg_M_f32	202.529999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.93999998
AbsHwPos_TempHwPos_HwDeg_M_f32	848.539978
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1271.65002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.930000007
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_Uls_f32	0.930000007

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AbsHwPos\_Per2

Name	Input Value		
k MaxSensorlessAuthority Uls f32	0.119999997		
k_MaxVehCntrOffDiff_HwDeg_f32	1357.40002		
k MinSensorlessAuthority Uls f32	0.930000007		
k TurnsCntrAuthority Uls f32	0.930000007		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	1208.90002		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	683		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-665.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	200		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-330.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11531		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-807.97998	-807.97998 ± 0.0009	~

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-807.97998	-807.97998 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.93999998	0.93999998 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	773.069519	773.06958 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3364	3364	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	773.069519	773.06958 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.93999998	0.939999998 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	848.539978	848.539978 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1271.65002	-1271.65002 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.93999998	0.939999998 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	773.069519	773.06958 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3364	3364	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	202.529999	202.529999 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-330.5	-330.5 ± 0.0009	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	

Test Step 2.92 (Repeat Count = 1)		
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7543.18018	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.949999988	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	213	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.949999988	
AbsHwPos_HwPosSource_Cnt_M_u16	3476	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	828.26001	
AbsHwPos_RelHwPos_HwDeg_M_f32	5828.25977	
AbsHwPos SrlComHwPosStatus Cnt M u16	28969	

target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08.value

 $target\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset$ 

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AbsHwPos Per2 Input Value AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32 9802.53027 AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32 0.949999988 AbsHwPos\_TempHwPos\_HwDeg\_M\_f32 -868.820007 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32 6165.7002 AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc 1 AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus(ErrorStatus) target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus Rte\_Inst\_Ap\_AbsHwPos target\_Rte\_Inst\_Ap\_AbsHwPos k\_HwPosAuthorityStep\_Uls\_f32 k HwPosOutputLPFError HwDeg f32 1327 69995 0.939999998 k\_I2CHwAuthority\_Uls\_f32 0 129999995 k MaxSensorlessAuthority Uls f32 k\_MaxVehCntrOffDiff\_HwDeg\_f32 1387.09998 k MinSensorlessAuthority Uls f32 0.939999998 k\_TurnsCntrAuthority\_Uls\_f32 0.939999998 k UseTurnsCntr Cnt lac k\_VehCntrOffValidLimit\_HwDeg\_f32 1238.59998 target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_lgc.value target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value 703.099976 target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum.value Λ target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32.value 0.129999995

-645.700012

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 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ -310.799988 target\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 11655  $target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$ target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_( | target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_Igc target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target AbsHwPos Per2 HandwheelAuthority Uls f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32 target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16 target AbsHwPos Per2 HwPosSource Cnt u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32 target AbsHwPos Per2 I2CHwAbsPos HwDeg f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_ManufMode\_Cnt\_enum target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32$ target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32 target AbsHwPos Per2 SensorlessHwPos HwDeg f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32$ target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08 target AbsHwPos Per2 TurnsCntrValidity Cnt u08

Actual Value **Expected Value** Name Result AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32 5828.25977 5828.25977 ± 0.009 AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32 0.00999999046 0.00999999978 ± 0.004 AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32 -814.729004 -814.729004 ± 0.1 AbsHwPos\_HwPosSource\_Cnt\_M\_u16 65534 65534 HWPOS\_STATE\_INPUTINVALID HWPOS\_STATE\_INPUTINVALID AbsHwPos\_HwPosState\_Cnt\_M\_enum AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 -814.729004 -814.729004 ± 0.1 AbsHwPos SrlComHwPosStatus Cnt M u16 65535 65535 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32  $0 \pm 0.1$ 0 + 0.004AbsHwPos TargetHwAuthority Uls M f32 -868.820007 -868 820007 + 0 1 AbsHwPos\_TempHwPos\_HwDeg\_M\_f32 ~ AbsHwPos VehCntrOffset HwDeg M f32 6165.7002 6165.7002 ± 0.009 AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc AbsHwPos VehCntrValid Cnt M Igc 0 0 Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) 504 504 Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) 1  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value 0.00999999046 0.00999999978 + 0.004  $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ -814.729004 -814.729004 ± 0.1 target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value 65534 65534 65535 target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value 65535 target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value 0 ± 0.1 65535 65535 ± 0.09 target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32

target Pim EQLVehCntrOffset

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>~</b>



Test Step 2.93 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7437.22021		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.959999979		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-369.899994		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.959999979		
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	3588 5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	848.539978		
AbsHwPos RelHwPos HwDeg M f32	848.539978		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	29467		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0		
AbsHwPos TempHwPos HwDeg M f32	1889.09998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6059.74023		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.94999988		
k_HwPosOutputLPFError_HwDeg_f32	1357.40002		
k_I2CHwAuthority_UIs_f32	0.94999988		
k_MaxSensorlessAuthority_Uls_f32	0.140000001		
k_MaxVehCntrOffDiff_HwDeg_f32	1416.80005		
k_MinSensorlessAuthority_Uls_f32	0.94999988		
k_TurnsCntrAuthority_Uls_f32	0.94999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1268.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	723.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.140000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-626		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	208		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-291.100006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11779 5		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_t		PosPoducadPorf Cnt Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAut	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHw		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPos	Status Cnt u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	848.539978	848.539978 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1889.09998	1889.09998 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3588	3588	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1889.09998	1889.09998 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	29467	29467	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	1889.09998	1889.09998 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6059.74023	6059.74023 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
		*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
	1 0.00999999046 1600	0.00999999978 ± 0.004 1600 ± 0.1	

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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	29467	29467	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-291.100006	-291.100006 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.94 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	7331.25977		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.970000029		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.970000029		
AbsHwPos_HwPosSource_Cnt_M_u16	3700		
AbsHwPos_HwPosState_Cnt_M_enum	6		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-868.820007		
AbsHwPos_RelHwPos_HwDeg_M_f32	-868.820007		
AbsHwPos SrlComHwPosStatus Cnt M u16	29965		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	120.199997		
AbsHwPos_TargetHwAuthority_Uls_M_f32	1		
AbsHwPos TempHwPos HwDeg M f32	909.380005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1446.94995		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.959999979		
k HwPosOutputLPFError HwDeg f32	1387.09998		
k I2CHwAuthority Uls f32	0.959999979		
k_MaxSensorlessAuthority_Uls_f32	0.150000006		
k MaxVehCntrOffDiff HwDeg f32	1446.5		
k MinSensorlessAuthority Uls f32	0.959999979		
k TurnsCntrAuthority Uls f32	0.959999979		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1298		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	743.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.150000006		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-606.299988		
	212		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-271.399994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11903		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0	Dadward Darf Out Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	·-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-868.820007	-868.820007 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	•
Abeliw Poe Handwheel Position I PE M etr SV I IIe f32	885 704651	885 70450 ± 0.1	

885.704651

885.704651

HWPOS\_STATE\_INPUTINVALID

65534

65535

0

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum

 $AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32$ 

AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

HWPOS\_STATE\_INPUTINVALID

885.70459 ± 0.1

885.70459 ± 0.1

65534

65535

0 ± 0.1





Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1446.94995	-1446.94995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	885.704651	885.70459 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

est Step 2.95 (Repeat Count = 1)	
ame	Input Value
bsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7225.2998
bsHwPos_HandwheelAuthority_Uls_M_f32	0.980000019
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	140.300003
bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.980000019
bsHwPos_HwPosSource_Cnt_M_u16	3812
bsHwPos_HwPosState_Cnt_M_enum	7
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	889.099976
bsHwPos_RelHwPos_HwDeg_M_f32	1889.09998
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	30463
bsHwPos_SrlComHwPos_HwDeg_M_f32	140.300003
bsHwPos_TargetHwAuthority_Uls_M_f32	0.980000019
bsHwPos_TempHwPos_HwDeg_M_f32	929.659973
bsHwPos_VehCntrOffset_HwDeg_M_f32	7587.68994
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
bsHwPos_VehCntrValid_Cnt_M_lgc	0
te_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
te_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.970000029
_HwPosOutputLPFError_HwDeg_f32	1416.80005
_I2CHwAuthority_Uls_f32	0.970000029
_MaxSensorlessAuthority_Uls_f32	0.159999996
_MaxVehCntrOffDiff_HwDeg_f32	1476.19995
MinSensorlessAuthority_Uls_f32	0.970000029
_TurnsCntrAuthority_Uls_f32	0.970000029
_UseTurnsCntr_Cnt_lgc	0
VehCntrOffValidLimit_HwDeg_f32	1327.69995
rget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
rget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
rget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	763.400024
rget AbsHwPos Per2 ManufMode Cnt enum.value	0
rget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.159999996
rget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-586.599976
rget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	216
rget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-251.699997
rget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12027
rget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	1
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (	target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority Uls f32





# Name target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08 target\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset Name Actual Value Expected Value Resul AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_UIs\_f32 AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_UIs\_f32 AbsHwPos\_HwPosState\_Cnt\_M\_enum HWPOS\_STATE\_INPLITINVALID HWPOS\_STATE\_INPLITINVALID HWPOS\_STATE\_INPLITINVALID HWPOS\_STATE\_INPLITINVALID

name	Actual value	Expected value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1889.09998	1889.09998 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.980000019	0.980000019 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	929.659973	929.659973 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3812	3812	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	929.659973	929.659973 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	30463	30463	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.980000019	0.980000019 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	929.659973	929.659973 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7587.68994	7587.68994 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.980000019	0.980000019 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	929.659973	929.659973 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3812	3812	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	30463	30463	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	140.300003	140.300003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-251.699997	-251.699997 ± 0.0009	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.96 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-7119.33008
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.99000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	160.399994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.99000001
AbsHwPos_HwPosSource_Cnt_M_u16	3924
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	909.380005
AbsHwPos_RelHwPos_HwDeg_M_f32	909.380005
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	30961
AbsHwPos_SrlComHwPos_HwDeg_M_f32	160.399994
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.99000001
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7728.43018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.980000019
k_HwPosOutputLPFError_HwDeg_f32	1446.5
k_I2CHwAuthority_UIs_f32	0.980000019
k_MaxSensorlessAuthority_Uls_f32	0.170000002
k_MaxVehCntrOffDiff_HwDeg_f32	1505.90002
k_MinSensorlessAuthority_Uls_f32	0.980000019
k_TurnsCntrAuthority_Uls_f32	0.980000019
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1357.40002
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	783.5
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.170000002
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-566.900024
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	220





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5000.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12151		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11606.1465	-11606.1465 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11606.1465	-11606.1465 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7728.43018	7728.43018 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

5000.6001

5000.6001 ± 0.009

Test Step 2.97 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7013.37988
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	180.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	4036
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	929.659973
AbsHwPos_RelHwPos_HwDeg_M_f32	929.659973
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31459
AbsHwPos_SrlComHwPos_HwDeg_M_f32	180.5
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1369.17004
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.99000001
k_HwPosOutputLPFError_HwDeg_f32	1476.19995
k_I2CHwAuthority_Uls_f32	0.99000001

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 





Name	Input Value			
k_MaxSensorlessAuthority_Uls_f32	0.180000007			
k_MaxVehCntrOffDiff_HwDeg_f32	1535.59998			
k_MinSensorlessAuthority_Uls_f32	0.99000001			
k_TurnsCntrAuthority_UIs_f32	0.99000001			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	1387.09998			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.180000007			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-547.200012			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5120.3999			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12275			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos AlignedHwPos HwDeg M f32	929.659973	929.659973 ± 0.0009	<b>~</b>	

target_Rte_inst_Ap_AbshwPos.Piin_EOLVenChtronset	target_Pim_EOLVenChironset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	929.659973	929.659973 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1334.95007	1334.94995 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	4036	4036	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1334.95007	1334.94995 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31459	31459	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	180.5	180.5 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1369.17004	-1369.17004 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1334.95007	1334.94995 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4036	4036	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	31459	31459	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	180.5	180.5 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5120.3999	5120.3999 ± 0.009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.98 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6907.41992	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	200.600006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003	
AbsHwPos_HwPosSource_Cnt_M_u16	4148	
AbsHwPos_HwPosState_Cnt_M_enum	10	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	949.940002	
AbsHwPos_RelHwPos_HwDeg_M_f32	949.940002	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31957	

AbsHwPos\_Per2





Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	200.600006		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.20000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2804		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.100000001		
k_HwPosOutputLPFError_HwDeg_f32	1505.90002		
k I2CHwAuthority Uls f32	0.100000001		
k MaxSensorlessAuthority Uls f32	0.189999998		
k_MaxVehCntrOffDiff_HwDeg_f32	1565.30005		
k_MinSensorlessAuthority_Uls_f32	0.100000001		
k_TurnsCntrAuthority_UIs_f32	0.100000001		
k UseTurnsCntr Cnt Igc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1416.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	823.700012		
target AbsHwPos Per2 ManufMode Cnt enum.value	0		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.189999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-527.5		
	228		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	5240.2002		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12399		
	4		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		an Radius ad Part Cat. Ign	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_UIs_f32	target_AbsHwPos_Per2_HandwheelAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSf		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2804	2804 ± 0.009	
AheHwPoe VehCntrOfetl earn Cnt M Inc	1	1	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2804	2804 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim FOLVehCntrOffset FOLVehCntrOffset HwDeg 132	65535	65535 + 0.09	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.99 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6801.45996		
AbsHwPos HandwheelAuthority Uls M f32	0.300000012		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	220.699997		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.300000012		
AbsHwPos_HwPosSource_Cnt_M_u16	4260		
AbsHwPos_HwPosState_Cnt_M_enum	11		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	970.219971		
AbsHwPos_RelHwPos_HwDeg_M_f32	970.219971		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32455		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	220.699997		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2916		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLV	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.109999999		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998		
k_I2CHwAuthority_Uls_f32	0.109999999		
k_MaxSensorlessAuthority_Uls_f32	0.200000003		
k_MaxVehCntrOffDiff_HwDeg_f32	1595		
k_MinSensorlessAuthority_Uls_f32	0.109999999		
k_TurnsCntrAuthority_Uls_f32	0.109999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1446.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-507.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	232		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5360		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12523		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	I=	
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	970.219971	970.219971 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.30000012	0.300000012 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1037.47107	1037.47095 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	4260	4260	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1037.47107	1037.47095 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32455	32455	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	220.699997	220.699997 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.30000012	0.300000012 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2916	2916 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Pto Coll An Abolius Poo Nytr Piochter Cathi Toctation (Otation Cot Toron)		*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.300000012 1037.47107	0.300000012 ± 0.004 1037.47095 ± 0.1	





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	32455	32455	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	220.699997	220.699997 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5360	5360 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	6695.5		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	240.800003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40000006		
AbsHwPos HwPosSource Cnt M u16	4372		
AbsHwPos_HwPosState_Cnt_M_enum	12		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-990.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	-990.5		
AbsHwPos SrlComHwPosStatus Cnt M u16	32953		
AbsHwPos SrlComHwPos HwDeg M f32	240.800003		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
AbsHwPos TempHwPos HwDeg M f32	-3084.01001		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3028		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	enonitoniset_GetEnorStatus_EnorStatus	
k HwPosAuthorityStep Uls f32	0.119999997		
- · · · ·	1565.30005		
k_HwPosOutputLPFError_HwDeg_f32 k_I2CHwAuthority_Uls_f32	0.119999997		
k MaxSensorlessAuthority Uls f32	0.20999993		
- ·	50.5999985		
<_MaxVehCntrOffDiff_HwDeg_f32 <_MinConnections Authority   Un_f22			
	0.119999997		
k_TurnsCntrAuthority_Uls_f32	1		
k_UseTurnsCntr_Cnt_lgc k_VehCntrOffValidLimit_HwDeq_f32			
	1476.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	863.900024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.20999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-488.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5479.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12647		
rarget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	, <b>-</b> -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.0009	
	0.00000004	0.000000004 + 0.004	

AbsHwPos_AlignedHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	0.280000001 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1089.12402	-1089.12402 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1089.12402	-1089.12402 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>





Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3028	3028 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.280000001	0.280000001 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1089.12402	-1089.12402 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.101 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	200
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	260.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	4484
AbsHwPos_HwPosState_Cnt_M_enum	13
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1010.78003
AbsHwPos_RelHwPos_HwDeg_M_f32	1010.78003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33451
AbsHwPos_SrlComHwPos_HwDeg_M_f32	260.899994
hbsHwPos_TargetHwAuthority_Uls_M_f32	0.5
AbsHwPos_TempHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.129999995
_HwPosOutputLPFError_HwDeg_f32	1595
_I2CHwAuthority_Uls_f32	0.129999995
_MaxSensorlessAuthority_Uls_f32	0.219999999
_MaxVehCntrOffDiff_HwDeg_f32	80.3000031
_MinSensorlessAuthority_Uls_f32	0.129999995
z_TurnsCntrAuthority_Uls_f32	0.129999995
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	1505.90002
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	884
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.219999999
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-468.399994
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	240
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5599.6001
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12771
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1010.78003	1010.78003 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	4484	4484	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33451	33451	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	260.899994	260.899994 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	668.400024	668.400024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-248.300003	-248.300003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4484	4484	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	33451	33451	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	260.899994	260.899994 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	668.400024	668.400024 ± 0.0009	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.102 (Repeat Count = 1)	· ·
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	1000.25
AbsHwPos HandwheelAuthority Uls M f32	0.60000024
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.60000024
AbsHwPos_HwPosSource_Cnt_M_u16	4596
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1031.06006
AbsHwPos_RelHwPos_HwDeg_M_f32	123.400002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33949
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.60000024
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.140000001
k_HwPosOutputLPFError_HwDeg_f32	1149.5
k_I2CHwAuthority_UIs_f32	0.140000001
k_MaxSensorlessAuthority_Uls_f32	0.230000004
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.140000001
k_TurnsCntrAuthority_UIs_f32	0.140000001
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1535.59998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004

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Name	Input Value			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-448.700012			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	244			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12895	12895		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	2			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_AbsHwPosReduced$	_( target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_HandwheelPositi	on_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_F	lwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.0009	•	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000038	0.460000008 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	<b>✓</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID			
	TIME 03_3TATE_INFOTTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1395.13	HWPOS_STATE_INPUTINVALID  1395.13 ± 0.1	-	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16				
	1395.13	1395.13 ± 0.1		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	1395.13 65535	1395.13 ± 0.1 65535	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13 65535 0	1395.13 ± 0.1 65535 0 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	1395.13 65535 0 0	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13 65535 0 0 1395.13	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	1395.13 65535 0 0 1395.13 1448.94995	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1 1448.94995 ± 0.009		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1395.13 65535 0 0 1395.13 1448.94995	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1 1448.94995 ± 0.009		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1395.13 65535 0 0 1395.13 1448.94995 1	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1 1448.94995 ± 0.009		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1395.13 65535 0 0 1395.13 1448.94995 1 1	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1 1448.94995 ± 0.009 1 1 504		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1395.13 65535 0 0 1395.13 1448.94995 1 1 504	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1 1448.94995 ± 0.009 1 1 504		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1395.13 65535 0 0 1395.13 1448.94995 1 1 504	1395.13 ± 0.1 65535 0 ± 0.1 0 ± 0.004 1395.13 ± 0.1 1448.94995 ± 0.009 1 1 504 0		

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

65534

65535

5719.3999

65534

65535

0 ± 0.1

5719.3999 ± 0.009

Test Step 2.103 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	524.210022
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	4708
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1051.33997
AbsHwPos_RelHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	34447
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
K HwPosAuthorityStep Uls f32	0.150000006

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value





Name	Input Value			
k_HwPosOutputLPFError_HwDeg_f32	1179.19995			
k_I2CHwAuthority_Uls_f32	0.150000006			
k_MaxSensorlessAuthority_Uls_f32	0.23999995			
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997			
k_MinSensorlessAuthority_Uls_f32	0.150000006	0.150000006		
k_TurnsCntrAuthority_UIs_f32	0.150000006			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.239999995			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-429			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	248			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5839.2002			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13019			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3			
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	16 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.0009	•	
AbaHwDaa HandwhaalAutharity IIIa M f22	0.600000000	0.600000000 + 0.004		

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988	0.69999988 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1535.87	1535.87 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	4708	4708	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1535.87	1535.87 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988	0.69999988 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87	1535.87 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	953.210022	953.210022 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.69999988	0.69999988 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1535.87	1535.87 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4708	4708	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	953.210022	953.210022 ± 0.0009	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Test Step 2.104 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-324.119995	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	321.26001	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.800000012	
AbsHwPos_HwPosSource_Cnt_M_u16	4820	

AbsHwPos Per2

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Input Value AbsHwPos\_HwPosState\_Cnt\_M\_enum 1071.62 AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 147.300003 AbsHwPos\_RelHwPos\_HwDeg\_M\_f32 AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16 34945 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32 -6321.02979 AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32 0.800000012 AbsHwPos\_TempHwPos\_HwDeg\_M\_f32 -1676.60999 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32 1000.59998 AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus(ErrorStatus) target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus Rte\_Inst\_Ap\_AbsHwPos target\_Rte\_Inst\_Ap\_AbsHwPos k\_HwPosAuthorityStep\_Uls\_f32 0.159999996  $k\_HwPosOutputLPFError\_HwDeg\_f32$ 1208 90002 k\_I2CHwAuthority\_Uls\_f32 0.159999996 0.25 k MaxSensorlessAuthority Uls f32 k\_MaxVehCntrOffDiff\_HwDeg\_f32 169.399994 0.159999996 k MinSensorlessAuthority Uls f32 k\_TurnsCntrAuthority\_Uls\_f32 0.159999996 k\_UseTurnsCntr\_Cnt\_lgc  $k\_VehCntrOffValidLimit\_HwDeg\_f32$ 1595 target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_lgc.value target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_lgc.value target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value 140.300003 target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum.value 0  $target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32.value$ 0.25 target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32.value -409.299988 target\_AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08.value 252 target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32 5959 target\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16 13143 target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_( | target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_Igc target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32 target AbsHwPos Per2 HandwheelAuthority Uls f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32 target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16 target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32 target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_ManufMode\_Cnt\_enum$ target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32$ target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg f32 target AbsHwPos Per2 SensorlessHwPos HwDeg f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg f32 target AbsHwPos Per2 SrlComHwPos HwDeg f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08$ target\_AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08 target\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset target\_Pim\_EOLVehCntrOffset

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	147.300003	147.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.639999986	0.639999986 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1277.03601	-1277.03601 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1277.03601	-1277.03601 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-1676.60999	-1676.60999 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	85.1799927	85.1800003 ± 0.00009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.639999986	0.639999986 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1277.03601	-1277.03601 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	85.1799927	85.1800003 ± 0.00009	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.105 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-400.209991		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.89999976		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	341.540009		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.89999976		
AbsHwPos_HwPosSource_Cnt_M_u16	4932		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1091.90002		
AbsHwPos_RelHwPos_HwDeg_M_f32	-991.700012		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	35443		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.89999976		
AbsHwPos_TempHwPos_HwDeg_M_f32	1817.34998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-500.899994		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
K_HwPosAuthorityStep_Uls_f32	0.170000002		
k_HwPosOutputLPFError_HwDeg_f32	1238.59998		
k_I2CHwAuthority_Uls_f32	0.170000002		
k_MaxSensorlessAuthority_Uls_f32	0.25999999		
 K_MaxVehCntrOffDiff_HwDeg_f32	199.100006		
k MinSensorlessAuthority Uls f32	0.17000002		
C TurnsCntrAuthority UIs f32	0.170000002		
<ul> <li>UseTurnsCntr Cnt Igc</li> </ul>	0		
VehCntrOffValidLimit HwDeg f32	50.599985		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	0 160.399994		
	1		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25999999		
rarget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-389.600006		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	10		
rarget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6078.7998		
rarget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13267		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	-991.700012	-991.700012 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.729999959	0.730000019 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-316.485992	-316.485992 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	3	3	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_SENSORINVALID	HWPOS STATE SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-316.485992	-316.485992 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	-310.465992 ± 0.1	
AbsHwPos SrlComHwPos HwDeg M f32	-389.600006	-389.600006 ± 0.1	
	-000.00000	-505.00000 ± 0.1	

0.25999999

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

0.25999999 ± 0.004





Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-389.600006	-389.600006 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-10.6099854	-10.6099997 ± 0.00009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.729999959	0.730000019 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-316.485992	-316.485992 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-389.600006	-389.600006 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-10.6099854	-10.6099997 ± 0.00009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.106 (Repeat Count = 1)	
lame	Input Value
.bsHwPos_AlignedCumMtrPos_HwDeg_M_f32	6059.74023
bsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999
.bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	361.820007
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.10999999
.bsHwPos_HwPosSource_Cnt_M_u16	5044
.bsHwPos_HwPosState_Cnt_M_enum	5
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1112.18005
.bsHwPos_RelHwPos_HwDeg_M_f32	894.5
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	35941
.bsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999
.bsHwPos_TempHwPos_HwDeg_M_f32	1958.08997
.bsHwPos_VehCntrOffset_HwDeg_M_f32	3700
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
.bsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.180000007
HwPosOutputLPFError_HwDeg_f32	1268.30005
I2CHwAuthority Uls f32	0.180000007
MaxSensorlessAuthority Uls f32	0.270000011
MaxVehCntrOffDiff HwDeg f32	228.800003
MinSensorlessAuthority Uls f32	0.180000007
TurnsCntrAuthority Uls f32	0.180000007
UseTurnsCntr Cnt Igc	1
VehCntrOffValidLimit HwDeg f32	80.3000031
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5
arget AbsHwPos Per2 ManufMode Cnt enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.270000011
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-369.899994
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	6198.6001
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	13391
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	• /
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	894.5	894.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1958.08997	1958.08997 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1958.08997	1958.08997 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	1958.08997	1958.08997 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3700	3700 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6198.6001	6198.6001 ± 0.009	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.107 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-5953.77881
AbsHwPos HandwheelAuthority Uls M f32	0.119999997
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	382.100006
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.119999997
AbsHwPos HwPosSource Cnt M u16	5156
AbsHwPos HwPosState Cnt M enum	6
AbsHwPos PrevHandwheelPosition HwDeg M f32	-1132.45996
AbshwPos RelHwPos HwDeg M f32	5641
AbsHwPos SrlComHwPosStatus Cnt M u16	36439
	-6743.25
AbsHwPos_SrlComHwPos_HwDeg_M_f32	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3812
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_Uls_f32	0.18999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001
k_MaxVehCntrOffDiff_HwDeg_f32	258.5
k_MinSensorlessAuthority_Uls_f32	0.18999998
k_TurnsCntrAuthority_Uls_f32	0.18999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	110
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-350.200012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	30
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6318.3999





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13515		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	1		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerror and all the properties of the$	f_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3000000000000000000000000000000000000$	32 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutl	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	2 target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	5641	5641 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	588.107605	588.107605 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	5156	5156	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	588.107605	588.107605 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36439	36439	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25	-6743.25 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3812	3812 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	588.107605	588.107605 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	5156	5156	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	36439	36439	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 2.108 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5847.81982
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	402.380005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995
AbsHwPos_HwPosSource_Cnt_M_u16	5268
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1152.73999
AbsHwPos_RelHwPos_HwDeg_M_f32	150.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36937
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3924
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.200000003
k_HwPosOutputLPFError_HwDeg_f32	1327.69995
k_I2CHwAuthority_Uls_f32	0.200000003
k_MaxSensorlessAuthority_Uls_f32	0.289999992

AbsHwPos Per2

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Input Value k\_MaxVehCntrOffDiff\_HwDeg\_f32 288.200012 k\_MinSensorlessAuthority\_Uls\_f32 0.200000003 0.200000003 k TurnsCntrAuthority Uls f32 k\_UseTurnsCntr\_Cnt\_lgc k\_VehCntrOffValidLimit\_HwDeg\_f32 139.699997 target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_lgc.value 0 target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value 1 220 699997  $target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value$ target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum.value  $target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32.value$ 0.289999992 target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32.value -330.5 target\_AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08.value 100 target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32 6438.2002 13639 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16 target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf ( | target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_Igc target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32 target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16$ target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32$ target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_ManufMode\_Cnt\_enum target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum  $target\_Rte\_Inst\_Ap\_AbsHwPos\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32$ target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32 target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32$ target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08$ target\_AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08 target\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset target\_Pim\_EOLVehCntrOffset

target_itte_inst_Ap_Absnivros.Filii_Lockvenontronset	target_Filli_LOLVeriontiOnset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	150.5	150.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	58.9264832	58.9264984 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	58.9264832	58.9264984 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023	6883.99023 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007	-2239.57007 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3924	3924 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	58.9264832	58.9264984 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6438.2002	6438.2002 ± 0.009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.109 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5741.85986	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.140000001	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	422.660004	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.140000001	
AbsHwPos_HwPosSource_Cnt_M_u16	5380	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1173.02002	
AbsHwPos_RelHwPos_HwDeg_M_f32	-620	

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AbsHwPos\_Per2

Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	37435		
AbsHwPos SrlComHwPos HwDeg M f32	7024.72998		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.140000001		
AbsHwPos TempHwPos HwDeg M f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4036		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	nCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.20999993		
k_HwPosOutputLPFError_HwDeg_f32	1357.40002		
k_I2CHwAuthority_Uls_f32	0.209999993		
k_MaxSensorlessAuthority_Uls_f32	0.300000012		
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994		
k_MinSensorlessAuthority_Uls_f32	0.209999993		
k_TurnsCntrAuthority_Uls_f32	0.209999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	169.399994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.30000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-310.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6558		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13763		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	( target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<del></del>	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32			
	L-620	-620 + 0 0009	Result
	-620 n 200000003	-620 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.209999993	0.209999993 ± 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0.209999993 1705.85986	0.209999993 ± 0.004 1705.85999 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	0.209999993 1705.85986 1	0.209999993 ± 0.004 1705.85999 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1	0
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845	0.20999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845	0
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986	0.20999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1	0
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.2099999993 ± 0.004	0
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1	0
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_Learn_Cnt_M_lgc	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504	
AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffst_earn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1 0	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504	
AbsHwPos_HandwheelAuthority_Uls_M_f32  AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32  AbsHwPos_HwPosSource_Cnt_M_u16  AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32  AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1 0 504	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504 1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504 1 1 0.209999993 ± 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993 1600	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504 1 1 0.209999993 ± 0.004 1600 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993 1600 1	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.2099999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504 1 1 0.2099999993 ± 0.004 1600 ± 0.1 1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0.209999993 1705.85986 1 HWPOS_STATE_SENSORVALID 1705.85986 21845 1705.85986 0.209999993 1705.85986 4036 1 0 504 1 1 0.209999993 1600	0.209999993 ± 0.004 1705.85999 ± 0.1 1 HWPOS_STATE_SENSORVALID 1705.85999 ± 0.1 21845 1705.85999 ± 0.1 0.209999993 ± 0.004 1705.85999 ± 0.1 4036 ± 0.009 1 0 504 1 1 0.209999993 ± 0.004 1600 ± 0.1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.110 (Repeat Count = 1)	Installed		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5635.8999		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006		
AbsHwPos_HwPosSource_Cnt_M_u16	5492		
AbsHwPos_HwPosState_Cnt_M_enum	9		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1193.30005		
AbsHwPos_RelHwPos_HwDeg_M_f32	201.199997		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	37933		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4148		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.219999999		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_UIs_f32	0.219999999		
k_MaxSensorlessAuthority_Uls_f32	0.310000002		
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006		
k_MinSensorlessAuthority_Uls_f32	0.219999999		
k_TurnsCntrAuthority_UIs_f32	0.219999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	199.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.310000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	60		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	6677.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13887		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u		sReducedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio	,	
	target AbsHwPos Per2 HwPosSource C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosVa	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDog_f32	target_AbsHwPos_Per2_SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	201.199997	201.199997 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos SrlComHwPos HwDeg M f32	0	0 ± 0.1	•

0

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

0 ± 0.004





Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4148	4148 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.111 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	-5529.93018
AbsHwPos HandwheelAuthority Uls M f32	0.159999996
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	11725
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.159999996
AbsHwPos HwPosSource Cnt M u16	5604
AbsHwPos HwPosState Cnt M enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1213.57996
AbsHwPos_RelHwPos_HwDeg_M_f32	64.9800034
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38431
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7306.20996
AbsHwPos TargetHwAuthority Uls M f32	0.159999996
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4260
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.230000004
k_HwPosOutputLPFError_HwDeg_f32	1416.80005
k_I2CHwAuthority_UIs_f32	0.230000004
k_MaxSensorlessAuthority_Uls_f32	0.319999993
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988
k_MinSensorlessAuthority_Uls_f32	0.230000004
k_TurnsCntrAuthority_Uls_f32	0.230000004
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	228.800003
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.319999993
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-271.399994
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	70
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6797.6001
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14011
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
	-





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	64.9800034	64.9800034 ± 0.00009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996	0.159999996 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	9423.11328	9423.11328 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	5604	5604	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	9423.11328	9423.11328 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38431	38431	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7306.20996	7306.20996 ± 0.1	•	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.159999996	0.159999996 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	<b>~</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4260	4260 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>~</b>	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.159999996	0.159999996 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>~</b>	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	5604	5604	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	38431	38431	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6797.6001	6797.6001 ± 0.009	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.112 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	5423.97998
AbsHwPos HandwheelAuthority Uls M f32	0.170000002
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	0
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.170000002
AbsHwPos HwPosSource Cnt M u16	5716
AbsHwPos HwPosState Cnt M enum	11
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1233.85999
AbsHwPos_RelHwPos_HwDeg_M_f32	8888
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7446.9502
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	949.940002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4372
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.23999995
k_HwPosOutputLPFError_HwDeg_f32	1446.5
k_I2CHwAuthority_Uls_f32	0.23999995
k_MaxSensorlessAuthority_Uls_f32	0.330000013
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.23999995
k_TurnsCntrAuthority_UIs_f32	0.23999995
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	258.5
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.330000013
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-251.699997
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6917.3999





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14135		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPertsUpsPersU$	rf_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3000000000000000000000000000000000000$	32 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutl	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	2 target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16000000000000000000000000000000000000$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	8888	8888 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002	949.940002 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	949.940002	949.940002 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4372	4372 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	949.940002	949.940002 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AhsHwPos Per? CP1 ChecknointReached	1	Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	-

Test Step 2.113 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001
AbsHwPos_HwPosSource_Cnt_M_u16	296
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.270000011
k_HwPosOutputLPFError_HwDeg_f32	1179.19995
k_I2CHwAuthority_Uls_f32	0.270000011





Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.340000004		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_UIs_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Ormation{AbsHwPos}{AbsHwP$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	<b>~</b>
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.114 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-5212.05908	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-564.619995	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.189999998	
AbsHwPos_HwPosSource_Cnt_M_u16	5940	
AbsHwPos_HwPosState_Cnt_M_enum	13	

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AbsHwPos Per2 Input Value AbsHwPos PrevHandwheelPosition\_HwDeg\_M\_f32 1274.42004 471 AbsHwPos\_RelHwPos\_HwDeg\_M\_f32 39925 AbsHwPos SrlComHwPosStatus Cnt M u16 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32 7728.43018 AbsHwPos TargetHwAuthority Uls M f32 0.189999998 AbsHwPos\_TempHwPos\_HwDeg\_M\_f32 -990.5 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32 4596  $AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc$ 1 AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus(ErrorStatus) Rte\_Inst\_Ap\_AbsHwPos target\_Rte\_Inst\_Ap\_AbsHwPos 0.25999999 k HwPosAuthorityStep Uls f32 1505.90002 k\_HwPosOutputLPFError\_HwDeg\_f32 k\_I2CHwAuthority\_Uls\_f32 0.25999999 0.349999994 k MaxSensorlessAuthority Uls f32 466.399994 k MaxVehCntrOffDiff HwDeg f32 k\_MinSensorlessAuthority\_Uls\_f32 0.25999999 k\_TurnsCntrAuthority\_Uls\_f32 0.25999999 k\_UseTurnsCntr\_Cnt\_lgc k\_VehCntrOffValidLimit\_HwDeg\_f32 317.899994  $target\_AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Cnt\_lgc.value$ target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc.value target\_AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32.value 341 299988

target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum.value 0.349999994 target AbsHwPos Per2 SensorlessAuthority Uls f32.value  $target\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32.value$ -212.300003 target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value 100  $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 7157 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16 14383  $target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$ target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf ( target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16

 $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc$  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_I2CHwAbsPos\_HwDeg\_f32$  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_ManufMode\_Cnt\_enum$  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32$ 

target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32  $target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_TurnsCntrValidity\_Cnt\_u08$  $target\_Rte\_Inst\_Ap\_AbsHwPos.Pim\_EOLVehCntrOffset$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32 target AbsHwPos Per2 HandwheelPosition HwDeg f32 target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16 target\_AbsHwPos\_Per2\_I2CHwAbsPosValid\_Cnt\_Igc target AbsHwPos Per2 I2CHwAbsPos HwDeg f32 target\_AbsHwPos\_Per2\_ManufMode\_Cnt\_enum target\_AbsHwPos\_Per2\_SensorlessAuthority\_Uls\_f32 target AbsHwPos Per2 SensorlessHwPos HwDeg f32 target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16 target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32 target AbsHwPos Per2 TurnsCntrValidity Cnt u08 target\_Pim\_EOLVehCntrOffset

Name Actual Value **Expected Value** Result AbsHwPos\_AlignedHwPos\_HwDeg\_M\_f32 -471 -471 ± 0.0009  $AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32$ 0 0 + 0.004AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32 -645.53717 -645.53717 ± 0.1 65534 AbsHwPos HwPosSource Cnt M u16 65534 HWPOS\_STATE\_INPUTINVALID AbsHwPos\_HwPosState\_Cnt\_M\_enum HWPOS\_STATE\_INPUTINVALID AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32 -645 53717 -645.53717 ± 0.1 65535 65535 AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16 AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32 0  $0 \pm 0.1$ AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32 0 ± 0.004 **~** 0 AbsHwPos TempHwPos HwDeg M f32 -990.5 -990.5 ± 0.1 AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32 4596 4596 ± 0.009 **~** AbsHwPos\_VehCntrOfstLearn\_Cnt\_M\_lgc 1 1 AbsHwPos\_VehCntrValid\_Cnt\_M\_lgc Λ Λ Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) 504 504 ~  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)$ Λ Λ Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) 0 0  $0 \pm 0.004$ target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value -645.53717 -645.53717 ± 0.1 65534 65534 target AbsHwPos Per2 HwPosSource Cnt u16.value target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value 65535 65535 target AbsHwPos Per2 SrlComHwPos HwDeg f32.value  $0 \pm 0.1$ target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32 7157  $7157 \pm 0.009$ 





Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.115 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5106.08984		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20000003		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	484		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0		
AbsHwPos_HwPosSource_Cnt_M_u16	6052		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1294.69995		
AbsHwPos_RelHwPos_HwDeg_M_f32	4649		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40423		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7869.16992		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	1010.78003		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4708		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	3
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<hr/> k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998		
c_I2CHwAuthority_Uls_f32	0.270000011		
<_MaxSensorlessAuthority_Uls_f32	0.360000014		
<pre>k_MaxVehCntrOffDiff_HwDeg_f32</pre>	496.100006		
_MinSensorlessAuthority_Uls_f32	0.270000011		
C_TurnsCntrAuthority_Uls_f32	0.270000011		
<_UseTurnsCntr_Cnt_lgc	0		
c_VehCntrOffValidLimit_HwDeg_f32	347.600006		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-192.600006		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	110		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7276.7998		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14507		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_( target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuti	nority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3.	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cnt enum		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority Uls f32		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	4649	4649 ± 0.009	7.030
AbsHwPos HandwheelAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1010.78003	1010.78003 ± 0.1	
NosHwPos_HandwheerrosidonErr_iw_sti.3v_ois_i32	6052	6052	
	HWPOS_STATE_INIT		
AbsHwPos_HwPosState_Cnt_M_enum		HWPOS_STATE_INIT 1010.78003 ± 0.1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1010.78003		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40423	40423	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32			

1010.78003

AbsHwPos\_TempHwPos\_HwDeg\_M\_f32

1010.78003 ± 0.1

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AbsHwPos\_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4708	4708 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1010.78003	1010.78003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	6052	6052	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	40423	40423	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7276.7998	7276.7998 ± 0.009	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.116 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	5000.12988		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20999993		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-564.299988		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1		
AbsHwPos_HwPosSource_Cnt_M_u16	6164		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1314.97998		
AbsHwPos_RelHwPos_HwDeg_M_f32	234		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40921		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.209999993		
AbsHwPos_TempHwPos_HwDeg_M_f32	123.400002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1687		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	monta o mod_octemoro attao_emoro attao	
K HwPosAuthorityStep Uls f32	0.280000001		
K_HwPosOutputLPFError_HwDeg_f32	1565.30005		
k_I2CHwAuthority_Uls_f32	0.280000001		
_MaxSensorlessAuthority_Uls_f32	0.370000005		
c_MaxVehCntrOffDiff_HwDeg_f32	525.799988		
k_MinSensorlessAuthority_Uls_f32	0.280000001		
x_TurnsCntrAuthority_Uls_f32	0.280000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	482		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.370000005		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-172.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	120		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7396.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14631		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwI	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_ManufMode_C target AbsHwPos Per2 SensorlessAutl	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl	,	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPos		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<i>/</i>	
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	234	234 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	123.400002	123.400002 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	123.400002	123.400002 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
	123.400002	123.400002 ± 0.1	
		1687 ± 0.009	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1687	4	
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1 0	0	
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 0 504	0 504	
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 0 504 1	0 504 1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 504	0 504 1 1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1 0 504 1 1	0 504 1 1 0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value  target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 0 504 1	0 504 1 1	

65535

0

65535

0 ± 0.1

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

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Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.117 (Repeat Count = 1)			<b>√</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9556.42969		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-941.200012		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.569999993		
AbsHwPos_HwPosSource_Cnt_M_u16	1348		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-409.299988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-409.299988		
AbsHwPos SrlComHwPosStatus Cnt M u16	41419		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.569999993		
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCni	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos  Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos target_Rte_Inst_Ap_AbsHwPos	i Onsci_GetE11013tatus_E11013tatus	
k HwPosAuthorityStep Uls f32	0.75		
k_HwPosOutputLPFError_HwDeg_f32	674.299988		
	0.75		
k_I2CHwAuthority_UIs_f32	0.839999974		
k_MaxSensorlessAuthority_UIs_f32			
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988		
k_MinSensorlessAuthority_UIs_f32	0.75		
k_TurnsCntrAuthority_Uls_f32	0.75		
k_UseTurnsCntr_Cnt_lgc	674,200088		
k_VehCntrOffValidLimit_HwDeg_f32	674.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.83999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	130		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-409.299988	-409.299988 ± 0.0009	•
AbeHwPos HandwheelAuthority Ille M f32	0.560000003	0.560000003 ± 0.004	

0.569999993

HWPOS\_STATE\_INPUTINVALID

952.06073

952.06073

41419

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

 $AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32$ 

AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16 AbsHwPos\_HwPosState\_Cnt\_M\_enum 0.569999993 ± 0.004

HWPOS\_STATE\_INPUTINVALID

952.06073 ± 0.1

952.06073 ± 0.1

41419

AbsHwPos\_Per2



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004	6180.29004 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.569999993	0.569999993 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006	2380.31006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003	2802.53003 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.569999993	0.569999993 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	952.06073	952.06073 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	41419	41419	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Fest Step 2.118 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-514.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-921.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.579999983
AbsHwPos HwPosSource Cnt M u16	1460
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213
AbsHwPos_RelHwPos_HwDeg_M_f32	213
AbsHwPos SrlComHwPosStatus Cnt M u16	41917
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979
AbsHwPos TargetHwAuthority Uls M f32	0.579999983
AbsHwPos TempHwPos HwDeg M f32	2521.05005
AbsHwPos VehCntrOffset HwDeg M f32	2943.27002
AbsHwPos VehCntrOfstLearn Cnt M Igc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
KIC_ITSE_AD_ADST WIT OS  KIC_ITSE_AD_ADST WIT OS  KIC_ITSE_AD_ADST WIT OS  KIC_ITSE_AD_ADST WIT OS	0.75999999
:_HwPosOutputLPFError_HwDeg_f32	704
CI2CHwAuthority Uls f32	0.75999999
:_NaxSensorlessAuthority_Uls_f32	0.850000024
: MaxVehCntrOffDiff HwDeg f32	852.5
MinSensorlessAuthority Uls f32	0.75999999
TurnsCntrAuthority Uls f32	0.75999999
:TurnsCntr_Cnt_lgc	1
: VehCntrOffValidLimit HwDeg f32	704
	1
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorSta	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduced	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDe	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Ig	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f3	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	213	213 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1075.17896	1075.17896 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>~</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1075.17896	1075.17896 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	485.97998	485.980011 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1075.17896	1075.17896 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	485.97998	485.980011 ± 0.0009	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.119 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-874.22998
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.589999974
AbsHwPos_HwPosSource_Cnt_M_u16	1572
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42415
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.769999981
k_HwPosOutputLPFError_HwDeg_f32	733.700012
k_I2CHwAuthority_Uls_f32	0.769999981
k_MaxSensorlessAuthority_Uls_f32	0.860000014
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.769999981
k_TurnsCntrAuthority_UIs_f32	0.769999981
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	733.700012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1





Name	Input Value		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2385.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	prity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_F	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1437.41101	-1437.41113 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	106.369995	106.370003 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.589999974	0.589999974 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	-
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	106.369995	106.370003 ± 0.0009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.120 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-325.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.790000021
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.60000024
AbsHwPos_HwPosSource_Cnt_M_u16	1684
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42913
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos





Name	Input Value		
k_HwPosAuthorityStep_Uls_f32	0.779999971		
k_HwPosOutputLPFError_HwDeg_f32	763.400024		
k_I2CHwAuthority_UIs_f32	0.779999971		
k_MaxSensorlessAuthority_Uls_f32	0.870000005		
k_MaxVehCntrOffDiff_HwDeg_f32	911.900024		
k_MinSensorlessAuthority_Uls_f32	0.779999971		
k_TurnsCntrAuthority_UIs_f32	0.779999971		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	763.400024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-232		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	•
AbeHwDos HandwhoolDositionI DE M str SV I IIIs f32	1870 15706	1870 15706 ± 0 1	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	<b>~</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>~</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	635.580017	635.580017 ± 0.0009	<b>~</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995	2415.69995 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.121 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-514.320007	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-921.5	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.579999983	





Name	Input Value		
AbsHwPos_HwPosSource_Cnt_M_u16	1460		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213		
AbsHwPos_RelHwPos_HwDeg_M_f32	213		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41917		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.579999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.75999999		
k_HwPosOutputLPFError_HwDeg_f32	704		
k_I2CHwAuthority_Uls_f32	0.75999999		
k_MaxSensorlessAuthority_Uls_f32	0.850000024		
k_MaxVehCntrOffDiff_HwDeg_f32	852.5		
k_MinSensorlessAuthority_UIs_f32	0.75999999		
k_TurnsCntrAuthority_UIs_f32	0.75999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	704		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423		
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1	1_
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	213	213 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1075.17896	1075.17896 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1075.17896	1075.17896 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	485.97998	485.980011 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	· ·
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	
	1075.17896	1075.17896 ± 0.1	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value			
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534 65535	65535	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

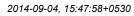
Test Step 2.122 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-874.22998		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	324.5		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.589999974		
AbsHwPos_HwPosSource_Cnt_M_u16	1572		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42415		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974		
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.769999981		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.769999981		
k_MaxSensorlessAuthority_Uls_f32	0.860000014		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.769999981		
k_TurnsCntrAuthority_Uls_f32	0.769999981		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	733.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2385.5		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0		ReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target AbsHwPos Per2 HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Ci		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32			
	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	/	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1437.41101	-1437.41113 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	
AbaHurDoo TorgotHurAuthority IIIo M f22	0.590000074	0.590000074 + 0.004	

0.589999974

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

0.589999974 ± 0.004

AbsHwPos\_Per2





Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	106.369995	106.370003 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.589999974	0.589999974 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	106.369995	106.370003 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.123 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-325,320007
AbsHwPos HandwheelAuthority Uls M f32	0.790000021
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.60000024
AbsHwPos HwPosSource Cnt M u16	1684
AbsHwPos HwPosState Cnt M enum	3
AbsHwPos PrevHandwheelPosition HwDeg M f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42913
AbsHwPos SrlComHwPos HwDeg M f32	6602.50977
AbsHwPos TargetHwAuthority Uls M f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VenChtrOfistLearn Cnt M lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0.779999971
k_HwPosOutputLPFError_HwDeg_f32	763.400024
k_I2CHwAuthority_Uls_f32	0.779999971
k MaxSensorlessAuthority Uls f32	0.870000005
k MaxVehCntrOffDiff HwDeg f32	911,900024
k MinSensorlessAuthority Uls f32	0.779999971
k TurnsCntrAuthority Uls f32	0.779999971
k UseTurnsCntr Cnt Igc	1
k VehCntrOffValidLimit HwDeg f32	763.400024
	1
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	-232
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	2
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16

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AbsHwPos\_Per2

Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	635.580017	635.580017 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	635.580017	635.580017 ± 0.0009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

AbsHwPos\_Per2

2014-09-04, 15:47:58+0530



Test Case 3: Path Test

2014-09-04, 15:47:58+0530

AbsHwPos\_Per2



#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

CPU Cycles:

TS3.1 1467.00 Cycles
TS3.2 2915.00 Cycles
TS3.3 1421.00 Cycles
TS3.3 1421.00 Cycles
TS3.4 1965.00 Cycles
TS3.5 2827.00 Cycles
TS3.5 2827.00 Cycles
TS3.6 2340.00 Cycles
TS3.7 2783.00 Cycles
TS3.10 1374.00 Cycles
TS3.10 1374.00 Cycles
TS3.11 1438.00 Cycles
TS3.11 18860.00 Cycles
TS3.12 1850.00 Cycles
TS3.13 1896.00 Cycles
TS3.14 1860.00 Cycles
TS3.15 1365.00 Cycles
TS3.15 1365.00 Cycles
TS3.17 1844.00 Cycles
TS3.18 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.20 1426.00 Cycles
TS3.21 1904.00 Cycles
TS3.22 1861.00 Cycles
TS3.23 1844.00 Cycles
TS3.24 1904.00 Cycles
TS3.25 1904.00 Cycles
TS3.27 1373.00 Cycles
TS3.28 1910.00 Cycles
TS3.28 1910.00 Cycles
TS3.29 1438.00 Cycles
TS3.29 1438.00 Cycles
TS3.29 1438.00 Cycles
TS3.30 1904.00 Cycles



```
Description Vector Description:
```

```
if ( (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) || (AbsHwPos_HwPosState_Cnt_M_enum ==
#WPOS_STATE_INPUTINVALID) )=>False
if (AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc == TRUE)=>False
if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >=
k_HwPosOutputLPFError_HwDeg_f32)=>True
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=>False
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=>True
if (k_UseTurnsCntr_Cnt_lgc == FALSE)=>True
if (l2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False'
TS3.2"
 if (Diagoslation Wrosineduceur ein [__in_c__in_c] = 1100_j=1100 if (AbsHwPos_VehCntrOfstLearn_Cnt_M_igc == TRUE)=>True if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>False if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>True
if( NvMStatus_Cnt_T_enum != NVM_REQ_PENDING )=>True"
if ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) =>False||
(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID)=>True)
if (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE)=>False"
TS3 4
163.4 if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>True if (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NvMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>True"
if( (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NyMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>False"
 if( (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) >
(NvMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>False"
TS3 7"
if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>False
if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)=>False else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False"
if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)=>False else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=>False
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=>False"
TS3.9"
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=>True if (k_UseTurnsCntr_Cnt_Igc == TRUE)=>True if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)=>True"
TS3.10'
if (k_UseTurnsCntr_Cnt_lgc == TRUE)=>False
if (l2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False"
TS3.11
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True"
TS3.12"
else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>False"
TS3.13"
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>True"
if (k_UseTurnsCntr_Cnt_lgc == FALSE)=>False
if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>True
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>True"
 TS3.15"
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True"
TS3.16"
 if (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => False"
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>False
if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>True if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>False"
if (Sensorle
              essAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>False'
TS3 19"
else if (AbsHwPos HwPosState Cnt M enum == HWPOS STATE SENSORINVALID)=>True
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE) )=>True"
TS3.21"
TS3 22"
if (SensorlessAuthority Uls T f32 >= k MaxSensorlessAuthority Uls f32)=>False"
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>True"
```



TS3.26"  $if (Sensorless Authority\_Uls\_T\_f32 >= k\_MinSensorless Authority\_Uls\_f32) => False"$ TS3 27"

if (SensorlessAuthority\_Uls\_T\_f32 >= k\_MaxSensorlessAuthority\_Uls\_f32)=>False"

if( NvMStatus\_Cnt\_T\_enum != NVM\_REQ\_PENDING )=>False" TS3.29"

(AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32>=D\_MAXAUTHORITY\_ULS\_F32)=>True"
TS3.30"(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=False,(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=True,

(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=True,
(AbsHwPos\_HwPosState\_Cnt\_M\_enum == HWPOS\_STATE\_SENSORINVALID)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=True,
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCntr\_Cnt\_lgc == TRUE)=False)
(I2CHwAbsPosValid\_Cnt\_T\_lgc == TRUE))=False,(k\_UseTurnsCnt\_Lgc == TRUE)=True,(k\_UseTurnsCnt\_Lgc == TRUE)=True,(k\_

Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125	
AbsHwPos HandwheelAuthority Uls M f32	0	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-1600	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0	
AbsHwPos HwPosSource Cnt M u16	1	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-1600	
AbsHwPos RelHwPos HwDeg M f32	-10125	
AbsHwPos_srlComHwPosStatus_Cnt_M_u16	21845	
NbSHwPos SrlComHwPos HwDeg M f32	-11725	
AbsHwPos TargetHwAuthority Uls M f32	0	
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	
AbsHwPos VehCntrOfstLearn Cnt M Igc	0	
	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos 0	
:_HwPosAuthorityStep_Uls_f32	0	
_HwPosOutputLPFError_HwDeg_f32	0	
I2CHwAuthority_Uls_f32		
_MaxSensorlessAuthority_UIs_f32	0	
_MaxVehCntrOffDiff_HwDeg_f32	0	
_MinSensorlessAuthority_Uls_f32	0	
_TurnsCntrAuthority_Uls_f32	0	
C_UseTurnsCntr_Cnt_lgc	0	
	0	
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0	
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0	
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0	
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600	
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0	
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	
Name	Actual Value Expected Value	Resi

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1600	-1600 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1600	-1600 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

lame	Input Value
	10125
lbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	
bsHwPos_HandwheelAuthority_Uls_M_f32	1 1600
.bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1.2.7
bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1
.bsHwPos_HwPosSource_Cnt_M_u16	65534
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1600
bsHwPos_RelHwPos_HwDeg_M_f32	10125
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
.bsHwPos_SrlComHwPos_HwDeg_M_f32	11725
bsHwPos_TargetHwAuthority_Uls_M_f32	1
lbsHwPos_TempHwPos_HwDeg_M_f32	11725
lbsHwPos_VehCntrOffset_HwDeg_M_f32	65535
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
.bsHwPos_VehCntrValid_Cnt_M_lgc	1
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	1
_HwPosOutputLPFError_HwDeg_f32	1620
_I2CHwAuthority_Uls_f32	1
_MaxSensorlessAuthority_Uls_f32	1
_MaxVehCntrOffDiff_HwDeg_f32	1600
_MinSensorlessAuthority_Uls_f32	1
_TurnsCntrAuthority_Uls_f32	1
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1600
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedCumMtrPos HwDeg M f32	9556.42969
AbsHwPos HandwheelAuthority Uls M f32	0.75999999
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	204.899994
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.75999999
AbsHwPos HwPosSource Cnt M u16	1348
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	442.940002
AbsHwPos RelHwPos HwDeg M f32	442.940002
AbsHwPos SrlComHwPosStatus Cnt M u16	26817
AbsHwPos SrlComHwPos HwDeg M f32	123.400002
AbsHwPos TargetHwAuthority Uls M f32	0.75999999
AbsHwPos TempHwPos HwDeg M f32	5054.37012
AbsHwPos VehCntrOffset HwDeg M f32	8178.93018
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.75
k_HwPosOutputLPFError_HwDeg_f32	674.299988
k_I2CHwAuthority_Uls_f32	0.75
k_MaxSensorlessAuthority_Uls_f32	0.83999974
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988
k MinSensorlessAuthority Uls f32	0.75
k_TurnsCntrAuthority_UIs_f32	0.75
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	674.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001

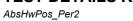




Name	Input Value			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299			
$target\_Rte\_Call\_Ap\_AbsHwPos\_EOLVehCntrOffset\_GetErrorStatus\_ErrorStatus$	3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_( target_AbsHwPos_Per2_DiagStatusHw	/PosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3$	2 target_AbsHwPos_Per2_HandwheelPo	sition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	e_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	sValid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0	Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAu	thority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos\_AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_SensorlessHv	vPos_HwDeg_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPo	sStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPo	s_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrVali	dity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	<b>✓</b>	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	<b>✓</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	✓	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	✓	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	✓	
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	✓	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75999999	0.75999999 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	123.400002	123.400002 ± 0.1	~	
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2325.1001	2325.1001 ± 0.009	<b>✓</b>	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.4 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1617.32996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40999996
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1527.19995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.409999996
AbsHwPos_HwPosSource_Cnt_M_u16	400
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	224.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	224.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24657
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3928.44995
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40999996
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1555.08997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.40000006
k_HwPosOutputLPFError_HwDeg_f32	1565.30005
k_I2CHwAuthority_UIs_f32	0.40000006
k_MaxSensorlessAuthority_Uls_f32	0.49000001
k_MaxVehCntrOffDiff_HwDeg_f32	0
k_MinSensorlessAuthority_Uls_f32	0.40000006





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Name	Input Value		
k_TurnsCntrAuthority_UIs_f32	0.40000006		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1208.90002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.49000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	884		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1268.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4959		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosf	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	224.100006	224.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2278.53882	-2278.53882 ± 0.1	~
AheHwPoe SrlComHwPoeStatus Cnt M u16	65535	65535	_

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Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	224.100006	224.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2278.53882	-2278.53882 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984	-7755.08984 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	733.329956	733.330017 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	733.329956	733.330017 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Test Step 3.5 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	898.809998	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.430000007	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	567.400024	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.430000007	
AbsHwPos_HwPosSource_Cnt_M_u16	416	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	243.300003	
AbsHwPos_RelHwPos_HwDeg_M_f32	243.300003	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24801	

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Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4209.93018		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.430000007		
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.419999987		
k HwPosOutputLPFError HwDeg f32	50.5999985		
k_I2CHwAuthority_Uls_f32	0.419999987		
k MaxSensorlessAuthority Uls f32	0.50999999		
k MaxVehCntrOffDiff HwDeg f32	1416.80005		
k_MinSensorlessAuthority_Uls_f32	0.419999987		
k_TurnsCntrAuthority_Uls_f32	0.41999987		
k_UseTurnsCntr_Cnt_Igc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1268.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.50999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	924.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1328.5		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	5207		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		PosReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	ity_cin_uoo	
	Actual Value	Expected Value	Poeu
Name	243.300003	•	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32		243.300003 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	147.139496	147.139496 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.139496	147.139496 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012	-409.950012 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-25.3900146	-25.3899994 ± 0.00009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	- '
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

0.0100000203

147.139496

65534

65535

1328.5

0.00999999978 ± 0.004

147.139496 ± 0.1

1328.5 ± 0.009

65534

65535

0 ± 0.1

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value



Test Step 3.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1000.25		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.600000024		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024		
AbsHwPos_HwPosSource_Cnt_M_u16	4596		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1031.06006		
AbsHwPos_RelHwPos_HwDeg_M_f32	123.400002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33949		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024		
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc		h Cata Official Cot Francisco Francisco Francisco	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.140000001		
k_HwPosOutputLPFError_HwDeg_f32	1149.5		
k_I2CHwAuthority_UIs_f32	0.140000001 0.230000004		
k_MaxSensorlessAuthority_Uls_f32 k_MaxVehCntrOffDiff_HwDeq_f32	0.230000004		
k_MinSensorlessAuthority_Uls_f32	0.140000001		
k_TurnsCntrAuthority_Uls_f32	0.140000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1535.59998		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.09998		
target AbsHwPos Per2 ManufMode Cnt enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-448.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	244		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12895		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer		PosReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f3			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPos	Valid Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	123.400002	123.400002 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.46000038	0.460000008 ± 0.004	-
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	-
AbsHwPos HwPosSource Cnt M u16	65534	65534	•
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	-
AbsHwPos PrevHandwheelPosition HwDeg M f32	1395.13	1395.13 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1448.94995	1448.94995 ± 0.009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
	0.460000038	0.460000008 ± 0.004	-
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value			
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1395.13	1395.13 ± 0.1	<b>✓</b>
	1395.13 65534	1395.13 ± 0.1 65534	





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999	5719.3999 ± 0.009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	269.209991		
AbsHwPos HandwheelAuthority Uls M f32	0.200000003		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	605.179993		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003		
AbsHwPos HwPosSource Cnt M u16	96		
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1507.09998		
AbsHwPos RelHwPos HwDeg M f32	1507.09998		
	21921		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.200000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<hr/> k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	436.700012		
k_I2CHwAuthority_Uls_f32	0.200000003		
<pre>&lt;_MaxSensorlessAuthority_Uls_f32</pre>	0.109999999		
c_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
C_MinSensorlessAuthority_Uls_f32	0.200000003		
<_TurnsCntrAuthority_Uls_f32	0.200000003		
<_UseTurnsCntr_Cnt_lgc	1		
<_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.109999999		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	120.199997		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	120.5		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	247		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(		teducedPerf Cnt lac	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	nt_uu8	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resi
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1507.09998	1507.09998 ± 0.009	
Abal huDaa Handushaal Authority I Ha M 522	_	0 + 0 004	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1507.09998	1507.09998 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	524.650024	524.650024 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	524.650024	524.650024 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	8920.66016 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	1358.69995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	524.650024	524.650024 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	120.5	120.5 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	691.429993
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-666.02002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	120
AbsHwPos_HwPosState_Cnt_M_enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	567.400024
AbsHwPos_RelHwPos_HwDeg_M_f32	567.400024
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976
AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.5
: HwPosOutputLPFError HwDeg f32	525.799988
C I2CHwAuthority Uls f32	0.5
MaxSensorlessAuthority Uls f32	0.140000001
MaxVehCntrOffDiff HwDeg f32	228.800003
MinSensorlessAuthority_Uls_f32	0.5
C TurnsCntrAuthority Uls f32	0.5
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	0
C VehCntrOffValidLimit HwDeg f32	169.399994
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	180.5
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.140000001
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	180.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	14
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	211.100006
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	619
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority Uls f32
wigot_itto_iiiot_rtp_rbbsitiwi ob.rbbsitiwi ob_i otz_ociibolicobratilolity_oib_loz	talgot_non ini oo_i ciz_ochonicoortatiinity_olo_loz





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	567.400024	567.400024 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997	120.199997 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	120	120	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>~</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997	120.199997 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137	22137	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	$0.5 \pm 0.004$	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	120.199997	120.199997 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	120	120	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	22137	22137	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006	211.100006 ± 0.0009	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.9 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	128.470001
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	584.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	88
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1487
AbsHwPos_RelHwPos_HwDeg_M_f32	1487
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21849
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.100000001
k_HwPosOutputLPFError_HwDeg_f32	407
k_I2CHwAuthority_Uls_f32	0.100000001
k_MaxSensorlessAuthority_Uls_f32	0.100000001
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.100000001
k_TurnsCntrAuthority_Uls_f32	0.100000001
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0

AbsHwPos\_Per2



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	90.3000031		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	123		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1487	1487 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	806.663025	806.663025 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	806.663025	806.663025 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012	9026.62012 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5	1328.5 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	806.663025	806.663025 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Pte Call AheHwPos Per? CP1 ChecknointReached	1	Pte Call AhsHwPos Per? CP1 ChecknointReached	1	-

65534

1600

90.3000031

65534

1600 ± 0.1

90.3000031 ± 0.00009

Test Step 3.10 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	972.909973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	706.580017
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	136
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1000.29999
AbsHwPos_RelHwPos_HwDeg_M_f32	-1000.29999
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22281
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.699999988
k_HwPosOutputLPFError_HwDeg_f32	585.200012
k_I2CHwAuthority_Uls_f32	0.699999988
k_MaxSensorlessAuthority_Uls_f32	0.159999996

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value\\ target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

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AbsHwPos\_Per2

Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.69999988		
k_TurnsCntrAuthority_UIs_f32	0.69999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.159999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	26		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	867		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf C	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	s_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	nt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
A. I. B. Ali. III B. II B. A. 60	4000 00000	1000 00000 : 0 000	

	10. g		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.253998	324.253998 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.253998	324.253998 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	324.253998	324.253998 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5	271.5 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Name	Input Value	
AbsHwPos AlignedCumMtrPos HwDeg M f32	1395.13	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.10999999	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	767.419983	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.10999999	
AbsHwPos_HwPosSource_Cnt_M_u16	160	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1547.30005	
AbsHwPos_RelHwPos_HwDeg_M_f32	1547.30005	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22497	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8072.97021	

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AbsHwPos\_Per2

Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999		
AbsHwPos TempHwPos HwDeg M f32	220.699997		
AbsHwPos VehCntrOffset HwDeg M f32	664.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
<hr/> K_HwPosAuthorityStep_Uls_f32	0.100000001		
k_HwPosOutputLPFError_HwDeg_f32	674.299988		
k_I2CHwAuthority_Uls_f32	0.100000001		
k_MaxSensorlessAuthority_Uls_f32	0.189999998		
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
k_MinSensorlessAuthority_Uls_f32	0.100000001		
k_TurnsCntrAuthority_Uls_f32	0.100000001		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	317.899994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	44		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	362.100006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1239		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u	target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	713.913757	713.913818 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	713.913757	713.913818 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281	281 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	281	281 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976	664.099976 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	713.913757	713.913818 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
	las.	004 + 0.4	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	281	281 ± 0.1	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 3.12 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-409.950012		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.300000012		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	625.460022		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.300000012		
AbsHwPos_HwPosSource_Cnt_M_u16	104 HWPOS STATE INIT		
AbsHwPos_HwPosState_Cnt_M_enum  AbsHwPos PrevHandwheelPosition HwDeg M f32	1527.19995		
AbsHwPos_RelHwPos_HwDeg_M_f32	1527.19995		
AbsHwPos SrlComHwPosStatus Cnt M u16	21993		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	nCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.300000012		
k_HwPosOutputLPFError_HwDeg_f32	466.399994		
k_I2CHwAuthority_UIs_f32	0.300000012		
k_MaxSensorlessAuthority_Uls_f32	0.119999997		
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994		
k_MinSensorlessAuthority_Uls_f32 k_TurnsCntrAuthority_Uls_f32	0.300000012 0.300000012		
k_TurnsCntrAuthority_Ois_isz k_UseTurnsCntr_Cnt_lgc	1		
k_Userumschit_Ght_igc k_VehCntrOffValidLimit_HwDeg_f32	110		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target AbsHwPos Per2 ManufMode Cnt enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	371		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Diag$	( target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	/ <b>-</b> -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPosS target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	ty_oni_uoo	
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1527.19995	1527.19995 ± 0.009	Kest
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	3378.58105	3378.58105 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3378.58105	3378.58105 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos SrlComHwPos HwDeg M f32	8814.7002	8814.7002 ± 0.1	
	0	0 ± 0.004	
		0000 50007 + 0.4	
AbsHwPos_TargetHwAuthority_Uls_M_f32	9802.53027	9802.53027 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027 1388.90002	1388.90002 ± 0.009	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32		1388.90002 ± 0.009	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1388.90002 0 0	1388.90002 ± 0.009 0 0	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1388.90002 0 0 504	1388.90002 ± 0.009 0 0 504	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1388.90002 0 0 504	1388.90002 ± 0.009 0 0 504	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1388.90002 0 0 504 1	1388.90002 ± 0.009 0 0 504 1	
AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1388.90002 0 0 504 1 1	1388.90002 ± 0.009 0 0 504 1 1 0 ± 0.004	
AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1388.90002 0 0 504 1 1 0 1600	1388.90002 ± 0.009 0 504 1 1 0 ± 0.004 1600 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1388.90002 0 0 504 1 1 0 1600 65534	1388.90002 ± 0.009 0 504 1 1 0 ± 0.004 1600 ± 0.1 65534	
AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TempHwPos_HwDeg_M_f32  AbsHwPos_VehCntrOffset_HwDeg_M_f32  AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc  AbsHwPos_VehCntrValid_Cnt_M_lgc  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  larget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value  larget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1388.90002 0 0 504 1 1 0 1600	1388.90002 ± 0.009 0 504 1 1 0 ± 0.004 1600 ± 0.1	

1600

 $target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

1600 ± 0.1

2014-09-04, 15:47:58+0530





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997	150.699997 ± 0.0009	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.13 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	-7306.20996		
AbsHwPos HandwheelAuthority Uls M f32	0.529999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	139.100006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.529999971		
AbsHwPos_HwPosSource_Cnt_M_u16	9496		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	389.200012		
AbsHwPos_RelHwPos_HwDeg_M_f32	389.200012		
AbsHwPos SrlComHwPosStatus Cnt M u16	25521		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5617.33008		
AbsHwPos TargetHwAuthority Uls M f32	0.529999971		
AbsHwPos_TempHwPos_HwDeg_M_f32	1817.34998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	rOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.519999981		
k_HwPosOutputLPFError_HwDeg_f32	347.600006		
k_I2CHwAuthority_UIs_f32	0.519999981		
k_MaxSensorlessAuthority_Uls_f32	0.610000014		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.519999981		
k_TurnsCntrAuthority_UIs_f32	0		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.610000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1125.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-862.400024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6447		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (		educedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Posule
AbsHwPos AlignedHwPos HwDeg M f32		389.200012 ± 0.0009	Result
ADSI IWE US_Ally I leu I WE US_I WE US_IVI_ISZ	389.200012	309.200012 I 0.0008	

0.00999999046

HWPOS\_STATE\_SENSORVALID

-4173.37207

-4173.37207

-7997.64014

21845

0

AbsHwPos\_HandwheelAuthority\_Uls\_M\_f32

AbsHwPos\_SrlComHwPosStatus\_Cnt\_M\_u16

AbsHwPos\_SrlComHwPos\_HwDeg\_M\_f32

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

AbsHwPos\_HwPosSource\_Cnt\_M\_u16

AbsHwPos\_HwPosState\_Cnt\_M\_enum
AbsHwPos\_PrevHandwheelPosition\_HwDeg\_M\_f32

AbsHwPos\_HandwheelPositionLPF\_M\_str.SV\_Uls\_f32

0.00999999978 ± 0.004

HWPOS\_STATE\_SENSORVALID

-4173.37207 ± 0.1

-4173.37207 ± 0.1

21845 -7997.64014 ± 0.1

0 ± 0.004

AbsHwPos\_Per2



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993	691.429993 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	<b>✓</b>
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

lame	Input Value
	-1676.60999
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0.12999995
AbsHwPos_HandwheelAuthority_Uls_M_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	176
AbsHwPos_HwPosSource_Cnt_M_u16	
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_SENSORVALID -901.799988
NoshwPos_PievHandwheeiPosition_nwDeg_M_132	-901.799988
ubsHwPos SrlComHwPosStatus Cnt M u16	22641
NbsHwPos SrlComHwPos HwDeg M f32	7861.0498
AbsHwPos TargetHwAuthority Uls M f32	0.12999995
AbsHwPos TempHwPos HwDeg M f32	260.899994
NbSHwPos VehCntrOffset HwDeg M f32	724.5
AbsHwPos VehCntrOfstLearn Cnt M lqc	1
AbsHwPos VehCntrValid Cnt M Igc	1
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
: HwPosAuthorityStep Uls f32	0.119999997
_HwPosOutputLPFError_HwDeg_f32	733.700012
: I2CHwAuthority Uls f32	0.119999997
MaxSensorlessAuthority Uls f32	0.20999993
MaxVehCntrOffDiff HwDeg f32	436.700012
MinSensorlessAuthority Uls f32	0.119999997
TurnsCntrAuthority Uls f32	0.119999997
UseTurnsCntr Cnt Igc	1
VehCntrOffValidLimit HwDeg f32	377.299988
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	321,200012
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	1
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	422.5
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1487
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus Error	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosRedu	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority UI	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_Hw	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cn	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UI	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwD	-





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStar	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 3.15 (Repeat Count = 1)	<b>√</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2661.79004
AbsHwPos HandwheelAuthority Uls M f32	0.200000003
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	949.940002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003
AbsHwPos_HwPosSource_Cnt_M_u16	232
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-941.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-941.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23145
AbsHwPos_SrlComHwPos_HwDeg_M_f32	972.909973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	941.599976
k_I2CHwAuthority_Uls_f32	0.189999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976
k_MinSensorlessAuthority_Uls_f32	0.189999998
k_TurnsCntrAuthority_Uls_f32	0.189999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	585.200012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value$ 

 $target\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32$ 

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value





Name	Input Value		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	98		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2355		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-900	-900 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-900	-900 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-900	-900 ± 0.1	<b>~</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-900	-900 ± 0.1	<b>~</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024	935.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

633.900024

0.189999998

-900

21845

-900

\*none\*

21845

-900 ± 0.1

-900 ± 0.1

0.189999998 ± 0.004

633.900024 ± 0.0009

Test Step 3.16 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1958.08997
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	848.539978
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006
AbsHwPos_HwPosSource_Cnt_M_u16	192
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	126.699997
AbsHwPos_RelHwPos_HwDeg_M_f32	126.699997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22785
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.140000001
k_HwPosOutputLPFError_HwDeg_f32	793.099976
k_I2CHwAuthority_Uls_f32	0.140000001





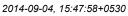
Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.230000004		
k_MaxVehCntrOffDiff_HwDeg_f32	196.100006		
k_MinSensorlessAuthority_Uls_f32	0.140000001		
k_TurnsCntrAuthority_UIs_f32	0.140000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	436.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1735		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006	301.100006 ± 0.1	<b>~</b>

talgot_i tto_inot_i tp_i tboi titi oon ini_Locatononi onoot	target_rcorrectioned		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006	301.100006 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	192	192	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024	784.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.150000006	0.150000006 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	192	192	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	269.209991	269.209991 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994	482.899994 ± 0.0009	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>~</b>		
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~		
Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_		

Test Step 3.17 (Repeat Count = 1)		V
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001	
AbsHwPos_HwPosSource_Cnt_M_u16	296	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998	

AbsHwPos\_Per2





Name	Input Value		
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCi	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_UIs_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	1		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_UIs_f32	0.270000011		
k_TurnsCntrAuthority_UIs_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.360000014		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		ReducedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_c		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDog_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	CIII_uu6	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	I=	1_
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
	504		

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32		875.5	± 0.0009	~
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReach	ed 1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCState	tus 1	~
Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	Rta Call AheHwPos Per2 CP1 ChecknointReach	ed 1	-

0.360000014

622.700012

21845

1600

504

504

21845

1600 ± 0.1

0.360000014 ± 0.004

622.700012 ± 0.1

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)$ 

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)  $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value$ 



Test Step 3.18 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	4209.93018		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.310000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos HandwheelPositionLPF M str.K Uls f32	1173.02002		
AbsHwPos_HwPosSource_Cnt_M_u16	0.310000002 320		
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_revHandwheelPosition_HwDeg_M_f32	-448.700012		
AbshwPos RelHwPos HwDeg M f32	-448.700012 -448.700012		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23937		
AbsHwPos SrlComHwPos HwDeg M f32	2521.05005		
AbsHwPos TargetHwAuthority Uls M f32	0.310000002		
AbsHwPos TempHwPos HwDeg M f32	8814.7002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVer	CntrOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos	_ <u>-</u> _	
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	1268.30005		
k_I2CHwAuthority_Uls_f32	0.20000003		
k_MaxSensorlessAuthority_Uls_f32	0.389999986		
k_MaxVehCntrOffDiff_HwDeg_f32	1060.40002		
k_MinSensorlessAuthority_Uls_f32	1		
k_TurnsCntrAuthority_Uls_f32	0.20000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	911.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-212.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.389999986		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	683		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	966.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3719		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_PersPersPersPersPersPersPersPersPersPers$	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	<del></del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_uo8	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1	I
Name Ababba Para Alfarradiba Para bas Para M. 600	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-448.700012	-448.700012 ± 0.0009	,
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.109999999	0.109999999 ± 0.004	1
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3541.94092	3541.94092 ± 0.1	•
AbshwPos_HwPosSource_Cnt_M_u16	320	320	1
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3541.94092	3541.94092 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16  AbsHwPos_SrlComHwPos_HwDos_HwPos_H #32	65534 2521.05005	65534 2521.05005 ± 0.1	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0		
AbsHwPos_TargetHwAuthority_Uls_M_f32		0 ± 0.004	
	8814.7002	8814.7002 ± 0.1	
AbsHwPos_TempHwPos_HwDeg_M_f32	8814 7002		
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002	8814.7002 ± 0.009	
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1 0	1 0	
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 0 504	1 0 504	
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 0 504 1	1 0 504 1	
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 504 1	1 0 504 1 1	
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 0 504 1	1 0 504 1	

320

320

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 3.19 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos AlignedCumMtrPos HwDeg M f32	1535.87		
AbsHwPos HandwheelAuthority Uls M f32	0.119999997		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	787.700012		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.119999997		
AbsHwPos_HwPosSource_Cnt_M_u16	68		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	1567.40002		
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997		
AbsHwPos TempHwPos HwDeg M f32	240.800003		
AbsHwPos VehCntrOffset HwDeg M f32	694.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	trOffset_GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.109999999		
k_HwPosOutputLPFError_HwDeg_f32	704		
k I2CHwAuthority Uls f32	0.109999999		
k MaxSensorlessAuthority Uls f32	0.20000003		
k MaxVehCntrOffDiff HwDeg f32	407		
k MinSensorlessAuthority Uls f32	0.109999999		
k TurnsCntrAuthority Uls f32	0.109999999		
k UseTurnsCntr Cnt lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.20000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Abal huDan Alianadi huDan LhuDan M 622	1567 40003	1567 40002 + 0.000	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20000003	0.200000003 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~





Name	Actual Value	Expected Value	Result
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2802.53003
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.209999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	970.219971
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.209999993
AbsHwPos_HwPosSource_Cnt_M_u16	240
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-921.5
AbsHwPos_RelHwPos_HwDeg_M_f32	-921.5
bsHwPos SrlComHwPosStatus Cnt M u16	23217
AbsHwPos SrlComHwPos HwDeg M f32	1113.65002
.bsHwPos TargetHwAuthority Uls M f32	0.209999993
ubsHwPos TempHwPos HwDeg M f32	7024.72998
AbsHwPos VehCntrOffset HwDeg M f32	966.099976
AbsHwPos VehCntrOfstLearn Cnt M Igc	1
hbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.200000003
HwPosOutputLPFError HwDeg f32	971.299988
I I2CHwAuthority Uls f32	0.20000003
MaxSensorlessAuthority Uls f32	0.289999992
MaxVehCntrOffDiff HwDeg f32	674.299988
MinSensorlessAuthority Uls f32	0.20000003
TurnsCntrAuthority Uls f32	0.20000003
UseTurnsCntr Cnt Igc	0
VehCntrOffValidLimit HwDeg f32	614.900024
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	900
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.289999992
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	482
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	104
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	664.099976
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2479
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorSt	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduce	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_t	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDe	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_I	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f3	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Fer2_Manuninode_Cit_enum arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f	



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-921.5	-921.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	955.473755	955.473816 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	955.473755	955.473816 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	900	900 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	900	900 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	966.099976	966.099976 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	955.473755	955.473816 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	900	900 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	664.099976	664.099976 ± 0.0009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

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Test Step 3.21 (Repeat Count = 1)	Invest Value
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_Uls_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_Uls_f32	0.159999996
k_TurnsCntrAuthority_UIs_f32	0.159999996
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	496.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0

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		•	
Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	<b>-</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

21845

65535

21845

0 ± 0.1

65535 ± 0.09

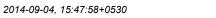
Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-1676.60999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.129999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.12999995
AbsHwPos_HwPosSource_Cnt_M_u16	176
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.12999995
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.119999997

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value \\ target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value$ 

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

AbsHwPos\_Per2





Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.209999993		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_UIs_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	·

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	<b>✓</b>
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	<b>✓</b>
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	<b>✓</b>
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	422.5	422.5 ± 0.0009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 3.23 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	3787.70996	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001	
AbsHwPos_HwPosSource_Cnt_M_u16	296	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	

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AbsHwPos\_Per2

Abath Page   Revision (Lew Page   M. 22)		
Abbit More, Si-Corrish Pro-Sistato, Crit. M. J. 16   Abbit More, Si-Corrish Pro-Sistato, Crit. M. J. 16   Abbit More, Langer Mauthon, U.S. M. 172   Abbit More, Langer Mauthon, U.S. M. 172   Abbit More, Langer Mauthon, U.S. M. 172   Abbit More, VerCinct Offset, Linkop, M. 172   Abbit More, VerCinct Offset, Delic Control Offset, Cell's rosistato (Error Status) (Error Status		
Abstance Target Handwarder, Link M. 192 Bill, Call Ap, Abstance Section M. 196 Bill, Call A		
Abatherio, Tangethevalurioni, U.B. M. 32 Abatherio, VenicintrOffiset, H. Mog. M. 32 Abatherio, VenicintrOffiset am, Coli M. 32 Abatherio, VenicintrOfficet am, Coli M. 32 Abatherio, Abatherio, PenicintrOfficet am, Coli M. 33 Abatherio, Alpharenteneethicon,		
AbstahrOps_Interfortible_Inter		
Abath-Pox_VerCintOffel em_Coll M_10c   Abath-Pox_VerCin		
Abbithory   Wendinfortial.com   Milling   O		
Abarbanops   Verbinder   Ver		
File   Data   Ap. AbsthMPos   GotErrorStatus   ErrorStatus   ErrorStat		
Ric Inst. Ap. AbsHrPos   Inst. Park AbsHrP		
K. HWP60AUDRING ISEP_UIL. J. J. 2   K. JUSCHWAURDON, UIS. J. 32   Larget, AbashWoo, Perz. Displaintui-MPG-ReducedPerf_Cnt_Igc-value   Larget, AbashWoo, Perz. J. 2004/AbashPos, HwDeg, J. 32 value   Larget, AbashWoo, Perz. J. 2004/AbashPos, HwDeg, J. 32 value   Larget, AbashWoo, Perz. J. 2004/AbashPos, HwDeg, J. 32 value   Larget, AbashWoo, Perz. J. 2004/AbashPos, HwDeg, J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. Turnschrit/Abishy, Chr. UIG. J. 32 value   Larget, Perz. J. 32 Perz. J. 32 value   Larget,		
K_12ChNAuthority_Us_52		
k, Mack-Rochinor (DII), His Dig 32         882.20012           k, MuriSensoriessAuthority, Uls, Dig 2         0.270000011           k, Turns-Cinn/Luminary, Uls, Dig 2         0.270000011           k, Vehrocht/Valid Limit, Pwbleg, Dig 1         222.2799888           k, Vehrocht/Valid Limit, Pwbleg, Dig 2         822.2799888           kanger, AbathwPos, Per2, Dig Chrwhabs-Posvalid, Cnt, Lip, value         90           kanger, AbathwPos, Per2, Dig Chrwhabs-Posvalid, Cnt, Lip, Value         92           kanger, AbathwPos, Per2, SensoriessAuthority, Uls, Si2 value         93,99000014           kanger, AbathwPos, Per2, SensoriessAuthority, Uls, Si2 value         92,270012           kanger, AbathwPos, Per2, Turns-Chrivalidity, Cnt, Ulo Svalue         92,70012           kanger, Man, Lib, Vehrocht-Christic Distance, Per2, DiagStatus-Hyb-PosreducedPer1, Status, Per2, Bern Schott, Per2, Per2, Per2, DiagStatus-Hyb-PosreducedPer1, Status, Per2,		
ManSendersexaturbiny, Uis_192   0.270000011		
Management   Ma		
K_UseTurmScnfr_Cht_Use		
K, UsePant Confugical Limit, HwDorg, 132   1822/19988   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19989   1822/19999   1822/19999   1822/19999   1822/19999   1822/19999   1822/19999   1822/199999   1822/199999   1822/1999999   1822/19999999999999999999999999999999999		
Record   AbshimPos   Perz   DiagStatushimPosReducadPerf_Cnt_ gc value   271.399994   1   1   1   1   1   1   1   1   1		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc value   0   0   1   1   1   1   1   1   1   1		
larget AbsHWPos Perz   ZCHWAbsPos Valid Cnt   govalue   271.399994   271.3999994   271.399994   271.399998   271.399994   271.399998   271.399998   271.399998   271.399998   271.399994   271.399998   271.3999998   271.3999999999999999999999999999999999999		
larget_AbsHwPos_Per2_L2CHwAbsPos_HwDeg_132 value         271.399994           larget_AbsHwPos_Per2_SensoriessAuthority_UIs_152 value         0.360000014           larget_AbsHwPos_Per2_SensoriessAuthority_UIs_152 value         622.700012           larget_AbsHwPos_Per2_SensoriessAuthority_UIs_CR2_Value         622.700012           larget_AbsHwPos_Per2_SensoriessAuthority_UIs_CR2_Value         100           larget_Pim_EOLVehCntr0ffset_EOLVehCntr0ffset_HwDeg_132         875.5           larget_Ric_Ind_Ap_AbsHwPos_Der2_TurnSchleg_GetErrorStatus_ErrorStatus         3437           larget_Ric_Ind_Ap_AbsHwPos_Der2_DiagStatusHwPosReducedPerf_Clarget_Ric_Ind_Ap_AbsHwPos_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Clarget_Ric_Ind_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_UIs_152         larget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc           larget_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_UIs_152         larget_AbsHwPos_Per2_HandwheelAuthority_UIs_152         larget_AbsHwPos_Per2_HandwheelAuthority_UIs_152         larget_AbsHwPos_Per2_HandwheelPosition_HwDeg_152         larget_AbsHwPos_Per2_HandwheelPosition_HwDeg_152         larget_AbsHwPos_Per2_HandwheelPosition_HwDeg_152         larget_AbsHwPos_Per2_MandwheelPosition_HwDeg_152         larget_AbsHwPos_Per2_HandwheelPosition_HwDeg_152         larget_AbsHwPos_Per2_MandwheelPosition_HwDeg_152         larget_AbsHwPos_Per2_MandwheelPosition_HwDeg_152         larget_AbsHwDes_Per2_Valor_UIs_Deg_152         larget_AbsHwDes_Per2_Valor_UIs_Deg_152         larget_AbsHwDes_Per2_Valor_UIs_Deg_152         larget_AbsHwDes_Per2_Valor_UIs_Deg_152		
larget AbsHWPos_Per2_ManufMode_Cnt_enum value		
target_AbsHwPos_Per2_SensorlessAuthority_Uis_f32.value   0.360000014		
larget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 xalue   622.700012		
target_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08.value   100		
target_Pim_EOLVehCnitOffset_EOLVehCnitOffset_EOLHwPosTrimPerformed_Cnt_u16   1347		
target_Pim_EOLVehCntrOffset_EOLHwPosTnimPerformed_Cnt_u16   3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Clarget_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPosValid_Cnt_Igc_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_Rte_Inst_Ap_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08_target_Rte_Inst_Ap_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08_target_Rte_Inst_Ap_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08_target_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08_target_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_AbsHwPos_Per2_SinComHwPos_HwDeg_f32_target_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08_target_AbsHwPos_Per2_SinComHwPos_HwDeg_f4_52_target_AbsHwPos_Per2_TurnsCnttValidity_Cnt_u08_target_AbsHwPos_Per2_SinComHwPos_FandwheelAuthority_Uls_Mf32_target_AbsHwPos_Per2_SinComHwPos_FandwheelAuthority_Uls_Mf32_target_AbsHwPos_Per2_LindowHwDeg_f4_52_target_AbsHwPos_Per2_LindowHwDeg_f4_52_target_AbsHwPos_Per2_LindowHwDeg_f4_52_target_AbsHwPos_Per2_LindowHwDeg_f4_52_target_AbsHwD		
target_Rte_Inst_Ap_AbsHwPos AbsHwPos Per2_DiagStatusHwPosReducedPerf_         target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32         target_Rte_Inst_Ap_AbsHwPos_Per2_HandwheelPostion_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_Per2_HwPosSource_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewhselPostion_HwDeg_f32         target_AbsHwPos_Per2_HwDosSource_Cnt_u16           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewhselPostion_HwDeg_f32         target_AbsHwPos_Per2_IndewhselPostion_HwDeg_f32         target_AbsHwPos_Per2_IndewhselPostion_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndewhselPostylid_Cnt_Igc         target_AbsHwPos_Per2_IndewhselPostylid_Cnt_Igc         target_AbsHwPos_Per2_IndewhselPostylid_Cnt_Igc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32         target_AbsHwPos_Per2_IndewhselPostylid_Cnt_IndewhselPostylid_Cnt_IndewhselPostylid_Cnt_IndewtselPostylid_Cnt_IndewthselPostylid_Cnt_IndewthselPostylid_Cnt_IndewthselPostylid_Cnt_IndewthselPostylid_Cnt_IndewthselPostylid_C		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_Per2_I2CHwAbsPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAtthority_Uls_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAtthority_Uls_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TarmsCntrValidity_Cnt_u08   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TarmsCntrValidity_Cnt_u08   target_Rte_Inst_Ap_AbsHwPos_Per2_Inst_Ap_AbsHwPos_Inst_Ap_AbsHwPos_Inst_Ap_AbsH		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HVPosSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IVEHwAbsPosValid_Cnt_lgc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IVEHwAbsPosValid_Cnt_lgc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IVEHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IVEHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_AbsHwPos_Per2_SifComHwPosHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Cnt_u16 target_AbsHwPos_Per2_SifComHwPosHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_Pim_EOLVehCntrOffset  Name  Actual Value  Actual Value  AbsHwPos_HandwheelPosition_HyDeg_M_f32  AbsHwPos_HandwheelPosition_HyDeg_M_f32  AbsHwPos_HwPosState_Cnt_M_enum  HWPOS_STATE_SENSORVALID  AbsHwPos_Fret_HandwheelPosition_HyDeg_M_f32  AbsHwPos_SifComHwPos_HwDeg_M_f32  AbsHwPos_SifComHwPos_HwDeg_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_TargetHwAuthority_Uls_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_HwDeg_M_f32  AbsHwPos_VehCntrOffste_M_Deg_M_f32  AbsHwPos_VehCntrOffste_M_Deg_M_f32  AbsHwPos_VehCnt		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_ECHwAbsPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_ECHwAbsPos_HwDeg_f32   target_Rte_Inst_Ap_AbsHwPos_Per2_SensoriessAuthority_UIs_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_UIs_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_UIs_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_UIs_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32   target_AbsHwPos_Per2_SensoriessAuthority_UIs_f32   target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_f32   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_SirComHwPos_HwDeg_M_f32   target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08   target_AbsHwPos_Per2_SirComHwPos_HwDeg_M_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_M_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_M_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_M_f32   target_AbsHwPos_Per2_SirComHwPos_HwDeg_M_f32   target_AbsHwPos_Per2_SirComHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)   target_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)   target_AbsHwPos_Per2_Scnvointed_AbsHwDes_Nx		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc         target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32         target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ManufMode_Cnt_enum         target_AbsHwPos_Per2_SensoriessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32         target_AbsHwPos_Per2_SensoriessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensoriessAuthority_Uls_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32           AbsHwPos_Per2_SensoriessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensoriessHwP		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ManufMode_Cnt_enum         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ManufMode_Cnt_enum         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensorlessHwPos_Per2_SensorlessHwPos_Per2_SensorlessHwPos_Per2_SensorlessHwPos_Per2_SensorlessHwPos_Per2_SensorlessHwPos_Per2_Sensorle		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ManufMode_Cnt_enum         target_AbsHwPos_Per2_ManufMode_Cnt_enum         target_AbsHwPos_Per2_ManufMode_Cnt_enum           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_face_right_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32           target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         1207.09998         1207.09998         1207.09998 ± 0.009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004         0.360000014 ± 0.004           AbsHwPos_HwPosSource_Cnt_M_u16         3         3         3           AbsHwPos_HwPosSource_Cnt_M_u16         3         3         3           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         622.700012         622.700012 ± 0.1         622.700012 ± 0.1           AbsHwPos_SirtComHwPos_HwDeg_M_f32         622.700012         622.700012 ± 0.2         622.700012 ± 0.2		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_AbsHwPos_Per2_SensorlessHwPos_Fe		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HvDeg_f32         target_AbsHwPos_AbsHwPos_Pos_Per2_SrlComHwPos_HvDeg_f32         target_A		
target_Rte_inst_Ap_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16         target_Rte_inst_Ap_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_inst_Ap_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_inst_Ap_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_inst_Ap_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_PhwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_PhwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         target_AbsHwPos_Per2_SrlComHwPos_HwDeg_Mcg_f32         622.700012         622.700012 ± 0.1         40.40000000000000000000000000000000000		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         1207.09998         1207.09998 ± 0.009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         622.700012         622.700012 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         3         3           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_Sroure_Cnt_M_u16         1HWPOS_STATE_SENSORVALID         HWPOS_STATE_SENSORVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         21845         21845           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1132.57996         -1132.57996 ± 0.009           AbsHwPos_VehCntrOfstLearn_Cnt_M_igc <td></td> <td></td>		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08         target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08           Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         1207.09998         1207.09998 ± 0.009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.Sv_Uls_f32         622.700012         622.700012 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         3         3           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_SENSORVALID         HWPOS_STATE_SENSORVALID           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         21845         21845           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         21845         21845           AbsHwPos_SrlComHwPos_HwDeg_M_f32         2098.83008         2098.83008 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1132.57996         -1132.57996 ± 0.009           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1132.57996         -1132.57996 ± 0.009           AbsHwPos_VehCntrOffset_Bar_Cnt_M_lgc         0         0           AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		
Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         1207.09998         1207.09998 ± 0.009           AbsHwPos_HandwheelAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32         622.700012         622.700012 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         3         3           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_SENSORVALID         HWPOS_STATE_SENSORVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         21845         21845           AbsHwPos_SrlComHwPos_HwDeg_M_f32         2098.83008         2098.83008 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_VenCnttrOffset_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_VenCnttrOffset_HwDeg_M_f32         -1132.57996         -1132.57996 ± 0.009           AbsHwPos_VenCnttrOffset_HwDeg_M_f32         1         1           AbsHwPos_VenCnttrOffset_Learn_Cnt_M_igc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         1		
Name         Actual Value         Expected Value           AbsHwPos_AlignedHwPos_HwDeg_M_f32         1207.09998         1207.09998 ± 0.009           AbsHwPos_HandwheelAuthority_UIs_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32         622.700012         622.700012 ± 0.1           AbsHwPos_HwPosSource_Cnt_M_u16         3         3           AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_SENSORVALID         HWPOS_STATE_SENSORVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         21845         21845           AbsHwPos_SrlComHwPos_HwDeg_M_f32         2098.83008         2098.83008 ± 0.1           AbsHwPos_TargetHwAuthority_UIs_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_VehCntrOffstL_HwDeg_M_f32         -1132.57996         -1132.57996 ± 0.009           AbsHwPos_VehCntrOfstLearn_cnt_M_igc         1         1           AbsHwPos_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		
AbsHwPos_AlignedHwPos_HwDeg_M_f32       1207.09998       1207.09998 ± 0.009         AbsHwPos_HandwheelAuthority_Uls_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       622.700012       622.700012 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       3       3         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       21845       21845         AbsHwPos_SrlComHwPos_HwDeg_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1	D,	Resul
AbsHwPos_HandwheelAuthority_Uls_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       622.700012       622.700012 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       3       3         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       21845       21845         AbsHwPos_TargetHwAuthority_Uls_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TempHwPos_HwDeg_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1	I Ke	esui
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32       622.700012       622.700012 ± 0.1         AbsHwPos_HwPosSource_Cnt_M_u16       3       3         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       21845       21845         AbsHwPos_TargetHwAuthority_Uls_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TempHwPos_HwDeg_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_HwPosSource_Cnt_M_u16       3       3         AbsHwPos_HwPosState_Cnt_M_enum       HWPOS_STATE_SENSORVALID       HWPOS_STATE_SENSORVALID         AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       21845       21845         AbsHwPos_SrlComHwPos_HwDeg_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TargetHwAuthority_UIs_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_HwPosState_Cnt_M_enum         HWPOS_STATE_SENSORVALID         HWPOS_STATE_SENSORVALID           AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_SrlComHwPosStatus_Cnt_M_u16         21845         21845           AbsHwPos_SrlComHwPos_HwDeg_M_f32         2098.83008         2098.83008 ± 0.1           AbsHwPos_TargetHwAuthority_Uls_M_f32         0.360000014         0.360000014 ± 0.004           AbsHwPos_TempHwPos_HwDeg_M_f32         622.700012         622.700012 ± 0.1           AbsHwPos_VehCntrOffset_HwDeg_M_f32         -1132.57996         -1132.57996 ± 0.009           AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc         1         1           AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       21845       21845         AbsHwPos_SrlComHwPos_HwDeg_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16       21845       21845         AbsHwPos_SrlComHwPos_HwDeg_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_SrlComHwPos_HwDeg_M_f32       2098.83008       2098.83008 ± 0.1         AbsHwPos_TargetHwAuthority_Uls_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_TargetHwAuthority_UIs_M_f32       0.360000014       0.360000014 ± 0.004         AbsHwPos_TempHwPos_HwDeg_M_f32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_TempHwPos_HwDeg_M_32       622.700012       622.700012 ± 0.1         AbsHwPos_VehCntrOffset_HwDeg_M_32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_VehCntrOffset_HwDeg_M_f32       -1132.57996       -1132.57996 ± 0.009         AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc       1       1         AbsHwPos_VehCntrValid_Cnt_M_lgc       0       0         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       504       504         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
AbsHwPos_VehCntrValid_Cnt_M_lgc         0         0           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         504         504           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         1         1           Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         1         1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)     504       Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)     1       Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)     1       1     1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       1       1         Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       1       1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 1 1		
TRINGE DISTRICT OS 1 ETC. HATTAWITECIMATIONIN OIS 192, VALUE 10.3000/00/14 10.3000/00/14 10.3000/00/14 10.3000/00/14		
target AbsHwPos Per2 HandwheelPosition HwDeg f32.value 622.700012 622.700012 ± 0.1		
target_AbsHwPos_Per2_HandwheelPostition_HwDeg_t32.value 622.700012 622.700012 ± 0.1 target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value 3 3		
target_AbshwPos_Per2_RriComHwPosStatus_Cnt_u16.value 21845 21845		
target_AbsHwPos_Per2_SrlComHwPos_tatus_Cnt_u16.value 21845 21845 21845 21845 1600 ± 0.1		
target_AbsnwPos_Per2_Str.CottnhwPos_nwDeg_13z.value 1600 ± 0.1 target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32 65535 ± 6.09		



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

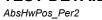
Test Step 3.24 (Repeat Count = 1) Name	Input Value		
	-2239.57007		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	0.170000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002		
	208		
AbsHwPos_HwPosStoto_Cnt_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.170000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.159999996		
k_HwPosOutputLPFError_HwDeg_f32	852.5		
k_I2CHwAuthority_Uls_f32	0.159999996		
k_MaxSensorlessAuthority_Uls_f32	0.25		
k_MaxVehCntrOffDiff_HwDeg_f32	555.5		
k_MinSensorlessAuthority_Uls_f32	0.159999996		
k_TurnsCntrAuthority_Uls_f32	0.159999996		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	496.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.25		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPo	osReducedPerf Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPosV	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cn		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs f32	target_AbsHwPos_Per2_SensorlessAuthor	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDog f32	target_AbsHwPos_Per2_SensorlessHwP		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	y_0.11_400	
		Form a sake of Markov	
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	`
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbelluBos SrlComHuBos HuDos M 533	0	0 ± 0.1	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0.25		

0.25

AbsHwPos\_TargetHwAuthority\_Uls\_M\_f32

0.25 ± 0.004

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Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Stan 2 25 (Panest Count = 4)	
Test Step 3.25 (Repeat Count = 1)	le ave
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
K HwPosAuthorityStep Uls f32	0.159999996
HwPosOutputLPFError HwDeg f32	852.5
C I2CHwAuthority Uls f32	0.159999996
<pre>C_ISST Mathematical Control Contr</pre>	0.25
MaxVehCntrOffDiff HwDeg f32	555.5
MinSensorlessAuthority Uls f32	0.159999996
< TurnsCntrAuthority Uls f32	0.159999996
CTUTISCHITAUTIONY_DIS_132  UseTurnsCntr Cnt Igc	1
Cose rumschit_Chi_gc     VehCntrOffValidLimit HwDeg f32	496.100006
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_( target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
karget_nte_mat_np_nuarriwr va.nuarriwr va_reiz_aneuninwrva_nwbeg_i32	Langer_Abon WE 09_F 612_OHOOHII WE 09_HWD6Y_132

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.26 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_UIs_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.200000003
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.409999996
k_TurnsCntrAuthority_Uls_f32	0.109999999
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	347.600006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988
$target\_Pim\_EOLVehCntrOffset.EOLHwPosTrimPerformed\_Cnt\_u16$	1363

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 





Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_DiagStatusHwPosReducedPerf\_Inst\_Ap\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst\_AbsHwPosReducedPerf\_Inst$	target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SensorlessHwPos\_HwDeg\_f32$	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
$target\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16$	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	722.072021	722.072021 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003	240.800003 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999791	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	722.072021	722.072021 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	168	168	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	<b>✓</b>

392.299988

1600

1600 ± 0.1

392.299988 ± 0.0009

T4 04-9 0 07 (D-94 094 - 4)	
Test Step 3.27 (Repeat Count = 1)	<b>V</b>
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.319999993
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.109999999
k_TurnsCntrAuthority_UIs_f32	0.109999999





Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	( target_AbsHwPos_Per2_DiagStatusHwPo	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit	ion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_h	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	prity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.009	<b>✓</b>
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	<b>✓</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos PrevHandwheelPosition HwDeg M f32	729.307983	729.307983 ± 0.1	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos SrlComHwPos HwDeg M f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos TargetHwAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos TempHwPos HwDeg M f32	301.100006	301.100006 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.20000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	<b>✓</b>
torret Abellu Dec Der CriCom I tu Dec I tu Dec 622 value	204 400006	201 100006 + 0.1	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

301.100006

392.299988

301.100006 ± 0.1

392.299988 ± 0.0009

Test Step 3.28 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1600	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1600	
AbsHwPos_RelHwPos_HwDeg_M_f32	10125	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value

 $target\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32$ 

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AbsHwPos Per2

AbsHwPos_Per2		1606	
Name	Input Value		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	1		
k_HwPosOutputLPFError_HwDeg_f32	1620		
k_I2CHwAuthority_Uls_f32	1		
k_MaxSensorlessAuthority_Uls_f32	1		
k_MaxVehCntrOffDiff_HwDeg_f32	1600		
k_MinSensorlessAuthority_Uls_f32	1		
k_TurnsCntrAuthority_Uls_f32	1		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	43690		
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf Cnt Igc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	vDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStar	tus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos AlignedHwPos HwDeg M f32	10125	10125 ± 0.09	
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	
AbsHwPos VehCntrOfstl earn Cnt M Inc	1	1	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.09	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	<b>~</b>
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	<b>✓</b>
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	<b>✓</b>
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	<b>✓</b>
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	<b>~</b>
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace		<b>✓</b>		
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	<b>✓</b>

Test Step 3.29 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	7024.72998

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AbsHwPos_Per2		RAZO	reat
Name	Input Value		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.50999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	114.300003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.50999999		
AbsHwPos_HwPosSource_Cnt_M_u16	480		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	320.100006		
AbsHwPos RelHwPos HwDeg M f32	320.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25377		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5335.8501		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.50999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87		
AbsHwPos VehCntrOffset HwDeg M f32	-409.950012		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.5		
k_HwPosOutputLPFError_HwDeg_f32	288.200012		
k_I2CHwAuthority_Uls_f32	1		
k MaxSensorlessAuthority Uls f32	0.589999974		
k_MaxVehCntrOffDiff_HwDeg_f32	80.3000031		
k MinSensorlessAuthority Uls f32	0.5		
k_TurnsCntrAuthority_Uls_f32	0.5		
k_UseTurnsCntr_Cnt_lgc	0.5		
k VehCntrOffValidLimit HwDeq f32	1505.90002		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	803.599976		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	1		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.589999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1085		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	28		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-901.799988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6199		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	<del>-</del>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosV	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	320.100006	320.100006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	465.843018	465.842987 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	2	2	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	465.843018	465.842987 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012	-409.950012 ± 0.0009	
AbsHwPos_VehCntr/OfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	0	*none*	1 1

0

0

2

21845

465.843018

803.599976

-901.799988

\*none\*

\*none\* 1 ± 0.004

21845

2

465.842987 ± 0.1

803.599976 ± 0.1

-901.799988 ± 0.0009

Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)

target\_AbsHwPos\_Per2\_HandwheelAuthority\_Uls\_f32.value target\_AbsHwPos\_Per2\_HandwheelPosition\_HwDeg\_f32.value

target\_AbsHwPos\_Per2\_SrlComHwPosStatus\_Cnt\_u16.value

target\_AbsHwPos\_Per2\_SrlComHwPos\_HwDeg\_f32.value target\_Pim\_EOLVehCntrOffset\_HwDeg\_f32

target\_AbsHwPos\_Per2\_HwPosSource\_Cnt\_u16.value

 $Rte\_Call\_Ap\_AbsHwPos\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ 



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 3.30 (Repeat Count = 1)			•
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002		
AbsHwPos_HwPosSource_Cnt_M_u16	208		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5		
AbsHwPos_ReIHwPos_HwDeg_M_f32	151.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
hbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.159999996		
_HwPosOutputLPFError_HwDeg_f32	852.5		
z_I2CHwAuthority_Uls_f32	0.159999996		
_MaxSensorlessAuthority_Uls_f32	0.25		
:_MaxVehCntrOffDiff_HwDeg_f32	555.5		
_MinSensorlessAuthority_Uls_f32	0.159999996		
_TurnsCntrAuthority_Uls_f32	0.159999996		
_UseTurnsCntr_Cnt_lgc	1		
_VehCntrOffValidLimit_HwDeg_f32	496.100006		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	
.bsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	
.bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
NbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	0	0 ± 0.1	
	21845	21845	
\bsHwPos SrlComHwPosStatus Cnt M u16	121043		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32			

65535

AbsHwPos\_VehCntrOffset\_HwDeg\_M\_f32

65535 ± 0.09

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AbsHwPos\_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988	543.299988 ± 0.0009	~

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	<b>✓</b>
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

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AbsHwPos\_Per1

 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	2	
Successful	2	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>
Time Enabled	false

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AbsHwPos\_Per1

Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 1158.00 Cycles TS1.2 1714.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $(AbsHwPos\_RelHwPos\_HwDeg\_M\_f32>=D\_RELHWPOSMAX\_HWDEG\_F32)=>True"$ 

TS1.2 Longest Execution Path:

 $(AbsHwPos\_RelHwPos\_HwDeg\_M\_f32 <= D\_RELHWPOSMAX\_HWDEG\_F32) => True"$ 

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_Ap\_AbsHwPos\_Abs$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9875	9875 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	9875	9875 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	3200	3200 ± 0.1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.01		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_Inst\_Ap\_AbsHwPosA$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	25	25 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	25	25 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	25	25 ± 0.1	•

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~		



### **Test Case 2: Boundary Test**

### Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1718.00 Cycles
TS2.2 1162.00 Cycles
TS2.3 1715.00 Cycles
TS2.4 1715.00 Cycles
TS2.5 1715.00 Cycles
TS2.6 1715.00 Cycles
TS2.7 1721.00 Cycles
TS2.8 1714.00 Cycles
TS2.9 1715.00 Cycles
TS2.10 1715.00 Cycles
TS2.11 1715.00 Cycles
TS2.11 1715.00 Cycles
TS2.12 1715.00 Cycles
TS2.13 1715.00 Cycles
TS2.14 1715.00 Cycles
TS2.14 1715.00 Cycles TS2.13 1715.00 Cycles TS2.14 1715.00 Cycles TS2.15 1715.00 Cycles TS2.16 1715.00 Cycles TS2.17 1715.00 Cycles TS2.18 1715.00 Cycles TS2.19 1715.00 Cycles TS2.20 1715.00 Cycles

### Description

### Vector Description:

TS2.1 All Min

TS2.1 All Min
TS2.2 All Max
TS2.3 AlignedCumMechMtrPosCRF\_Deg\_f32==>Min
TS2.4 AlignedCumMechMtrPosCRF\_Deg\_f32==>Max
TS2.5 AlignedCumMechMtrPosCRF\_Deg\_f32==>Pos
TS2.6 AlignedCumMechMtrPosCRF\_Deg\_f32==>Zero
TS2.7 AlignedCumMechMtrPosCRF\_Deg\_f32==>Neg
TS2.8 ComplError\_HwDeg\_f32==>Min
TS2.9 ComplError\_HwDeg\_f32==>Min
TS2.10 ComplError\_HwDeg\_f32==>Pos
TS2.11 ComplError\_HwDeg\_f32==>Neg
TS2.12 ComplError\_HwDeg\_f32==>Neg
TS2.13 CumMechMtrPosCRF\_Deg\_f32==>Min
TS2.14 CumMechMtrPosCRF\_Deg\_f32==>Max
TS2.15 CumMechMtrPosCRF\_Deg\_f32==>Pos
TS2.16 CumMechMtrPosCRF\_Deg\_f32==>Pos
TS2.17 CumMechMtrPosCRF\_Deg\_f32==>Neg
TS2.18 k\_GearRatio\_Uls\_f32==>Min
TS2.19 k\_GearRatio\_Uls\_f32==>Max
TS2.20 k\_GearRatio\_Uls\_f32==>Pos

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.01		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_t tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_t	732	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	25	25 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	25	25 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	25	25 ± 0.1	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	<b>✓</b>		





Test Step 2.2 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	9875	9875 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	9875	9875 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	3200	3200 ± 0.1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 2.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.02		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	88.6		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	647.2		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_Inst\_Ap\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_InstAp\_AbsHwPos\_A$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-288.600006	-288.6 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-75.6559982	-75.656 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-75.6559982	-75.656 ± 0.1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•	

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.03		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	101.2		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-361		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Degrade and the property of t$	_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrF	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	198.800003	198.8 ± 0.0009	•
AbsHwPos_RelHwPos_HwDeg_M_f32	-112.029999	-112.03 ± 0.0009	-
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	-112.029999	-112.03 ± 0.1	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.5 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.04		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	543.87		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-54.8		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	912		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	76.5547943	76.5548 ± 00.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	91.2799988	91.28 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	91.2799988	91.28 ± 0.1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	0.05		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	0		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	31.7		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	6123.6		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_inst_AlignedCumMechMtrDosCRF\_Deg\_inst_AlignedCumMechMtrDosCRF\_D$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-31.7000008	-31.7 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	274.480011	274.48 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	274.480011	274.48 ± 0.1	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-879.6		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	89		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-4456		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-968.599976	-968.6 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-4545	-4545 ± 00.009	~
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	-3200	-3200 ± 0.1	-

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.07		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	7645.12		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	1023.54		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_fraction{                                    $	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	660.158386	660.1584 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	196.647797	196.6478 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	196.647797	196.6478 ± 0.1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.08		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	8123.4		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4789.6		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrl	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	524.871948	524.872 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	258.167999	258.168 ± 0.0009	<b>✓</b>
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	258.167999	258.168 ± 0.1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.10 (Repeat Count = 1)			·
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.09		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-8601.68		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	66.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	8555.66		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Inst\_Ap\_AbsHwPosAbsHw$	Deg_1 tgt_AbsHwPos_Per1_AlignedCun	nMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError	_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	2 tgt_AbsHwPos_Per1_CumMechN	/trPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_	HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-840.651184	-840.6512 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	703.50946	703.5094 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	703.50946	703.5094 ± 0.1	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~	



Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	9079.96		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	0		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	648.6		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_transformed and the property of the prope$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	907.996033	907.996 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	64.8600006	64.86 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	64.8600006	64.86 ± 0.1	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>		

Test Step 2.12 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.11		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	647.2		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-75.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	987.1		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrF	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	146.692001	146.692 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	184.080994	184.081 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	184.080994	184.081 ± 0.1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•	

Test Step 2.13 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.12		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-361		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	41.9		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_I$	Deg_1 tgt_AbsHwPos_Per1_AlignedCu	mMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplErro	r_HwDeg_f32	
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_CumMechMtrPosCRF\_Deg\_f3222222222222222222222222222222222222$	tgt_AbsHwPos_Per1_CumMechl	MtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos	_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-85.2200012	-85.22 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-1241.90002	-1241.9 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-1241.90002	-1241.9 ± 0.1	<b>✓</b>

Test Step Call Trace						
Actual Function Count Expected Function Count				Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>		



Test Step 2.14 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.13		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	912		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	69.4		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_transformed and the property of the prope$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	49.159996	49.16 ± 0.00009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	1230.59998	1230.6 ± 0.009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	1230.59998	1230.6 ± 0.1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~	

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.14		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	545.3		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-50.8		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	743.87		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_transformed and the property of the prope$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	127.141998	127.142 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	154.941803	154.9418 ± 0.0009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	154.941803	154.9418 ± 0.1	~

Test Step Call Trace					
Actual Function Count Expected Function			Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>~</b>	

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.15		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-3479		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	99		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_De	t tgt_AbsHwPos_Per1_AlignedCumMechMtrf	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-620.850037	-620.85 ± 0.0009	-
AbsHwPos_RelHwPos_HwDeg_M_f32	-99	-99 ± 0.00009	-
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	-99	-99 ± 0.1	-

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>



Test Step 2.17 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.16		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	7258		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-105.6		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-849.6		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_fraction{                                    $	eg_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1266.88	1266.88 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-30.3359909	-30.336 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-30.3359909	-30.336 ± 0.1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.18 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.01		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-6123.6		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	120.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4426		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	Deg_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-181.735992	-181.736 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-76.2400055	-76.24 ± 0.00009	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-76.2400055	-76.24 ± 0.1	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-2456		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	96.6		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	589		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Derick and the property of th$	g_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2552.6001	-2552.6 ± 0.009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	492.399994	492.4 ± 0.0009	-
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	492.399994	492.4 ± 0.1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	<b>✓</b>

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AbsHwPos\_Per1

Test Step 2.20 (Repeat Count = 1)			<b>~</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	0.85		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	1023.54		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-44.8		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-456.5		
$tgt\_Rte\_Inst\_Ap\_AbsHwPos.AbsHwPos\_Per1\_AlignedCumMechMtrPosCRF\_Deg\_transformed and the property of the prope$	_Deg_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	914.809021	914.809 ± 0.0009	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-343.225037	-343.225 ± 0.0009	<b>~</b>
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-343.225037	-343.225 ± 0.1	~

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

 Test Object
 AbsHwPos\_Init1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl</pre>
Time Enabled	false

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Razorcat

Attributes	
Name	Value
Time Unit	Cycles
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



### **Test Case 1: Metrics Test**

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 1113.00 Cycles TS1.2 1347.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

 $If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False \\ If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffs$ 

TS1.2 Longest Execution Path:

 $If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) >> True \\ If (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLvehCntrOffset.EOLvehCntrOffs$ 

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	ce_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	_Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPo	osStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.992837846	0.992837853 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cn	t_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_	enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosSta	tus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.961661458	0.961661459 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039	1039 ± 0.009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

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AbsHwPos\_Init1

Name	Actual Value	Expected Value	Result
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

# 

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_C	nt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPos	Status_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0	0 ± 0.015625	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	<b>✓</b>
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tot AbsHwPos Init1 SrlComHwPosStatus Cnt u16.value	65534	65534	<b>✓</b>

 $\ensuremath{\text{@}}$  Report created by TESSY V3.1.7, report template V2.1

Test Step 2.1 (Repeat Count = 1)

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Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	

Test Step 2.2 (Repeat Count = 1)			✓		
Name	Input Value				
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos				
k_HwPosOutputLPFCoeffFc_Hz_f32	500				
k_UseTurnsCntr_Cnt_lgc	1				
k_VehCntrOffValidLimit_HwDeg_f32	1600				
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2				
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535				
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690				
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset				
Name	Actual Value	Expected Value	Result		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.998132586	0.998132557 ± 0.015625	~		
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~		
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•		
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•		

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>~</b>	

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_0	Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPos	sStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.992837846	0.992837853 ± 0.015625	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	



Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	25.18166721		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	436.9878821		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enur	n	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.271262884	0.271262842 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	-
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.01001		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	ce_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode	_Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwP	osStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.961661458	0.961661459 ± 0.015625	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.01001	1039.01001 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt AbsHwPos Init1 HwPosSource Cnt u16.value	65534	65534	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	

65534

65534

Test Step 2.6 (Repeat Count = 1)	
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	379.2717962

tgt\_AbsHwPos\_Init1\_SrlComHwPosStatus\_Cnt\_u16.value



Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1201.851037		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1519		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum	1	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.991486251	0.991486226 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0	0 ± 0.000009	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>	

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	401.1355943		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	436.9807251		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700.02		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1984		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_0	Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPos	sStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.993531525	0.993531549 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	_

Test Step 2.8 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	162.9279281	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	0	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	879.9430598	
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2449	

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Name	Input Value			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	n		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.870931625	0.870931593 ± 0.015625	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	487.3372427		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1555.129529		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2914		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enu	m	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.997810423	0.997810453 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1555.12952	-1555.129529 ± 0.009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~





Test Step 2.10 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	402.9378512		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1434.02		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	835.0762143		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3379		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_C	nt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPos	Status_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.993676424	0.993676398 ± 0.015625	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>→</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>→</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>→</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	835.076233	835.0762143 ± 0.0009	<b>→</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>~</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>→</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>→</b>
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	•
color - color			
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>-</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	<b>V</b>

Test Step 2.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	49.55192		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1007.182555		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3844		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	e_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_0	Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPo	sStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0	0 ± 0.015625	<b>✓</b>
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.12 (Repeat Count = 1)	<b>→</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	500
k_UseTurnsCntr_Cnt_lgc	1



Name	Input Value		
k_VehCntrOffValidLimit_HwDeg_f32	516.6709843		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	669.6303178		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4309		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	า	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.998132586	0.998132557 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	<b>✓</b>

Test Step 2.13 (Repeat Count = 1)			✓	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	111.03			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	1264.996348			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1454.825825			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4774			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	ce_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode	_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwP	osStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.752227485	0.752134034 ± 0.015625	<b>✓</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.14 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_HwPosOutputLPFCoeffFc_Hz_f32	433.7
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	678
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	564.5
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5239
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16



Name	Input Value			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.995703876	0.995703845 ± 0.015625	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	564.5	564.5 ± 0.0009	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.15 (Repeat Count = 1)			✓	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_HwPosOutputLPFCoeffFc_Hz_f32	78			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	984			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-255			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5704			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.624755323	0.624755291 ± 0.015625	<b>✓</b>	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	<b>✓</b>	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-255	-255 ± 0.0009	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	<b>✓</b>	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	<b>✓</b>	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	<b>✓</b>	
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	64		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	127.6		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	987		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6169		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt	_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_e	num	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosState	us_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.552575648	0.552575611 ± 0.015625	•

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AbsHwPos\_Init1

Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

### Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 1113.00 Cycles TS3.2 1347.00 Cycles

Description Vector Description:

 $TS3.1 \quad (Abs\_m(Rte\_Pim\_EOLVehCntrOffset\_EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) = False \\ TS3.2 \quad (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLVehCntrOffset\_HwDeg\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) = True \\ TS3.2 \quad (Abs\_m(Rte\_Pim\_EOLVehCntrOffset.EOLVehC$ 

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enur	m	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.992837846	0.992837853 ± 0.015625	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	<b>✓</b>
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	

Test Step 3.2 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039	

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Name	Input Value			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode	tgt AbsHwPos Init1 ManufMode Cnt enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwP	PosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.961661458	0.961661459 ± 0.015625	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	<b>✓</b>	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039	1039 ± 0.009	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~	
tgt AbsHwPos Init1 SrlComHwPosStatus Cnt u16.value	65534	65534	<b>✓</b>	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

AbsHwPos\_SCom\_NxtSetTrim

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 Project
 AbsHwPos\_Tcl2cVd

 Module
 AbsHwPos\_Tcl2lVd

Test Object AbsHwPos\_SCom\_NxtSetTrim

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd
Configuration File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tcl2cVd\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\src\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester.Priti Mangalekar Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version:9 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:10 Data Dictionary Version:9 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):2954 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements: Test Date:9/4/2014 Comments: NOTE:Inline functions defined in ""Globalmacro.h"" are not unit tested.  NOTE 2: ""CBD_Sandbox_dbg.map"" file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Library	libc.a
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Time Enabled	false

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AbsHwPos\_SCom\_NxtSetTrim

Attributes					
Name	Value				
Time Unit	Cycles				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\CBD_AbsHwPos_Tci2cVd\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				

AbsHwPos\_SCom\_NxtSetTrim

### Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 937.00 Cycles TS1.2 1724.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

if (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08)=>False"

TS1.2 Longest Execution Path:

 $\label{local_cont_u08} \begin{tabular}{ll} if (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => True \\ if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32) <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => True" \\ \end{tabular}$ 

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	·
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	500		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1268.56		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPo	S	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data		
k_VehCntrOffValidLimit_HwDeg_f32	1589.108169	1589.108169	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	500	500 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	500	500 ± 0.0009	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•	



## AbsHwPos\_SCom\_NxtSetTrim

### **Test Case 2: Boundary Test**

### Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

CPU Cycles:

TS'2.1 937.00 Cycles
TS2.2 936.00 Cycles
TS2.3 1511.00 Cycles
TS2.4 1512.00 Cycles
TS2.4 1512.00 Cycles
TS2.5 1725.00 Cycles
TS2.6 1511.00 Cycles
TS2.7 1511.00 Cycles
TS2.8 936.00 Cycles
TS2.9 936.00 Cycles
TS2.10 934.00 Cycles
TS2.11 1511.00 Cycles
TS2.12 1512.00 Cycles
TS2.13 1512.00 Cycles
TS2.14 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.16 1512.00 Cycles
TS2.17 1725.00 Cycles
TS2.18 1511.00 Cycles

### Description

Vector Description:

TS2.1 All Min TS2.2 All Max

TS2.3 Offset\_HwDeg\_T\_f32=>Min

TS2.3 Offset\_HwDeg\_T\_f32=>Min
TS2.4 Offset\_HwDeg\_T\_f32=>Max
TS2.5 Offset\_HwDeg\_T\_f32=>Zero
TS2.6 Offset\_HwDeg\_T\_f32=>Pos
TS2.7 Offset\_HwDeg\_T\_f32=>Pos
TS2.7 Offset\_HwDeg\_T\_f32=>Neg
TS2.8 TurnsCounterValidity\_Cnt\_u08=>Min
TS2.9 TurnsCounterValidity\_Cnt\_u08=>Max
TS2.10 TurnsCounterValidity\_Cnt\_u08=>Men
TS2.11 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Min
TS2.12 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Min
TS2.13 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Zero
TS2.14 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Zero
TS2.15 AbsHwPos\_AlignedCumMtrPos\_HwDeg\_M\_f32=>Pos
TS2.16 k\_VehCntrOffValidLimit\_HwDeg\_f32=>Max
TS2.18 k\_VehCntrOffValidLimit\_HwDeg\_f32=>Pos

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.2 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_	TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result

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AbsHwPos\_SCom\_NxtSetTrim

Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	

Test Step 2.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2171.085737		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.49		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	8	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1541.247884		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.23		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.48999	1039.49 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.22998	1039.23 ± 0.009	<b>✓</b>

Test Step Call Trace				<b>~</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	2617.103771		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	700.85		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	8	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1493.816735		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	700.849976	700.85 ± 0.0009	<b>~</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0	0 ± 0.000009	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	~

Test Step 2.5 (Repeat Count = 1)		V
Name	Input Value	
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	500	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1268.56	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	
Offset_HwDeg_T_f32	0	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1589.108169	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700	

AbsHwPos\_SCom\_NxtSetTrim

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Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	0	0	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	500	500 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	500	500 ± 0.0009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 2.6 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	600		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-887		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	277082.5472		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1344.177408		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	100		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-887	-887 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	100	100 ± 0.0009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	~

Test Step 2.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1639.564105		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-460.23		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-123.209999		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	655.3265634		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	200		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-460.230011	-460.23 ± 0.0009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	200	200 ± 0.0009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.8 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-648.31
AbsHwPos_VehCntrOffset_HwDeg_M_f32	171.1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Offset_HwDeg_T_f32	1296856.832

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AbsHwPos\_SCom\_NxtSetTrim

Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_T	urnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1289.24598		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	300.01001		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	171.100006	171.1 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	300.01001	300.01001 ± 0.0009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	988.5		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-461.79		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	332748.3392		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1584.074584		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	400		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-461.790009	-461.79 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	400	400 ± 0.0009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1024		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-946		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	60205312		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	;	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	413.7657129		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	500		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	56		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	-
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-946	-946 ± 0.0009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	500	500 ± 0.0009	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	~

Test Step 2.11 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125

AbsHwPos\_SCom\_NxtSetTrim

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Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.49		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	3267.28		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Turns	sCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1541.247884		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.48999	1039.49 ± 0.009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039	1039 ± 0.009	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	

Test Step 2.12 (Repeat Count = 1)			✓.
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	416.5		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	547		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	•	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPe	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1112.066602		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	700.02002		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	416.5	416.5 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	700.02002	700.02002 ± 0.0009	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 2.13 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1153.65		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	305.294		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	27.19890016		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	800		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1153.65002	-1153.65 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	800	800 ± 0.0009	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	_	

AbsHwPos\_SCom\_NxtSetTrim



Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1245.01		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1404.81		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	194.788		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos	_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	698		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-100		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1404.81006	-1404.81 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-100	-100 ± 0.0009	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	

Test Step 2.15 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-3264.02		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	430.045		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	177.82		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	8	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1347.2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-200		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	430.045013	430.045 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-200	-200 ± 0.0009	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	

Test Step 2.16 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2590.633843		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1084.22		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	542.1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	3	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-300.019989		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1084.21997	1084.22 ± 0.009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-300.019989	-300.019989 ± 0.0009	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•		



AbsHwPos	SCom	NxtSetTrim

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	104.2976486		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-564		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	964.2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	•	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwP	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	0	0	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-859.902344	-859.9023514 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	<b>✓</b>
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-859.902344	-859.9023514 ± 0.0009	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	1481.360235		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	874		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Offset_HwDeg_T_f32	356		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPd	os_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1000.12		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	736		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	874	874 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	736	736 ± 0.0009	~

Test Step Call Trace					V
	Actual Function	Count	Expected Function	Count	Result
	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

### Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 937.00 Cycles TS3.2 1724.00 Cycles TS3.3 1513.00 Cycles

#### Description Vector Description:

 $TS3.1 \quad \text{if (TurnsCntrValidity\_Cnt\_T\_u08} == D\_TCVVALID\_CNT\_U08) => False \\ TS3.2 \quad \text{"if (TurnsCntrValidity\_Cnt\_T\_u08} == D\_TCVVALID\_CNT\_U08) => True \\ \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_T\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg\_f32)} <= k\_VehCntrOffValidLimit\_HwDeg\_f32) => False } \\ TS3.3 \quad \text{if (Abs\_f32\_m(TmpOffset\_HwDeg$ 

Test Step 3.1 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-10125
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600

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AbsHwPos_SCom_NxtSetTrim			Razorcat
Name	Input Value		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	s_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	34	34	<b>✓</b>
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tat Pim EOLVehCntrOffset.EOLVehCntrOffset HwDea f32	-1600	-1600 ± 0.009	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	500		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1268.56		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos	_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1589.108169		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	500	500 ± 0.0009	<b>✓</b>
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	500	500 ± 0.0009	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte Call Ap AbsHwPos EOLVehCntrOffset WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	<b>✓</b>	

Name	Input Value		
AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32	-2171.085737		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.49		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Offset_HwDeg_T_f32	-4294967295		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPo	s_TurnsCntrValidity_Cnt_u08_data	
k_VehCntrOffValidLimit_HwDeg_f32	1541.247884		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.23		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_NxtSetTrim()	49	49	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.48999	1039.49 ± 0.009	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	1039.22998	1039.23 ± 0.009	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	Rte Read Ap AbsHwPos TurnsCntrValidity Cnt u08	1	_	